

# TO-220-3L-C Plastic-Encapsulate MOSFETS

## CJP85N80

N-Channel Power MOSFET

<b>V<sub>(BR)DSS</sub></b>	<b>R<sub>DS(on)MAX</sub></b>	<b>I<sub>D</sub></b>
85V	8.5mΩ@10V	80A

### DESCRIPTION

The CJP85N80 uses advanced trench technology and design to provide excellent R<sub>DS(on)</sub> with low gate charge. Good stability and uniformity with high E<sub>AS</sub>. This device is suitable for use in PWM, load switching and general purpose applications.

### FEATURE

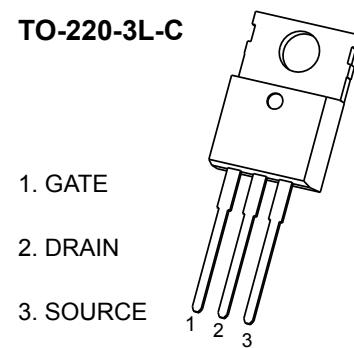
- Advanced trench process technology
- Special designed for convertors and power controls
- High density cell design for ultra low R<sub>DS(on)</sub>
- Fully characterized avalanche voltage and current
- Fast switching
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

### MARKING



CJP85N80= Device code  
 Solid dot = Green molding compound device,  
 if none, the normal device  
 YY = Code

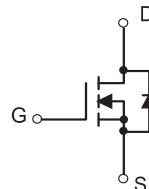
**TO-220-3L-C**



### APPLICATION

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

### EQUIV ALENT CIRCUIT



### Maximum ratings (T<sub>a</sub>=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source voltage	V <sub>DS</sub>	85	V
Gate-Source Voltage	V <sub>GS</sub>	±20	
Continuous Drain Current	I <sub>D</sub>	80	A
Pulsed Drain Current (note 1)	I <sub>DM</sub>	320	
Power Dissipation (note 2 , T <sub>a</sub> =25°C)	P <sub>D</sub>	2	W
Maximum Power Dissipation (note 3 , T <sub>c</sub> =25°C)		170	W
Single Pulsed Avalanche Energy (note 4)	E <sub>AS</sub>	620	mJ
Thermal Resistance from Junction to Ambient	R <sub>θJA</sub>	62.5	°C/W
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 ~+150	

Notes 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. This test is performed with no heat sink at T<sub>a</sub>=25°C.
3. This test is performed with infinite heat sink at T<sub>c</sub>=25°C.
4. E<sub>AS</sub> condition: T<sub>j</sub>=25°C, V<sub>DD</sub>=40V, V<sub>GS</sub>=10V, L=0.5mH, R<sub>g</sub>=25Ω.

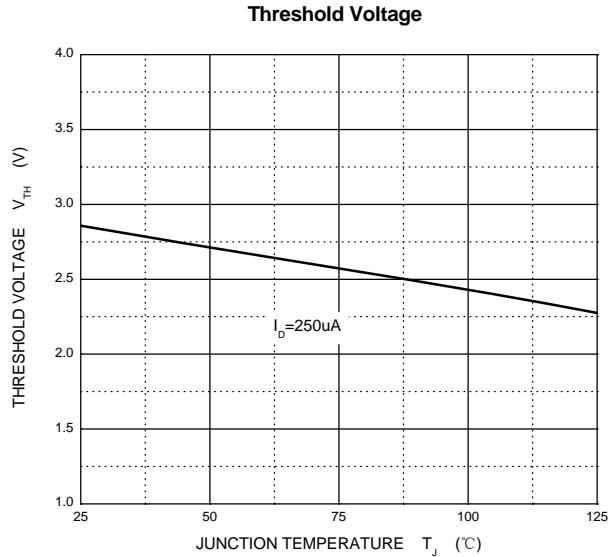
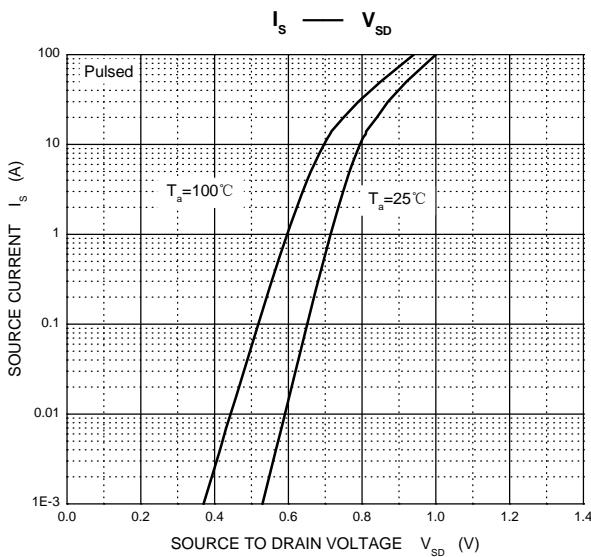
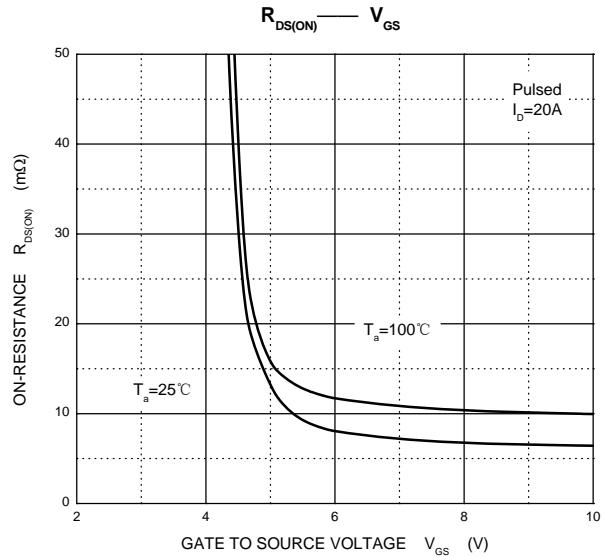
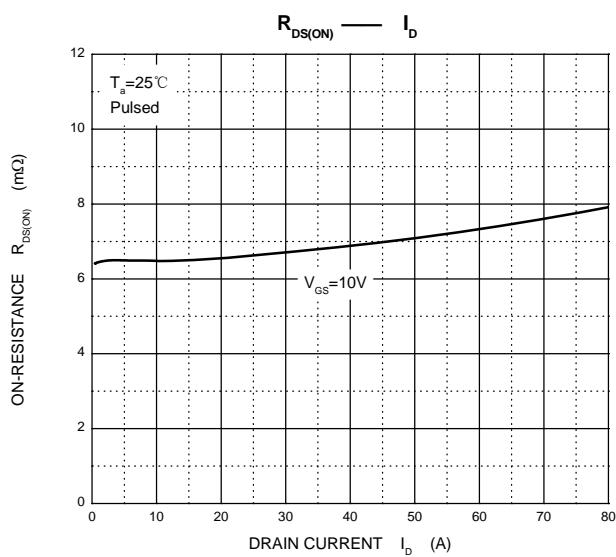
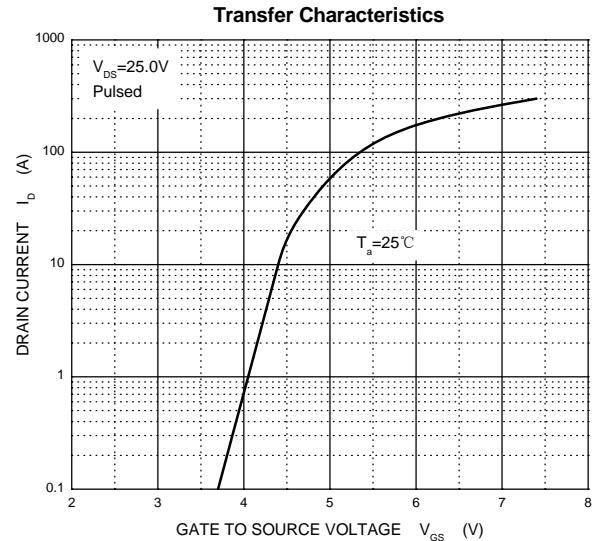
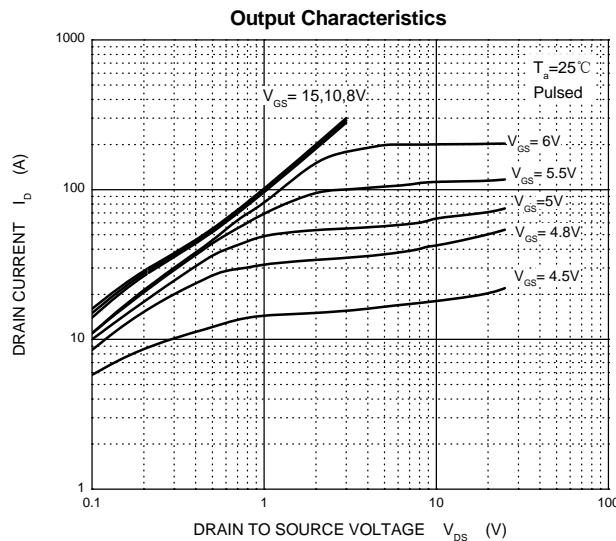
### Electrical characteristics ( $T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static characteristics</b>						
Drain-source breakdown voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0, I_{\text{D}} = 250\mu\text{A}$	85			V
Gate-threshold voltage (note 1)	$V_{\text{GS(th)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	2.0	3.0	4.0	
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}} = 85\text{V}, V_{\text{GS}} = 0$			1	$\mu\text{A}$
Gate-body leakage current	$I_{\text{GSS}}$	$V_{\text{DS}} = 0, V_{\text{GS}} = \pm 20\text{V}$			$\pm 100$	nA
Drain-source on-state resistance (note 1)	$R_{\text{DS(on)}}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 40\text{A}$		6.8	8.5	$\text{m}\Omega$
Forward transconductance (note 1)	$g_{\text{fs}}$	$V_{\text{DS}} = 10\text{V}, I_{\text{D}} = 40\text{A}$		60		S
<b>Dynamic characteristics</b> (note 2)						
Input capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0, f = 1\text{MHz}$		4400		pF
Output capacitance	$C_{\text{oss}}$			340		
Reverse transfer capacitance	$C_{\text{rss}}$			260		
<b>Switching characteristics</b> (note 2)						
Turn-on delay time	$t_{\text{d(on)}}$	$V_{\text{DD}} = 30\text{V}, I_{\text{D}} = 2\text{A}, R_{\text{L}} = 15\Omega, V_{\text{GS}} = 10\text{V}, R_{\text{G}} = 2.5\Omega$		18		ns
Rise time	$t_{\text{r}}$			12		
Turn-off delay time	$t_{\text{d(off)}}$			56		
Fall Time	$t_{\text{f}}$			15		
Total gate charge	$Q_{\text{g}}$	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 30\text{A}$		100		nC
Gate-source charge	$Q_{\text{gs}}$			20		
Gate-drain charge	$Q_{\text{gd}}$			30		
<b>Source-Drain Diode characteristics</b>						
Diode forward current	$I_{\text{s}}$				80	A
Diode pulsed forward current	$I_{\text{SM}}$				320	A
Diode Forward voltage (note 1)	$V_{\text{SD}}$	$V_{\text{GS}} = 0, I_{\text{s}} = 40\text{A}$			1.2	V
Diode reverse recovery time (note 2)	$t_{\text{rr}}$	$I_{\text{F}} = 75\text{A}, di/dt = 100\text{A}/\mu\text{s}$			36	ns
Diode reverse recovery charge (note 2)	$Q_{\text{rr}}$				56	nC

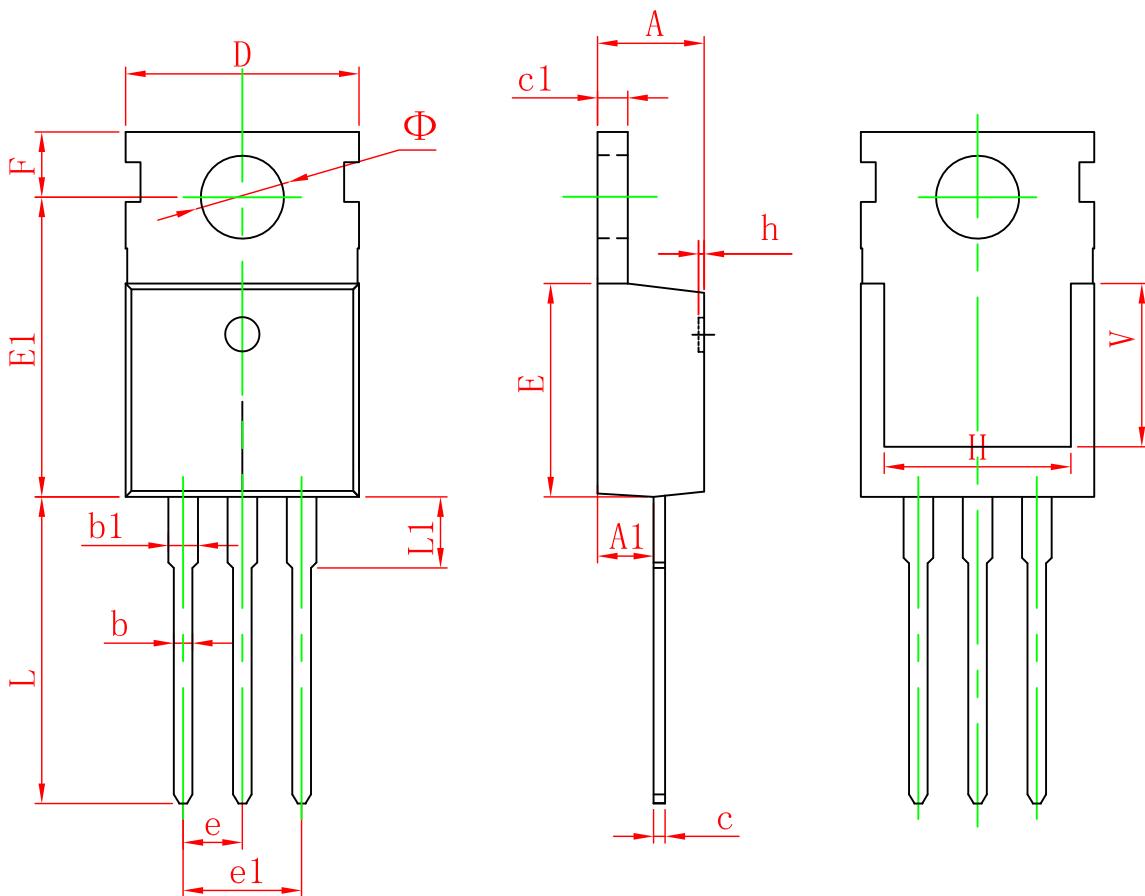
Notes: 1. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

2. These parameters have no way to verify.

## Typical Characteristics



## TO-220-3L-C Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.950	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
Φ	3.400	3.800	0.134	0.150