

HT42B564-x USB (HID) to UART Bridge IC

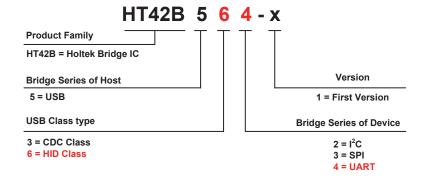
Features

- Operating Voltage (V_{DD}): 3.3V~5.5V
- UART pin Voltage (V_{DDIO}): 1.8V \sim V $_{DD}$ (Less than V_{DD} voltage)
- Power down and wake-up functions to reduce power consumption
- Fully integrated 12MHz oscillator with 0.25% accuracy for all USB modes which requires no external components
- · USB interface
 - USB 2.0 Full Speed compatible
 - Implements USB Human Interface Device (HID) class
 - + Internal $1.5k\Omega$ pull-high resistor on D+ pin
- Fully-duplex Universal Asynchronous Receiver and Transmitter Interface – UART
 - Supports Baud Rate up to 115200bps
 - Supports maximum 32 bytes transmit buffer and 32 bytes receive buffer
 - UART Data formats supported:
 - Data bits: 8
 - Stop bits: 1 and 2
 - Parity: odd, even, no parity
 - Supports RX pin resume signal to request a remote wake-up
 - Supports VDDIO pin for UART pins power supply
- Supports standard Windows[®] drivers for Windows XP, Vista, Win 7, Win 8/8.1, Win10
- Package types: 10-pin SOP

General Description

The HT42B564-x device is a high performance USB to UART bridge controller with fully integrated USB and UART interface functions, designed for applications that communicate with various types of UART. The device includes a USB 2.0 full speed compatible interface which is used for PC communication. The device also includes a fully integrated high speed oscillator which is used as clock source for the USB and UART baud rate generator. The baud rate generator can support up to 115200bps of baud rate for the UART interface.

USB Bridge IC Naming Rules



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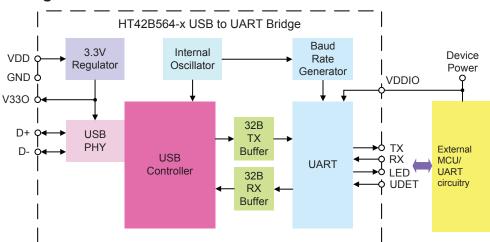


Selection Table

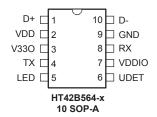
Most features are common to all devices. The following table summarises the main features of each device.

| Part No. | Description | VDD | USB | Virtual COM | HID | FIFO/Buffer | Interface Data Rate | I/O VDD | Package |
|------------|-----------------------------------|-------|------------|----------------|----------|--------------------------------|-------------------------|------------|-----------------------------|
| HT42B532-x | USB to I ² C Bridge | | | √ | _ | TX: 62 bytes RX: 62 bytes | Up to 400kHz | √ | 8SOP 10MSOP |
| HT42B533-x | USB to SPI Bridge | 3.3V~ | | √ | _ | TX: 128 bytes RX: 128 bytes | Up to 8MHz | √ | 10MSOP 16NSOP |
| HT42B534-x | USB to UART Bridge | 5.5V | Full-Speed | V | _ | TX: 128 bytes RX: 128 bytes | Up to 3Mbps Baud | V | 8/10SOP 10MSOP 16NSOP |
| HT42B564-x | USB (HID) to UART Bridge | | | _ | V | TX: 32 bytes RX: 32 bytes | Up to 115.2kbps Baud | V | 10SOP |

Block Diagram



Pin Assignment



| Package Type | Marking |
|--------------|------------|
| 10SOP | HT42B564-x |

Note: x=1 for version number.

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Pin Description

| Pin Name | Type | Description | |
|----------|------|--|--|
| D+ | I/O | USB D+ Line | |
| D- | I/O | USB D- Line | |
| TX | 0 | Asynchronous data output – UART Transmit | |
| RX | I | Asynchronous data input – UART Receive | |
| LED | 0 | TX/RX signal LED indication, active low | |
| UDET | I | USB plug-in/out detect pin | |
| V33O | 0 | 3.3V regulator output | |
| VDDIO | PWR | Positive power supply for TX/RX pins | |
| VDD | PWR | Positive power supply, USB bus power | |
| GND | PWR | Negative power supply, ground | |

Absolute Maximum Ratings

| Supply Voltage | V_{SS} -0.3V to V_{SS} +6.0V | I _{OH} Total | 80mA |
|-----------------------|----------------------------------|-------------------------|-------|
| Input Voltage | V_{SS} -0.3V to V_{DD} +0.3V | I _{OL} Total | 80mA |
| Storage Temperature | 55°C to 150°C | Total Power Dissipation | 500mW |
| Operating Temperature | -40°C to 85°C | | |

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to these devices. Functional operation of these devices at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect devices reliability.

D.C Characteristics

Ta=25°C

| Cymphal | I Parameter | | Test Conditions | Min. | T | | Unit |
|-------------------|--|-----------------|--|----------------------|------|----------------------|------|
| Symbol | Parameter | V _{DD} | Conditions | Wiin. | Тур. | Max. | Unit |
| V _{DD} | Operating Voltage | _ | _ | 3.3 | _ | 5.5 | V |
| V _{DDIO} | UART Pins VDDIO input voltage | _ | _ | 1.8 | _ | V _{DD} | V |
| I _{DD} | Operating Current | 5V | No load, USB Plug-in | _ | 11 | 16 | mA |
| I _{sus} | Suspend Current – USB | 5V | Suspend mode, No load, USB on, other peripherals off | _ | 360 | 450 | μΑ |
| I _{STB} | Standby Current – Non-USB | | Standby mode, No load, USB Plug-out, other peripherals off, VDD power from VDDIO | _ | 0.1 | 1.0 | μA |
| V _{IL} | Input Pin Input Low Voltage | Itage — — | | 0 | _ | 0.2V _{DDIO} | V |
| V _{IH} | Input Pin Input High Voltage | _ | _ | 0.8V _{DDIO} | _ | V _{DDIO} | V |
| | I/O Die Ciale Command | 3V | V = 0.4V | 4 | 8 | _ | mA |
| I _{OL} | I/O Pin Sink Current | | $V_{OL} = 0.1 V_{DDIO}$ | 10 | 20 | _ | mA |
| | I/O Die Course Current | 3V | V = 0.0V | -2 | -4 | _ | mA |
| I _{OH} | I/O Pin Source Current | 5V | $V_{OH} = 0.9V_{DDIO}$ | -5 | -10 | _ | mA |
| | VO Die Dull bieb Desistere | 3V | _ | 20 | 60 | 100 | kΩ |
| R _{PH} | I/O Pin Pull-high Resistance | 5V | _ | 10 | 30 | 50 | kΩ |
| | Innut Lookaga Current | 3V | \\ -\\ or\\ -\\ | _ | _ | ±1 | μA |
| I _{LEAK} | Input Leakage Current | | $V_{IN} = V_{DD}$ or $V_{IN} = V_{SS}$ | _ | _ | ±1 | μA |
| V _{V33O} | 3.3V Regulator Output Voltage | 5V | 5V I _{V33O} = 70mA | | 3.3 | 3.6 | V |
| R _{UDP1} | Pull-high Resistance between D+ and V33O | 3.3V | _ | -5% | 1.5 | +5% | kΩ |

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A.C Characteristics

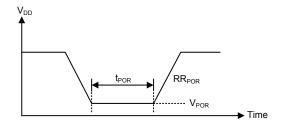
Ta=25°C

| Cumbal | Davamatar | Te | Min. | Turn | Max. | Unit | |
|-------------------|-----------------------------------|-----------------|-------------------------------------|--------|------|--------|-------------------|
| Symbol | Parameter | V _{DD} | Condition | IVIII. | Тур. | wax. | Unit |
| f _{HIRC} | High Speed Internal RC Oscillator | 3.3V~5.5V | USB mode | -0.25% | 12 | +0.25% | MHz |
| t _{sst} | System Start-up Timer Period | _ | RX pin Wake-up from power down mode | 16 | _ | _ | t _{HIRC} |
| t _{RSTD} | System Reset Delay Time | _ | Power-on reset | 25 | 50 | 100 | ms |

Power-on Reset Characteristics

Ta=25°C

| Cymhal | I. Baramatar | | est Conditions | Min | Tim | May | l lmi4 |
|-------------------|---|------------------------|----------------|-------|------|------|--------|
| Symbol | Parameter | V _{DD} | Conditions | Min. | Тур. | Max. | Unit |
| V _{POR} | V _{DD} Start Voltage to Ensure Power-on Reset | _ | _ | _ | _ | 100 | mV |
| RR _{POR} | V _{DD} Rising Rate to Ensure Power-on Reset | _ | _ | 0.035 | _ | _ | V/ms |
| t _{POR} | Minimum Time for V _{DD} Stays at V _{POR} to Ensure Power-on Reset | | _ | 1 | _ | _ | ms |



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USB Interface

The USB interface, being USB 2.0 full-speed compatible, is a 4-wire serial bus that allows communication between a host device and up to 127 max peripheral devices on the same bus. A token based protocol method is used by the host device for communication control. Other advantages of the USB bus include live plugging and unplugging and dynamic device configuration. As the complexity of USB data protocol does not permit comprehensive USB operation information to be provided in this datasheet, the reader should therefore consult other external information for a detailed understanding of the USB interface.

Power Planes

There are two power planes for the device and they are the USB bus power input (VDD) and 3.3V regulator output (V33O).

For the USB SIE, VDD will supply all circuits related to the USB SIE and be sourced from pin "VDD". Once the USB device is removed from the USB bus and there is no power on the USB BUS, the USB SIE circuit will no longer be operational.

USB Interface Operation

To communicate with an external USB host, the internal USB module has external pins known as D+ and D- along with the 3.3V regulator output V33O. A Serial Interface Engine, SIE, decodes the incoming USB data stream and transfers it to the correct endpoint buffer memory known as the FIFO. The USB module has 3 endpoints, EPO ~ EP2. Endpoint 0 supports the Control transfer while endpoint 1 ~ endpoint 2 support the Interrupt. The HT42B564-x Bridge IC supports the USB Human Interface Device Class, HID, for communication.

| Endpoint | Transfer Type |
|----------|---------------|
| 0 | Control |
| 1 | Interrupt In |
| 2 | Interrupt Out |

USB Endpoint Transfer Type

If there is no signal on the USB bus for over 3ms, the USB device will enter the suspend mode. The device enters the suspend state to meet the requirements of the USB suspend current specification. When the resume signal is asserted by the USB host, the device will be woken up and leave the suspend mode.

As the USB device has a remote wake-up function, the USB device can be woken-up the USB host by sending a remote wake-up pulse. Once the USB host receives a remote wake-up signal from the USB device, the host will send a resume signal to the device.

USB VID and PID Configuration

The device has configured the default Vendor ID (VID: 0x04D9), Product ID (PID: 0xB564) and product description strings of "USB HID UART Bridge".

This device has been configured to the default USB configuration data as shown in the following table.

| Parameter | Value (hex) |
|----------------------|---------------------|
| USB Vendor ID (VID) | 0x04D9 |
| USB Product ID (PID) | 0xB564 |
| Remote wake-up | Default enable |
| Manufacturer Name | Holtek |
| Product Description | USB HID UART Bridge |
| Serial Number | 0000 |

USB Data Format

Read Data: 32 bytes FIFO for each read data, the first byte valid for this number of data length received, ranges from 0 to 31. If there are more than 31 bytes of data, it will be divided into multiple times read.

Write Data: 32 bytes FIFO for each write data to be written first byte number, ranging from 0 to 31. If there are more than 31 bytes then write multiple times.

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UART Interface

The HT42B564-x contains an integrated full-duplex asynchronous serial communications UART interface that enables communication with external devices that contain a serial interface. The UART function has many features and can transmit and receive data serially by transferring a frame of data with eight data bits per transmission as well as being able to detect errors when the data is overwritten or incorrectly framed. The UART function possesses its own internal interrupt which can be used to indicate when a reception occurs or when a transmission terminates.

The integrated UART function contains the following features:

- Full-duplex, asynchronous communication
- 8 bits character length
- Even, odd or no parity options
- · One or two stop bits
- · Baud rate generator with prescaler
- 32-byte Deep FIFO Transmit Data Buffer
- 32-byte Deep FIFO Receive Data Buffer
- RX pin wake-up function
- UART pins power supply by VDDIO pin

UART External Pins

To communicate with an external serial interface, the internal UART has two external pins known as TX and RX. The TX and RX pins are the UART transmitter and receiver pins respectively.

UART Data Transfer Scheme

The data will be transferred to the Transmit Shift Register from where it will be shifted out, LSB first, onto the TX pin at a rate controlled by the Baud Rate Generator. Data to be received by the UART is accepted on the external RX pin, from where it is shifted in, LSB first, to the Receiver Shift Register at a rate controlled by the Baud Rate Generator. The UART interface provides a 32 byte deep FIFO transmit data buffer and a 32 byte deep FIFO receive data buffer for applications.

For data transfer, the UART function utilises a non-return-to-zero, more commonly known as NRZ, format. This is composed of one start bit, eight data bits and one or two stop bits. Parity is supported by the UART hardware and can be setup to be even, odd or no parity. For the most common data format, 8 data bits along with no parity and one stop bit, denoted as 8, N, 1, is used as the default setting, which is the setting at power-on.

The following diagram shows the transmit and receive waveforms for 8-bit data format.

Baud Rate Generator

To setup the speed of the serial data communication, the UART function contains its own dedicated baud rate generator. The default UART baud rate is 9600bps and it can be controlled by the application program.

The baud rate and miss rate table:

| Baud Rate | Real Rate | Miss Rate (%) |
|-----------|-------------|---------------|
| 9600 | 9603.841537 | 0.04 |
| 19200 | 19207.68307 | 0.04 |
| 38400 | 38461.53846 | 0.16 |
| 57600 | 57692.30769 | 0.16 |
| 115200 | 115384.6154 | 0.16 |

UART Power Down and Wake-up

If the USB host sends a suspend signal to the HT42B564-x USB device, it will enter the suspend mode. It is recommended to ensure that the UART data transmission or reception has finished before the device enters the suspend mode.

The UART function contains a receiver RX pin wakeup function. A falling edge on the RX pin will wake up the device from the suspend mode.



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USB HID Protocol

USB VID/PID

VID: 0x04D9 PID: 0xB564

USB TO UART Command

The application program uses USB HID set/get feature command to implement UART settings. There are following command:

- Get UARTSetting Command
- Set UARTSetting Command (CMDCode=01)

Get UARTSetting Command

| Request Name | Report Type | Raw Data |
|--------------|-------------|----------|
| Get_Report | Feature | D0~D7 |

D1~D8: UARTSettings (Refer to Table)

| Byte | Field | Size | Description |
|-------|------------|------|--|
| D0 | CMDCode | 1 | Command code. |
| D1~D4 | DTERate | 4 | Data terminal rate,in bits per second. (support 9600/19200/38400/57600/115200) 9600 → 80 25 00 00 19200 → 00 4B 00 00 38400 → 00 84 03 00 57600 → 00 E1 00 00 115200 → 00 C2 01 00 |
| D5 | CharFromat | 1 | Stop bits 0-1 Stop bits 1-2 Stop bits |
| D6 | ParityType | 1 | Parity 0-None 1-Odd 2-Even |
| D7 | DataBits | 1 | Data bits(only 8) |

UARTSettings

Set UARTSetting Command

| Request Name | equest Name Report Type | |
|--------------|-------------------------|-------|
| Set_Report | Feature | D0~D7 |

D0~D7: UARTSettings (Refer to Table.1) CMDCode=01H

Write Data

| Request Name | Report Type | Raw Data |
|--------------|-------------|----------|
| Interrupt | Output | D0~D31 |

Endpoint size 32bytes

The first data of D0 is the data length.

Read Data

| Request Name | Report Type | Raw Data |
|--------------|-------------|----------|
| Interrupt | Input | D0~D31 |

Endpoint size 32bytes

The first data of D0 is the data length.



DLL Function

HT42B564-x USB Bridge DLL Function

Holtek provides a Dynamic-Link Library (DLL) to setup the HT42B564-x Bridge IC for USB to UART data communication.

The Dynamic-link library DLL is a shared library of function executable files, available in different languages for application calls to use. The complex computing and communications USB is collated into a "HIDAPI.DLL". Users can use this DLL to develop different client applications without having to spend excessive amounts of time to study and write USB related functions.

DLL Function Definition:

BOOL SetFeature(HANDLE hDevice, LPVOID pData, DWORD nLen);

BOOL GetFeature(HANDLE hDevice, LPVOID pData, DWORD nLen);

HANDLE OpenFirstHIDDevice(DWORD dwVID, DWORD dwPID, DWORD dwUsagePage, DWORD dwUsage, BOOL bSync);

HANDLE OpenNextHIDDevice(DWORD dwVID, DWORD dwPID, DWORD dwUsagePage, DWORD dwUsage, BOOL bSync);

void CloseHIDDevice(HANDLE hDevice);

Usage - Taking C language as an example:

- HINSTANCE hLib=LoadLibrary ("HIDAPI.DLL");
- The GetProcAddress function fetches the addresses for accessing various functions

Taking C language as an example, the declarations are as follows:

BOOL (*SetFeature)(HANDLE hDevice, LPVOID pData, DWORD nLen);

BOOL (*GetFeature)(HANDLE hDevice, LPVOID pData, DWORD nLen);

HANDLE (*OpenFirstHIDDevice)(DWORD wVID, DWORD wPID, DWORD wUsagePage, DWORD wUsage, BOOL bSync);

HANDLE (*OpenNextHIDDevice)(DWORD wVID, DWORD wPID, DWORD wUsagePage, DWORD wUsage, BOOL bSync);

void (*CloseHIDDevice)(HANDLE hDevice);

Get the addresses

(FARPROC&) OpenFirstHIDDevice=GetProcAddress (m hLib,"OpenFirstHIDDevice");

(FARPROC&) OpenNextHIDDevice=GetProcAddress (m hLib,"OpenNextHIDDevice");

(FARPROC&) CloseHIDDevice=GetProcAddress (m hLib,"CloseHIDDevice");

(FARPROC&) SetFeature=GetProcAddress (m hLib,"SetFeature");

(FARPROC&) GetFeature=GetProcAddress (m hLib, "GetFeature");

Function Description:

HANDLE OpenFirstHIDDevice(DWORD wVID, DWORD wPID, DWORD wUsagePage, DWORD wUsage, BOOL bSync);

HANDLE OpenNextHIDDevice(DWORD wVID, DWORD wPID, DWORD wUsagePage, DWORD wUsage,BOOL bSync);

Parameters:

wVID Device Vendor ID. Any VID input will be regarded as the Holtek Vendor ID 0x04D9 by default in this version. Consult with Holtek if another VID is required.

wPID Device Product ID.

wUsagePage/wUsage

The UsagePage/Usage adopted by the Report Descriptor will be written with a value of "0" if it is not specified. More detailed information is contained in the HID spec. When the same VID/PID has multiple interfaces simultaneously, it is required to specify the exact UsagePage/Usage, otherwise only the first interface handle value will be returned.

bSync Use synchronous or asynchronous transmission. Synchronous transmission means that the function will not return a value until the Input/Output has finished.

Return Value:

Represents the value of the activated device. A NULL will be returned if the open operation has failed.

When the same VID/PID USB devices exist simultaneously, the OpenFirstHIDDevice function can be used to open the first one followed by an OpenNextHIDDevice function to open the next one until a NULL is returned.

After activation, the device is able to execute ReadFile/WriteFile operations, which are the Windows API Standard Functions.

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Void CloseHIDDevice(HANDLE hDevice);

Parameters:

hDevice Represents the value of the device to be closed.

BOOL SetFeature(HANDLE hDevice, LPVOID pData, DWORD nLen)

Parameters:

hDevice Represents the value of the activated device

pData

nLen

Data buffer where the Feature data is to be written. The first byte should be Report ID. A "0" value will be written to this byte if there is no Report ID.

.

Length of the Feature data to be written. The required length should be the Firmware-defined Feature Report length with the addition of one Report ID byte.

Return value:

1: Write success

0: Write fail

BOOL GetFeature(HANDLE hDevice, LPVOID pData, DWORD nLen);

Return value:

hDevice Represents the value of the activated device

pData

Data buffer where the Feature data is to be read. The first byte should be Report ID. A "0" value will be written to this byte if there is no Penert ID.

if there is no Report ID.

nLen

Length of the Feature data to be read. The required length should be the Firmware-defined Feature Report length with the addition of one Report ID byte.

Return value:

1: Read success

0: Read fail

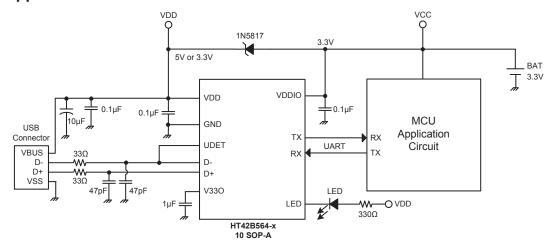
Considerations:

- The OpenFirstHIDDevice/OpenNextHIDDevice functions start up devices with a readable and writeable permission, therefore the device specified by the VID/PID should also be readable and writable. This means that the Firmware must provide OutputReport/InputReport or Interrupt IN/ Interrupt OUT.
- Use general file access functions, such as WriteFile(hDevice, ...) in the C language to write data to OutputReport or Interrupt OUT. The required data length should be the Firmware-defined OutputReport length with the addition of one Report ID byte. The first byte must be the Report ID. It will be "0" if there is no Report ID.
- Use general file access functions, such as ReadFile(hDevice, ...) in the C language, to read data from InputReport or Interrupt IN. The required data length should be the Firmware-defined InputReport length with the addition of one Report ID byte. The first byte must be the Report ID. It will be "0" if there is no Report ID.
- To use the SetFeature/GetFeature functions, the device must provide Feature Report, which means the Report Descriptor of the Firmware should contain the Feature Type.

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Application Circuits



Note: 1. The V_{CC} voltage must be less than the V_{DD} voltage. The V_{CC} supply can be sourced from a Li-battery.

2. The UDET pin can used to detect a USB plug-in for data transfer or to detect a USB plug-out for entering the energy saving mode.

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Package Information

Note that the package information provided here is for consultation purposes only. As this information may be updated at regular intervals users are reminded to consult the <u>Holtek website</u> for the latest version of the <u>Package/Carton Information</u>.

Additional supplementary information with regard to packaging is listed below. Click on the relevant section to be transferred to the relevant website page.

- Further Package Information (include Outline Dimensions, Product Tape and Reel Specifications)
- · Packing Meterials Information
- Carton information

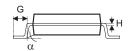
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10-pin SOP (150mil) Outline Dimensions







| Cumbal | Dimensions in inch | | | |
|--------|--------------------|-----------|-------|--|
| Symbol | Min. | Nom. | Max. | |
| Α | _ | 0.236 BSC | _ | |
| В | _ | 0.154 BSC | _ | |
| С | 0.012 | _ | 0.018 | |
| C' | _ | 0.193 BSC | _ | |
| D | _ | _ | 0.069 | |
| E | _ | 0.039 BSC | _ | |
| F | 0.004 | _ | 0.010 | |
| G | 0.016 | _ | 0.050 | |
| Н | 0.004 | _ | 0.010 | |
| α | 0° | _ | 8° | |

| Compleal | Dimensions in mm | | |
|----------|------------------|----------|------|
| Symbol | Min. | Nom. | Max. |
| A | _ | 6.00 BSC | _ |
| В | _ | 3.90 BSC | _ |
| С | 0.30 | _ | 0.45 |
| C' | _ | 4.90 BSC | _ |
| D | _ | _ | 1.75 |
| E | _ | 1.00 BSC | _ |
| F | 0.10 | _ | 0.25 |
| G | 0.40 | _ | 1.27 |
| Н | 0.10 | _ | 0.25 |
| α | 0° | _ | 8° |

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