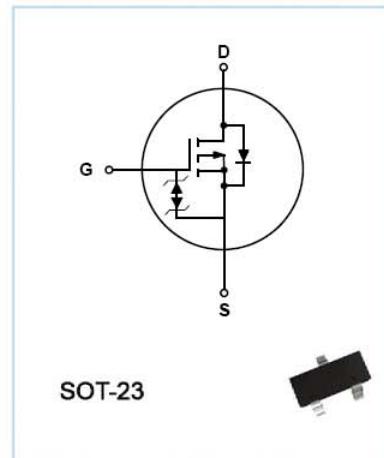


P-Channel Enhancement Mode MOSFET Feature

- -60V/-0.13A, $R_{DS(ON)} = \leq 6 \Omega$ (MAX) @ $V_{GS} = -10$ V
- Super High dense cell design for extremely low $R_{DS(ON)}$
- Reliable and Rugged
- SOT-23 for Surface Mount Package
- ESD protection



Applications

- Power Management
- Portable Equipment and Battery Powered Systems.

Absolute Maximum Ratings

$T_A=25^\circ\text{C}$ Unless Otherwise noted

Parameter	Symbol	Limit	Units
Drain-Source Voltage	V_{DS}	- 60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	-0.13	A

Electrical Characteristics

$T_A=25^\circ\text{C}$ Unless Otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Units
Off Characteristics						
Drain to Source Breakdown Voltage	$BVDSS$	$V_{GS}=0V, ID=-250\mu\text{A}$	-60	-	-	V
Zero-Gate Voltage Drain Current	$IDSS$	$V_{DS}=-50\text{V}, V_{GS}=0\text{V}$	-	-	-1	μA
Gate Body Leakage Current, Forward	$IGSSF$	$V_{GS}=20\text{V}, V_{DS}=0\text{V}$	-	-	10	μA
Gate Body Leakage Current, Reverse	$IGSSR$	$V_{GS}=-20\text{V}, V_{DS}=0\text{V}$	-	-	-10	μA
On Characteristics						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{GS}= V_{DS}, ID=-1\text{mA}$	-0.8	-1.5	-2.5	V
Static Drain-sourceOn-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = - 4.5 \text{ V}, I_{DS} = - 0.2 \text{ A}$	-	4	7	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Voltage	V_{SD}	$I_{SD} = 0.5 \text{ A}, V_{GS} = 0 \text{ V}$			-1.3	V

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DYNAMIC PARAMETERS						
Input Capacitance	Ciss	VGS=0V, VDS=-5V, f=1MHz		30		pF
Output Capacitance	Coss			10		pF
Reverse Transfer Capacitance	Crss			5		pF
SWITCHING PARAMETERS						
Turn-On Delay Time	tD(on)	VGS=-10V, VDS=-15V, ID=-0.25A, RL=50Ω		2.5		ns
Turn-On Rise Time	tr			1.0		ns
Turn-Off Delay Time	tD(off)			16		ns
Turn-Off Fall Time	tf			8		ns

Typical Characteristics

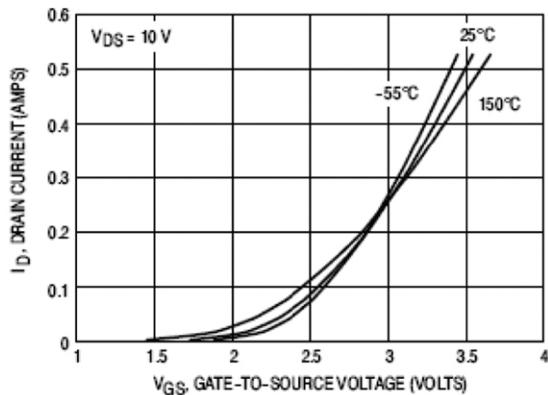


Figure 1. Transfer Characteristics

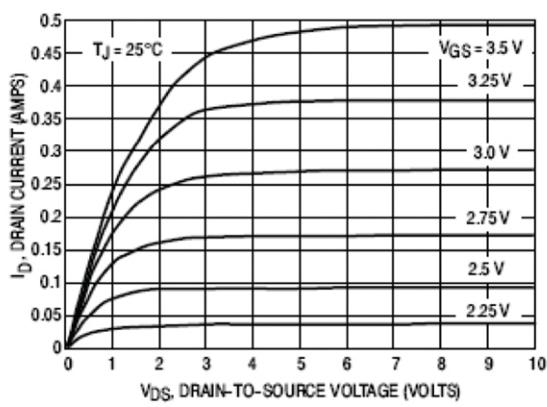


Figure 2. On-Region Characteristics

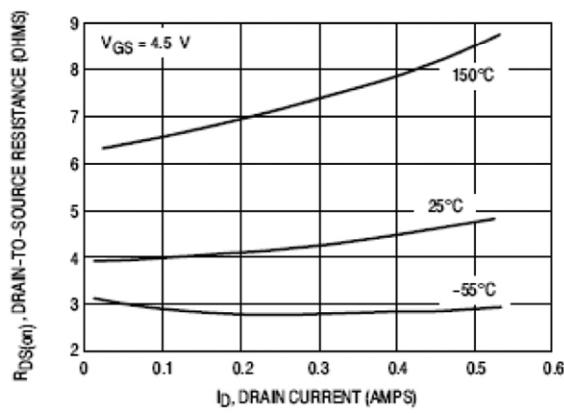


Figure 3. On-Resistance versus Drain Current

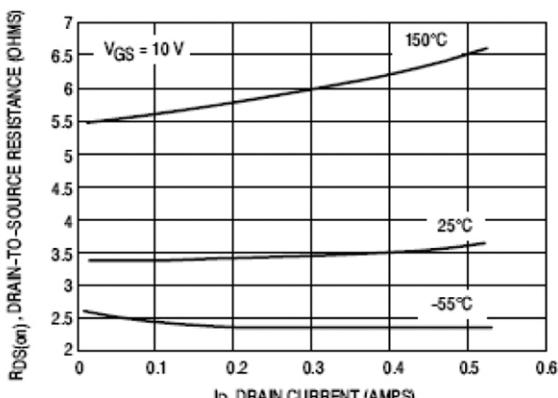


Figure 4. On-Resistance versus Drain Current

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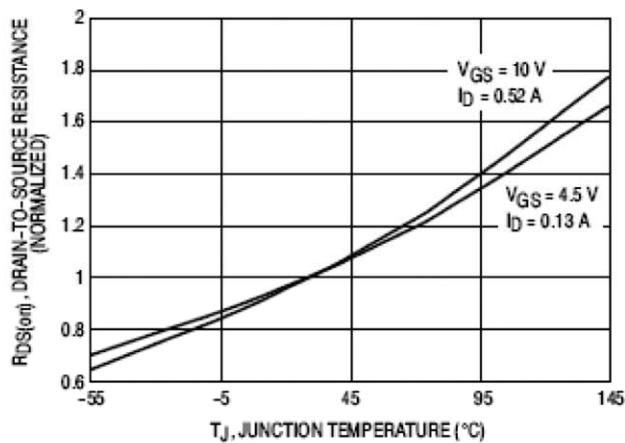


Figure 5. On-Resistance Variation with Temperature

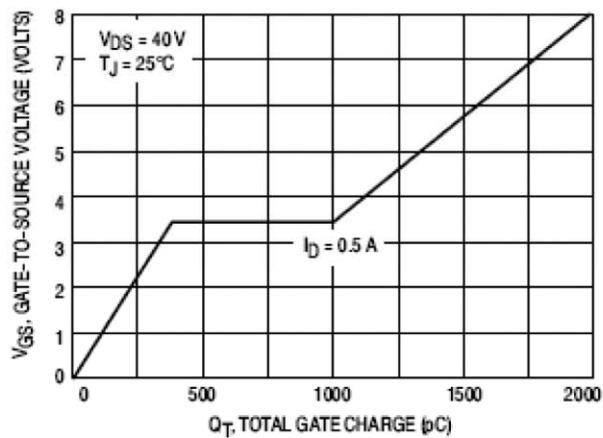


Figure 6. Gate Charge

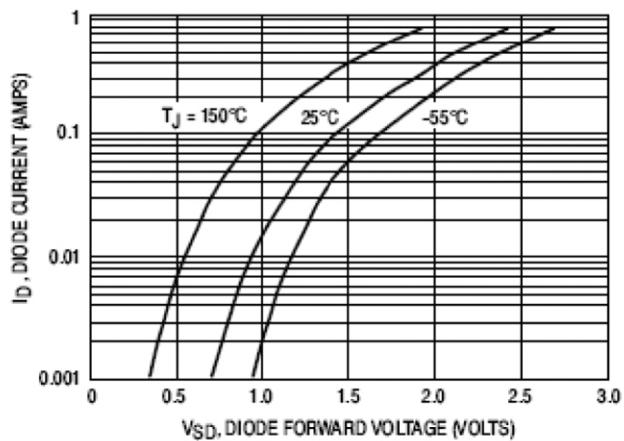


Figure 7. Body Diode Forward Voltage

SOT-23 Package Outline Dimensions (UNIT: mm)

