



Specification for Approval

Date: 2017/8/17

HCI1005LF-33NJ-MS8







Customer	:	盛泰

TAI-TECH P/N:

	CUSTOMER P/N:		
	DESCRIPTION:		
	QUANTITY:	pcs	<u>.</u>
REM	MARK:		
	Cı	stomer Approval Feedba	ack

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High Frequency Chip Inductor (Lead Free)

HCI1005LF-33NJ-MS8

DATE 17/06/27	DESCRIPTION 初版發行	APPROVED 楊祥忠	CHECKED	DRAWN
17/06/27	初版發行	楊祥忠		
		120 1 70.	詹偉特	張嘉玲

High Frequency Chip Inductor (Lead Free)

HCI1005LF-33NJ-MS8

1.Features

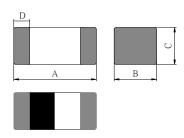
- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. S.M.T. type.
- 4. Suitable for reflow soldering.
- 5. Shapes and dimensions follow E.I.A. spec.
- 6. Available in various sizes.
- 7. Excellent solder ability and heat resistance.
- 8. High SRF up to 6GHz and above.
- 9. 100% Lead(Pb) & Halogen-Free and RoHS compliant.







2. Dimensions



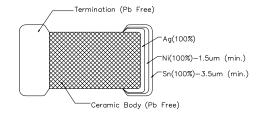
Chip Size				
Α	1.00±0.15			
В	0.50±0.15			
С	0.50±0.15			
D	0.25±0.10			

Units: mm

3. Part Numbering



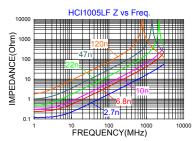
- A: Series
- B: Dimension L x W
- C: Category Code
- D: Material Lead Free Material
- E: Inductance 33N=33 nH F: Inductance Tolerance J=±5%
- G: marking

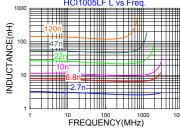


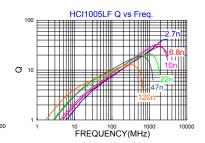
4.Specification

Tai-Tech	Inductance	Test Frequency	Q	Rated Current	DCR (Ω)	SRF (MHz)	
Part Number	(nH)	(Hz)	min.	(mA) max	max.	min.	
HCI1005LF-33NJ-MS8	33±5%	100M / 50mV	8	200	0.80	1300	

- Rated current: based on temperature rise test
- In compliance with EIA 595







TAI-TECH TBM01-170800362 P3.

5. Reliability and Test Condition

Item	Performance	Test Condition				
Series No.	HCI					
Operating Temperature	-40~+105°C (Including self-temperature rise)					
Transportation Storage Temperature	-40~+105°C (on board)	For long			ons, please	see the
Inductance (Ls)		Agilent42 Agilent E				
Q Factor	Refer to standard electrical characteristics list	Agilent42 Agilent16				
DC Resistance		Agilent 4	338			
Rated Current		DC Powe Over Rat some ris	ted Curr		ements, the	re will be
Temperature Rise Test	Rated Current < 1A ∆T 20°CMax Rated Current ≧ 1A ∆T 40°CMax	2. Tempe			current. by digital s	urface
Life test	Appearance: no damage. Impedance: within±15%of initial value.	times.(If Reflow F Tempera Applied of Duration Measure for 24±2	PC/JED Profiles) ature: 10 current: : 1000± id at ro hrs.	EC J-STD 05±2℃ rated curr 12hrs. om tempe	erature afte	sification
Load Humidity	Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow times.(IPC/JEDEC J-STD-020D Classif Reflow Profiles) Humidity: 85±2°C. Duration: 1000hrs Min. with 100% current. Measured at room temperature after profiles 1000 profiles 1			ssification	
Thermal shock	Preconditioning: Run through times.(IPC/JEDEC J-STD-020 Appearance: no damage. Appearance: no damage. Reflow Profiles) Condition for 1 cycle Impedance: within±15% of initial value. Step1: -40±2°C 30±5 min.				min. min. nin	sification
Vibration	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	times.(If Reflow P Oscillation minutes Equipme Total Am	PC/JED Profiles) on Frequent: Vi ent: Vi aplitude: Time: 1	EC J-STD uency: 10 bration ch 1.52mm± 2 hours(2)		ssification
Bending	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	following >=0805ind <0805ind Bending >=0805ind <0805ind	dimens nch(201 ch(2012 depth: nch(201 ch(2012	sions: 2mm):40x	m	
		Test co	ndition	:		
Shock	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value		Peak Value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec
	Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	SMD Lead	50 50	11	Half-sine	11.3
		Lead	50	11	Half-sine	11.3
Insulation Resistance	IR>1GΩ	Chip Inde		nly 0±10%V fo	or 30Sec.	

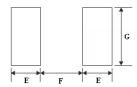
TAI-TECH TBM01-170800362 P4.

Item	Performance	Test Condition		
Solderability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec. Solder: Sn96.5%-Ag3%-Cu0.5% Solder temperature: 245±5°C Flux for lead free: Rosin. 9.5% Depth: completely cover the termination. Dip time: 4±1sec.		
		Number of heat cycles: 1		
Resistance to Soldering	Appearance : No damage. Impedance : within±15% of initial value	Temperature (°C) Time (s) Temperature ramp/immersion and emersion rate		
Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s		
		Depth: completely cover the termination		
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.		

6. Soldering and Mounting

6-1. Recommended PC Board Pattern

	Chip Size						Pattern ow Sold	
Series	Type	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)
HCI	1005	1.00±0.15	0.50±0.15	0.50±0.15	0.25±0.10	0.50	0.40	0.60



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)