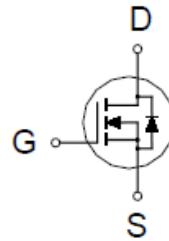
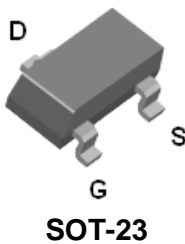


P3203CMG

N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	32m Ω @ $V_{GS} = 4.5V$	6A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS	
Drain-Source Voltage	V_{DS}	30	V	
Gate-Source Voltage	V_{GS}	± 12		
Continuous Drain Current	I_D	$T_A = 25\text{ }^\circ\text{C}$	6	A
		$T_A = 70\text{ }^\circ\text{C}$	5	
Pulsed Drain Current ¹	I_{DM}	30		
Power Dissipation	P_D	$T_A = 25\text{ }^\circ\text{C}$	1.25	W
		$T_A = 70\text{ }^\circ\text{C}$	0.8	
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ³	$R_{\theta JA}$		100	$^\circ\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

²Limited only by maximum temperature allowed.

P3203CMG

N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.45	0.7	1.2	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±16V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 16V, V _{GS} = 0V			1	μA
		V _{DS} = 10V, V _{GS} = 0V, T _J = 55 °C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 5V, V _{GS} = 4.5V	30			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 2.5V, I _D = 4A		32	50	mΩ
		V _{GS} = 4.5V, I _D = 5A		24	32	
		V _{GS} = 10V, I _D = 6A		22	28	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 5A		33		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		620		pF
Output Capacitance	C _{oss}			69		
Reverse Transfer Capacitance	C _{rss}			62		
Total Gate Charge ²	Q _g	V _{DS} = 0.5V _{(BR)DSS} , V _{GS} = 4.5V, I _D = 5A		8		nC
Gate-Source Charge ²	Q _{gs}			1.5		
Gate-Drain Charge ²	Q _{gd}			3		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 15V I _D ≅ 5A, V _{GS} = 4.5V, R _{GS} = 6Ω		4.5		nS
Rise Time ²	t _r			4		
Turn-Off Delay Time ²	t _{d(off)}			37		
Fall Time ²	t _f			6		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTIC (T_J = 25 °C)						
Continuous Current	I _S				1	A
Forward Voltage ¹	V _{SD}	I _F = 1.3A, V _{GS} = 0V			1.3	V
Reverse Recovery Time	t _{rr}	I _F = 6A, di _F /dt = 100 A/μs		10.5		nS
Reverse Recovery Charge	Q _{rr}				2.1	

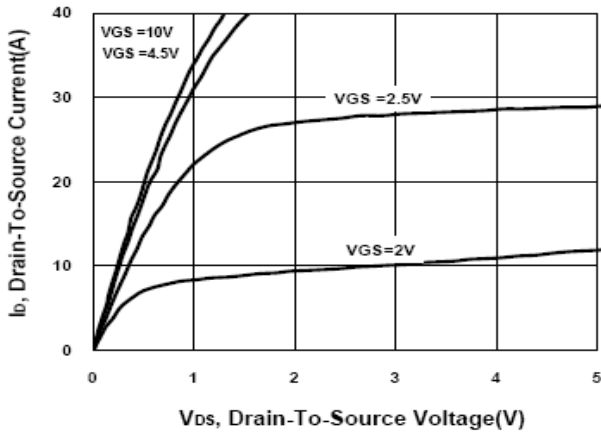
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

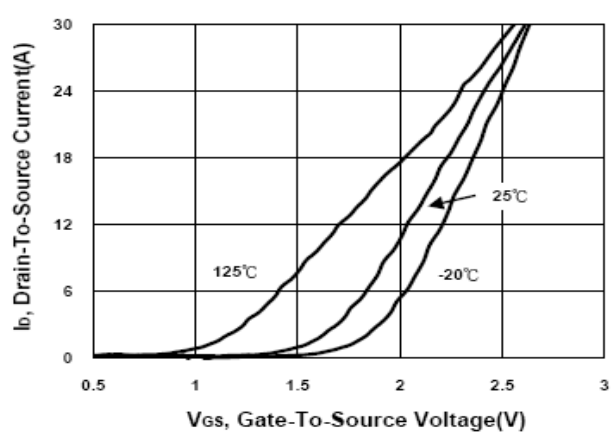
P3203CMG

N-Channel Enhancement Mode MOSFET

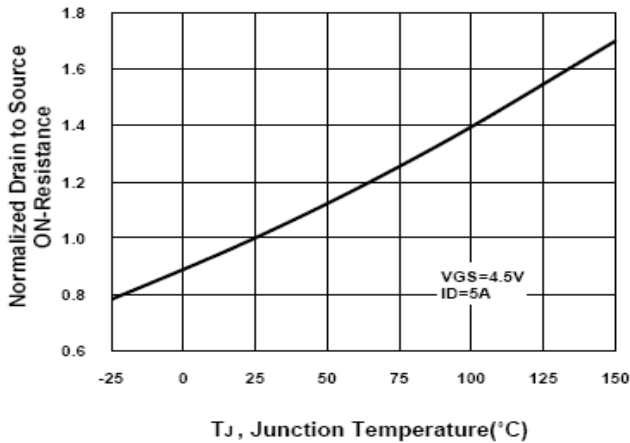
Output Characteristics



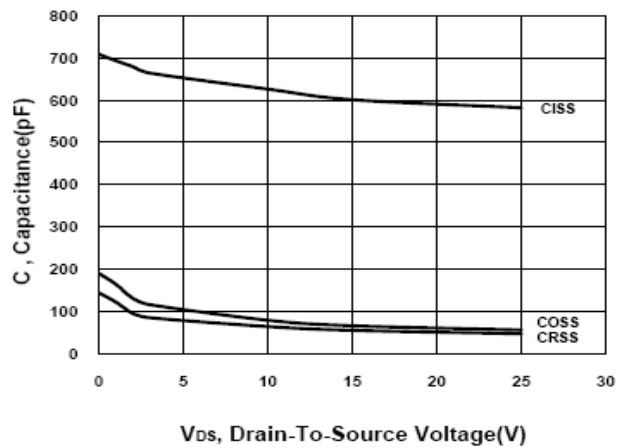
Transfer Characteristics



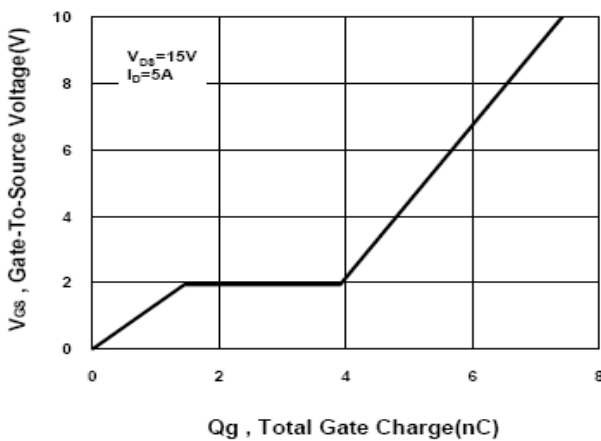
On-Resistance VS Temperature



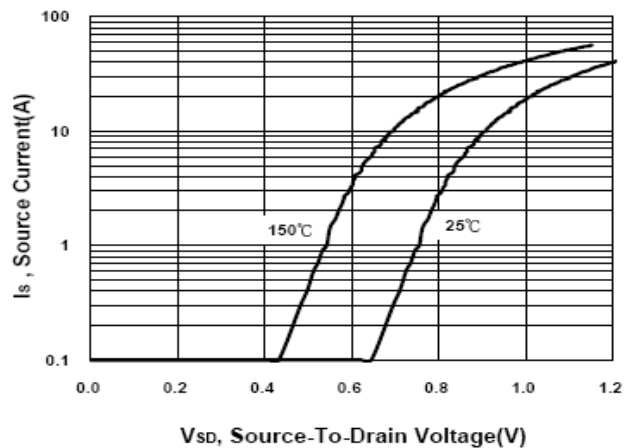
Capacitance Characteristic



Gate charge Characteristics



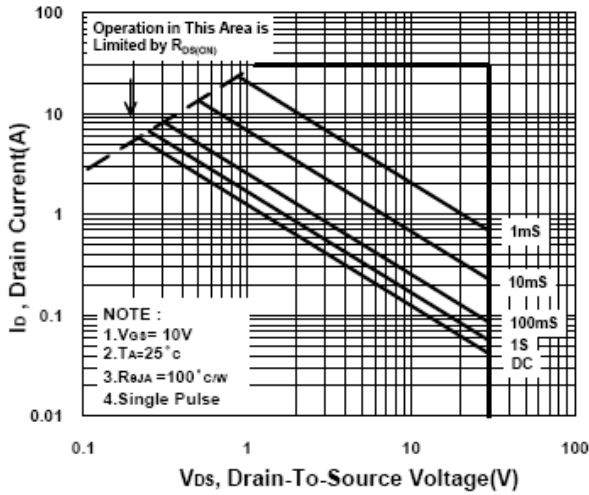
Source-Drain Diode Forward Voltage



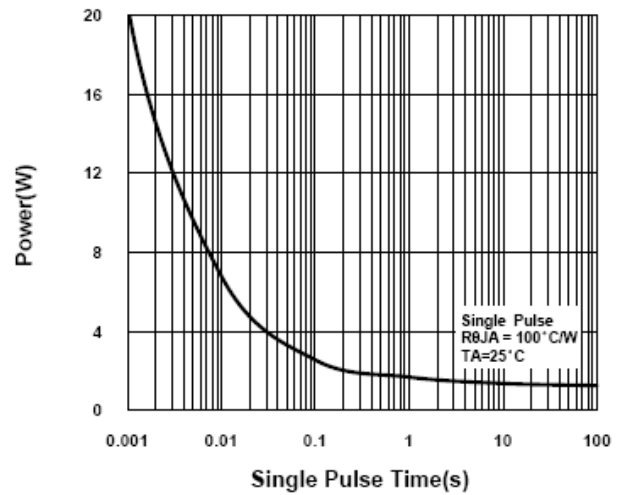
P3203CMG

N-Channel Enhancement Mode MOSFET

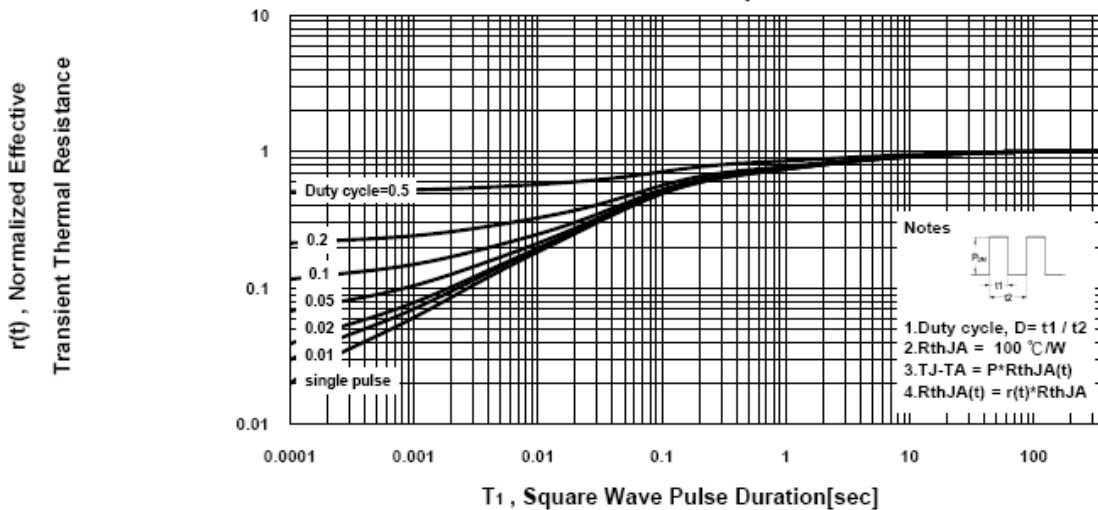
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



P3203CMG

N-Channel Enhancement Mode MOSFET

Package Dimension

SOT-23-3 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A		1.05		H	0.1		0.2
B	2.4		3	I	0.3		0.6
C	1.4		1.73				
D	2.7		3.1				
E	1		1.31				
F	0		0.15				
G	0.3		0.5				

