

HER201 THRU HER208

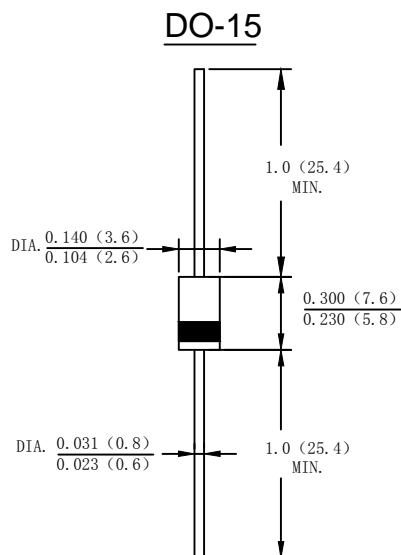
2.0 AMP. High Plastic silicon Efficient Rectifiers

Features

- Low forward voltage drop
- High current capability
- High reliability

Mechanical Data

- Case: Molded plastic DO-15
- Terminals: Plated leads solderable per MIL-STD-202, Method 208 guaranteed
- Polarity: Color band dented cathode end
- Mounting Position: Any



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	HER 201	HER 202	HER 203	HER 204	HER 205	HER 206	HER 207	HER 208	Unit
Maximum Recurrent Peak Reverse Voltage	V_{RM}	50	100	200	300	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	210	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	300	400	600	800	1000	V
Average Rectified Output Current (Note 1) @ $T_L=100^\circ C$	$I_{F(AV)}$	2.0								A
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	60								A
I^2t Rating for Fusing ($t < 8.3ms$)	I^2t	14.94								A ² s
Forward Voltage @ $I_F=2.0A$	V_{FM}	1.0		1.3			1.7			V
Peak Reverse Current @ $T_A=25^\circ C$	I_R	5.0								uA
At Rated DC Blocking Voltage @ $T_A=125^\circ C$		100								
Maximum Reverse Recovery Time (Note 2)	T_{RR}	50					75			nS
Typical Junction Capacitance (Note 3)	C_j	60					40			pF
Typical Thermal Resistance Junction to Ambient	$R_{\theta JA}$	25								°C/W
Operating Temperature Range	T_j	-65 to + 125								°C
Storage Temperature Range	T_{STG}	-65 to + 150								°C

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case

2. Reverse Recovery Test Conditions: $I_F=0.5A$, $I_R=1.0A$, $IRR=0.25A$

3. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

FIG. 1 - FORWARD CURRENT DERATING CURVE

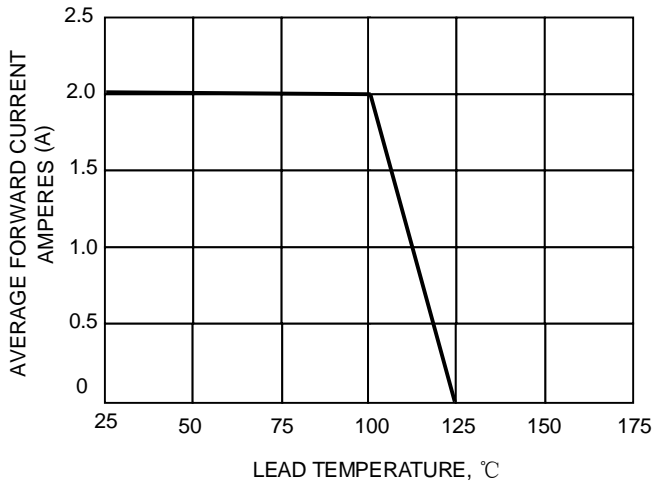


FIG. 2 – TYPICAL FORWARD CHARACTERISTICS

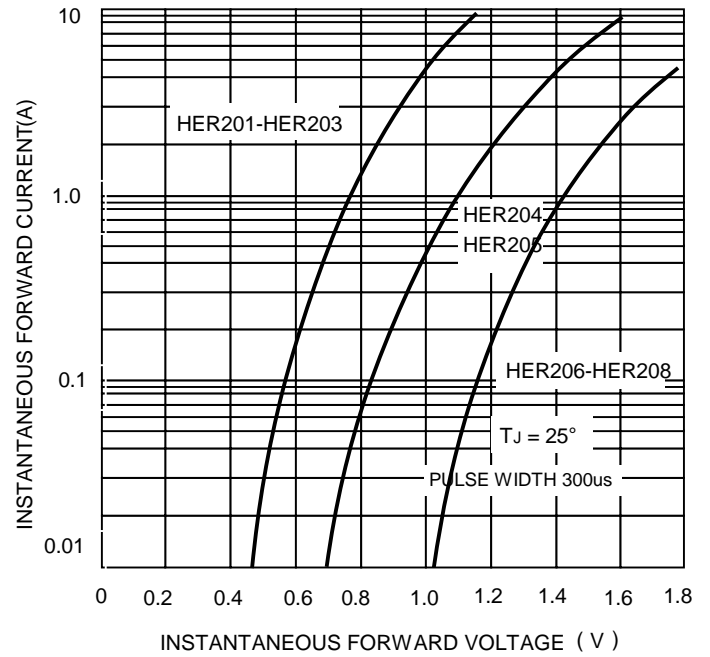


FIG. 3 – MAXIMUM NON-REPETITIVE SURGE CURRENT

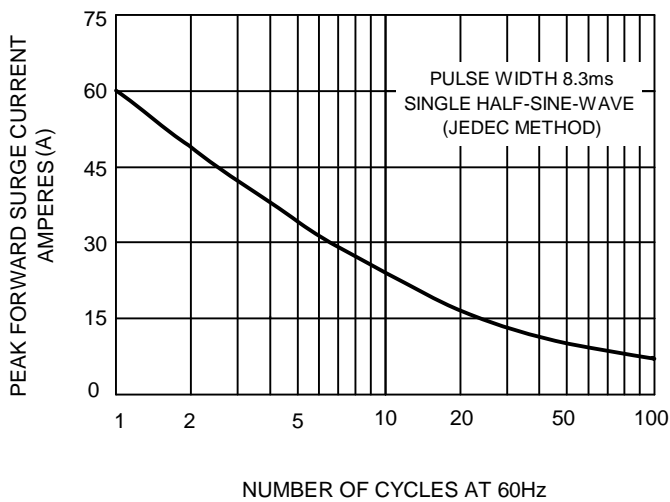
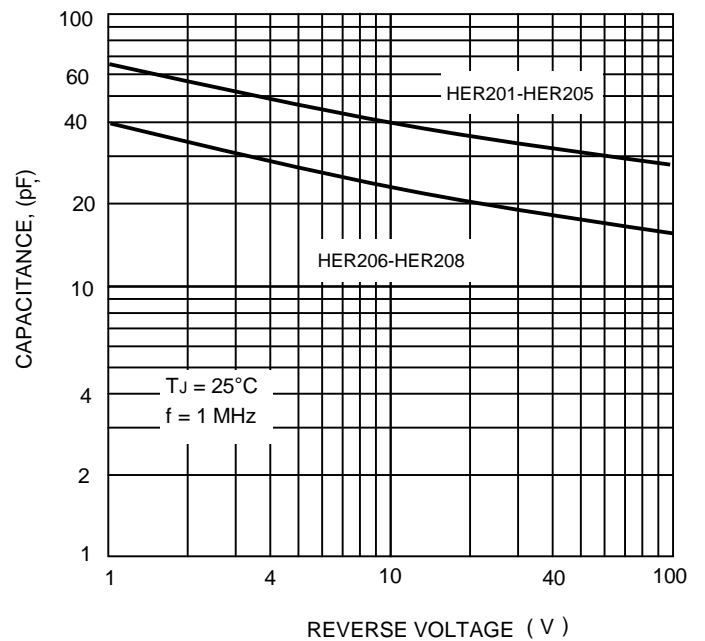


FIG. 4 – TYPICAL JUNCTION CAPACITANCE



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