

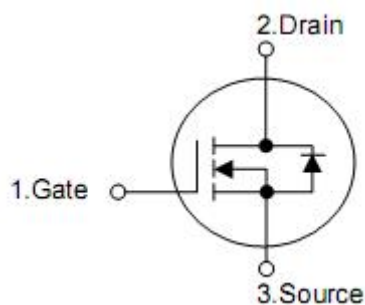
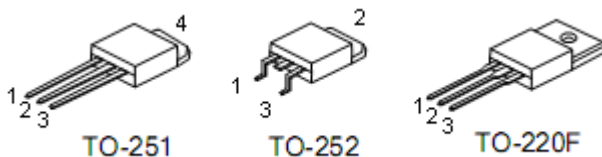
1. Description

This Power MOSFET is produced using KIA's advanced planar stripe DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

2. Features

- n $R_{DS(on) typ} = 1.8\Omega @ V_{GS} = 10V$
- n Low gate charge (typical 16nC)
- n High ruggedness
- n Fast switching
- n 100% avalanche tested
- n Improved dv/dt capability

3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source
4	Drain

4. Absolute maximum ratings

(T_C=25°C , unless otherwise noted)

Parameter	Symbol	Rating		Units
		To251	To252	
Drain-source voltage	V _{DSS}	700		V
Drain current continuous	I _D	T _C =25°C		5.8*
		T _C =100°C		3.0*
Drain current pulsed (note1)	I _{DM}	20*		A
Gate-source voltage	V _{GSS}	±30		V
Single Pulse avalanche energy (note2)	E _{AS}	150		mJ
Avalanche current (note1)	I _{AR}	4.8		A
Repetitive avalanche energy (note1)	E _{AR}	9.5		mJ
Peak diode recovery dv/dt (note3)	dv/dt	4.5		V/ns
Power dissipation	P _D	T _C =25°C		95
		Derate above 25°C		0.76
Operating and storage temperature range	T _J , T _{STG}	-55~+150		°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T _L	300		°C

* Drain current limited by maximum junction temperature.

5. Thermal characteristics

Parameter	Symbol	Rating		Unit
		To251/To252	TO220F	
Thermal resistance junction-case	R _{thJC}	1.3	2.6	°C/W
Thermal resistance, case-to-sink typ.	R _{thJS}	50	--	
Thermal resistance junction-ambient	R _{thJA}	110	62.5	

6. Electrical characteristics

(T_J=25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min	Typ	Max	Units
Off characteristics							
Drain-source breakdown voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	700	-	-	V
Breakdown voltage temperature coefficient		ΔBV _{DSS} /ΔT _J	I _D =250μA, Referenced to 25°C	-	0.7	-	V/°C
Zero gate voltage drain current		I _{DSS}	V _{DS} =700V, V _{GS} =0V	-	-	1	μA
			V _{DS} =560V, T _C =125°C	-	-	10	μA
Gate-body leakage current	Forward	I _{GSS}	V _{GS} =30V, V _{DS} =0V	-	-	100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V	-	-	-100	nA
On characteristics							
Gate threshold voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0	-	4.0	V
Static drain-source on-resistance		R _{DS(ON)}	V _{GS} =10V, I _D =2.4A	-	1.8	2.3	Ω
Dynamic characteristics							
Input capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	650	-	pF
Output capacitance		C _{OSS}		-	95	-	pF
Reverse transfer capacitance		C _{RSS}		-	10	-	pF
Switching characteristics							
Turn-on delay time		t _{D(ON)}	V _{DD} =350V, R _G =25Ω, I _D =6.0A (note 4,5)	-	30	-	ns
Rise time		t _R		-	40	-	ns
Turn-off delay time		t _{D(OFF)}		-	80	-	ns
Fall time		t _F		-	40	-	ns
Total gate charge		Q _G	V _{DS} =560V, V _{GS} =10V I _D =6.0A (note 4,5)	-	16	-	nC
Gate-source charge		Q _{GS}		-	4.5	-	nC
Gate-drain charge		Q _{GD}		-	5.0	-	nC
Drain-source diode characteristics							
Continuous drain-source current		I _S		-	-	4.8	A
Pulsed drain-source current		I _{SM}		-	-	20	A
Drain-source diode forward voltage		V _{SD}	V _{GS} =0V, I _S =4.8A	-	-	1.4	V
Reverse recovery time		t _{RR}	V _{GS} =0V, I _S =6.0A, di _F /dt=100A/μs (note 4)	-	280	-	ns
Reverse recovery charge		Q _{RR}		-	2.0	-	μC

Note: 1. Repetitive rating: pulse width limited by maximum junction temperature

2. L=8mH, I_{AS}=6.0A, V_{DD}=50V, R_G=25Ω, starting T_J=25°C

3. I_{SD}≤4.8A, di/dt ≤200A/μs, V_{DD}≤BV_{DSS}, starting T_J=25°C

4. Pulse test: pulse width ≤300μs, duty cycle ≤2%

5. Essentially independent of operating temperature

7. Test circuits and waveforms

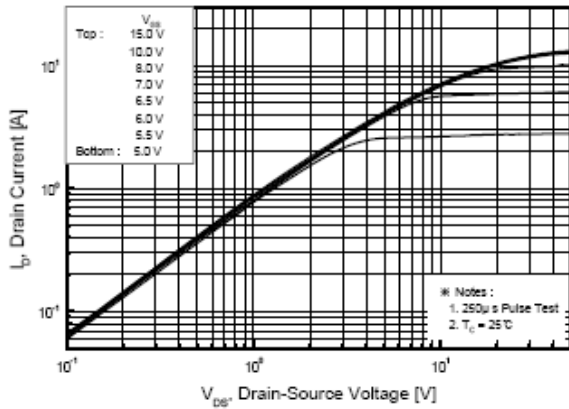


Figure 1. On-Region Characteristics

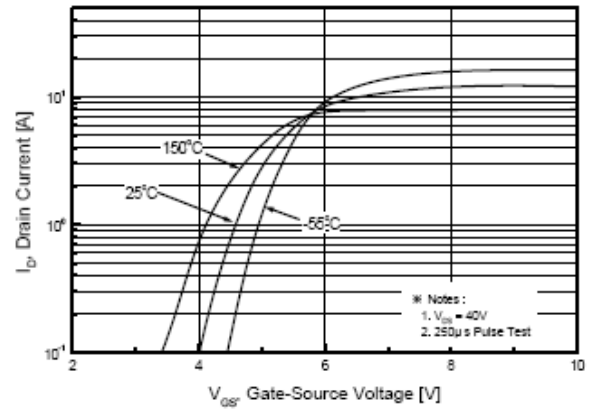


Figure 2. Transfer Characteristics

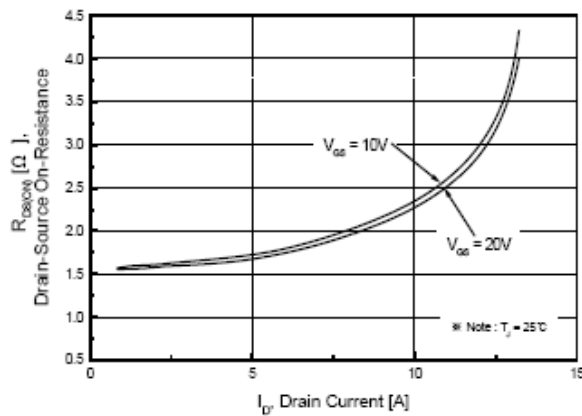


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

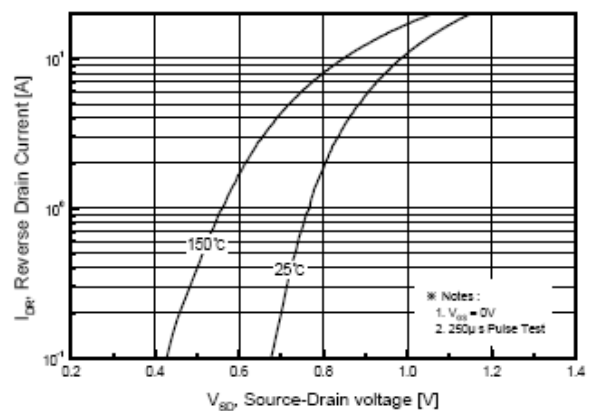


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

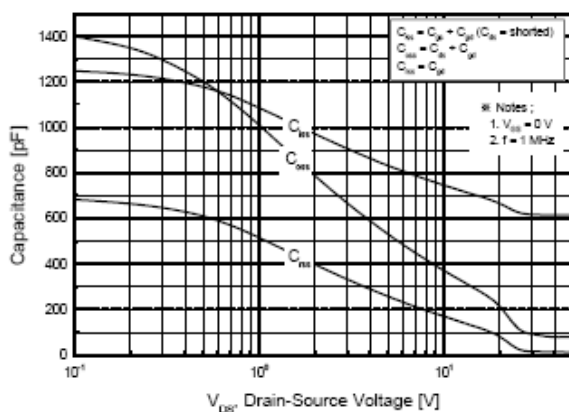


Figure 5. Capacitance Characteristics

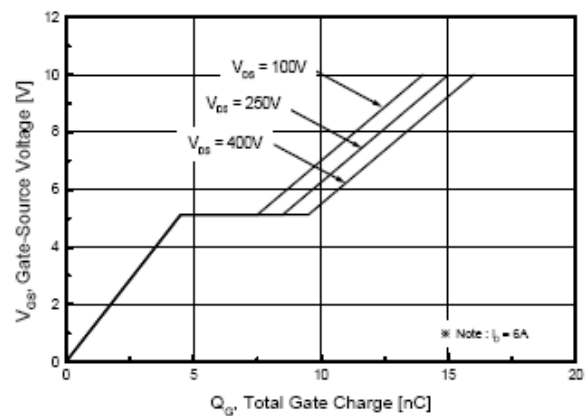


Figure 6. Gate Charge Characteristics

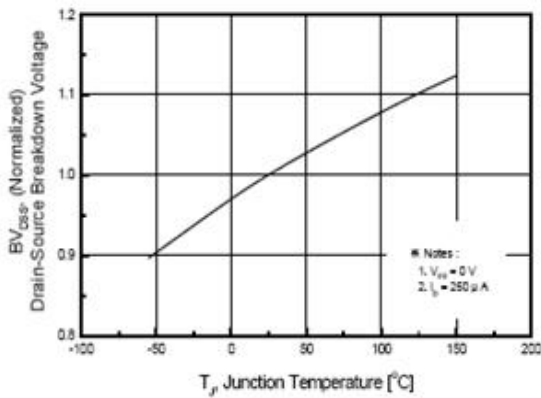


Figure 7. Breakdown Voltage Variation vs Temperature

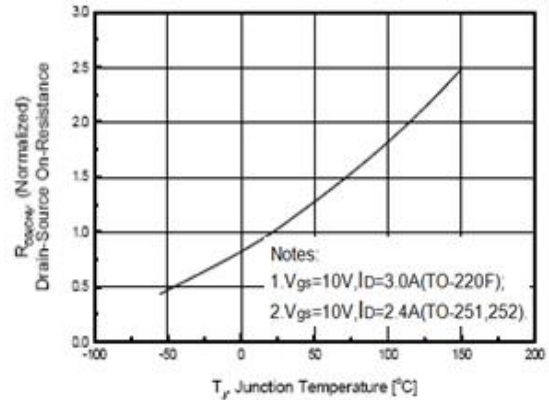


Figure 8. On-Resistance Variation vs Temperature

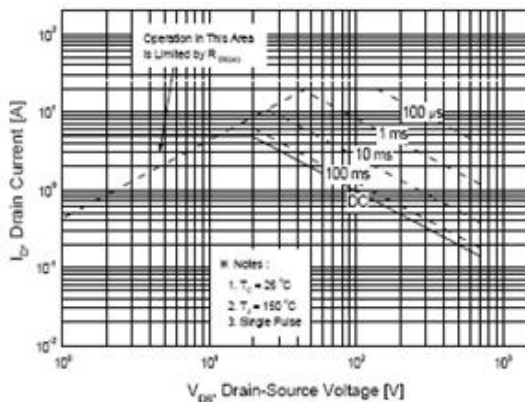


Figure 9. Maximum Safe Operating Area

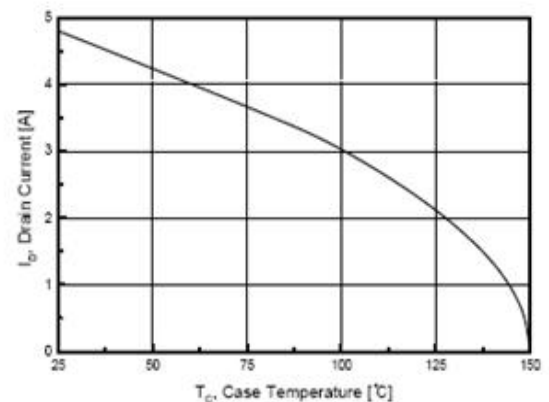


Figure 10. Maximum Drain Current vs Case Temperature

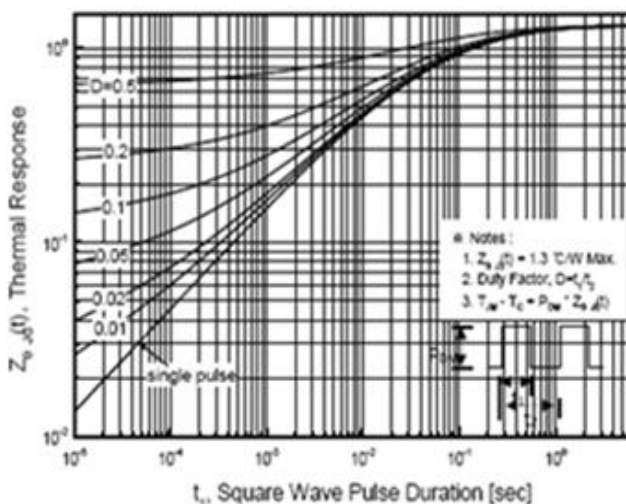


Figure 11-1. Transient Thermal Response Curve for (TO-251, 252)

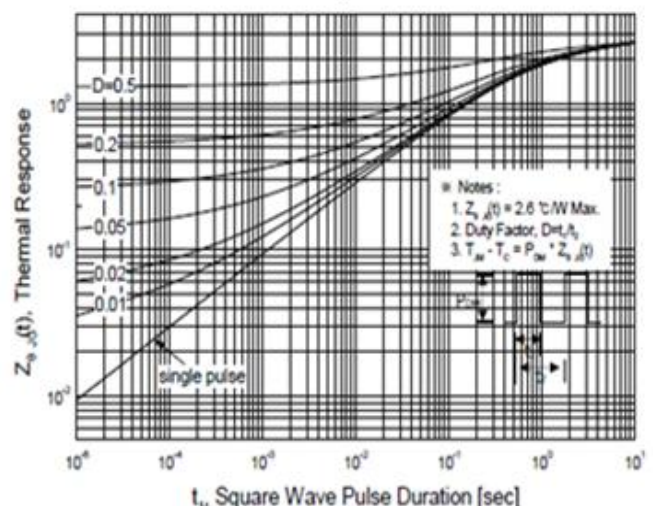


Figure 11-2. Transient Thermal Response Curve for (TO-220F)