



# STPS40170C

## HIGH VOLTAGE POWER SCHOTTKY RECTIFIER

**Table 1: Main Product Characteristics**

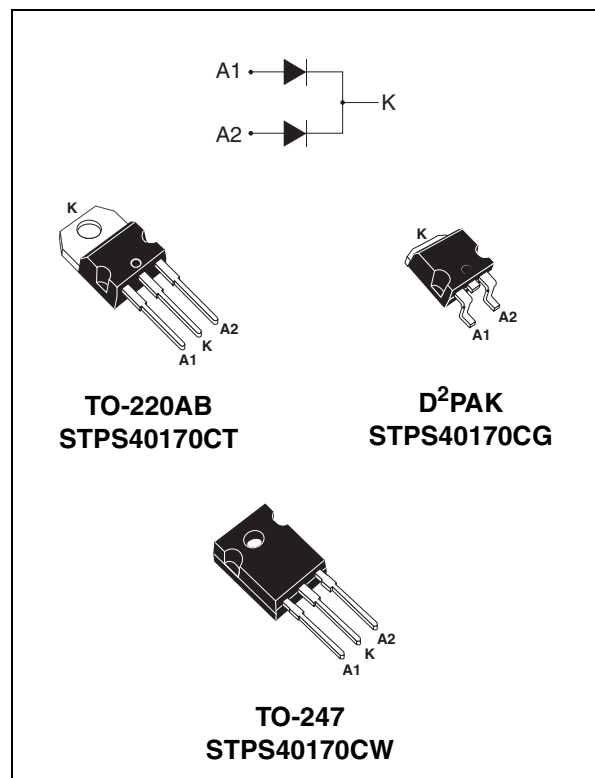
$I_{F(AV)}$	2 x 20 A
$V_{RRM}$	170 V
$T_j$	175 °C
$V_F(max)$	0.75 V

### FEATURES AND BENEFITS

- High junction temperature capability
- Low leakage current
- Good trade off between leakage current and forward voltage drop
- Low thermal resistance
- High frequency operation
- Avalanche specification

### DESCRIPTION

Dual center tab Schottky rectifier suited for High Frequency Switched Mode Power Supplies. Packaged in TO-220AB, D2PAK and TO-247, these devices are intended for use to enhance the reliability of the application.



**Table 2: Order Codes**

Part Numbers	Marking
STPS40170CT	STPS40170CT
STPS40170CG	STPS40170CG
STPS40170CG-TR	STPS40170CG
STPS40170CW	STPS40170CW

## STPS40170C

**Table 3: Absolute Ratings** (limiting values, per diode)

Symbol	Parameter		Value	Unit	
$V_{RRM}$	Repetitive peak reverse voltage		170	V	
$I_{F(RMS)}$	RMS forward current		60	A	
$I_{F(AV)}$	Average forward current	$T_C = 150\text{ °C}$ $\delta = 0.5$	Per diode	20	A
			Per device	40	
$I_{FSM}$	Surge non repetitive forward current	$t_p = 10\text{ ms}$ sinusoidal	250	A	
$P_{ARM}$	Repetitive peak avalanche power	$t_p = 1\text{ }\mu\text{s}$ $T_j = 25\text{ °C}$	14100	W	
$T_{stg}$	Storage temperature range		-65 to + 175	°C	
$T_j$	Maximum operating junction temperature *		175	°C	
dV/dt	Critical rate of rise of reverse voltage		10000	V/ $\mu\text{s}$	

\* :  $\frac{dP_{tot}}{dT_j} < \frac{1}{R_{th(j-a)}}$  thermal runaway condition for a diode on its own heatsink

**Table 4: Thermal Parameters**

Symbol	Parameter		Value	Unit
$R_{th(j-c)}$	Junction to case	Per diode	1.2	°C/W
		Total	0.85	
$R_{th(c)}$		Coupling	0.5	

When the diodes 1 and 2 are used simultaneously:

$$\Delta T_j(\text{diode } 1) = P(\text{diode } 1) \times R_{th(j-c)}(\text{Per diode}) + P(\text{diode } 2) \times R_{th(c)}$$

**Table 5: Static Electrical Characteristics** (per diode)

Symbol	Parameter	Tests conditions		Min.	Typ	Max.	Unit
$I_R$ *	Reverse leakage current	$T_j = 25\text{ °C}$	$V_R = V_{RRM}$			30	$\mu\text{A}$
		$T_j = 125\text{ °C}$			7	30	mA
$V_F$ **	Forward voltage drop	$T_j = 25\text{ °C}$	$I_F = 20\text{ A}$			0.92	V
		$T_j = 125\text{ °C}$			0.69	0.75	
		$T_j = 25\text{ °C}$	$I_F = 40\text{ A}$			1.00	
		$T_j = 125\text{ °C}$			0.79	0.86	

Pulse test: \*  $t_p = 5\text{ ms}$ ,  $\delta < 2\%$

\*\*  $t_p = 380\text{ }\mu\text{s}$ ,  $\delta < 2\%$

To evaluate the conduction losses use the following equation:  $P = 0.64 \times I_{F(AV)} + 0.055 I_F^2(\text{RMS})$

Figure 1: Average forward power dissipation versus average forward current (per diode)

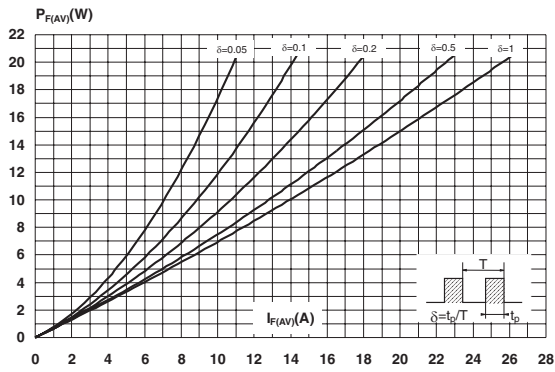


Figure 2: Average forward current versus ambient temperature ( $\delta = 0.5$ , per diode)

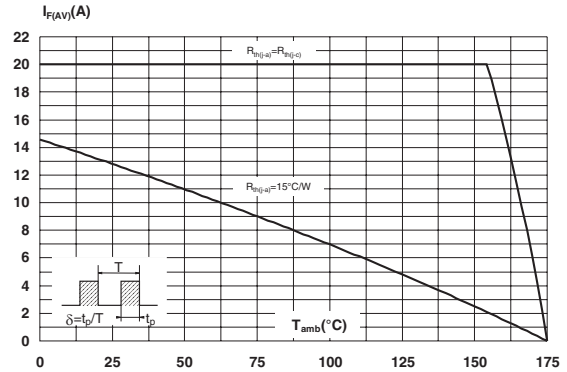


Figure 3: Normalized avalanche power derating versus pulse duration

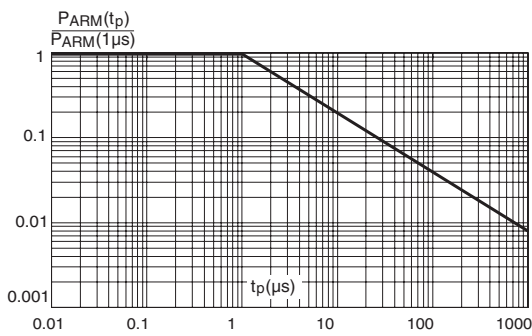


Figure 4: Normalized avalanche power derating versus junction temperature

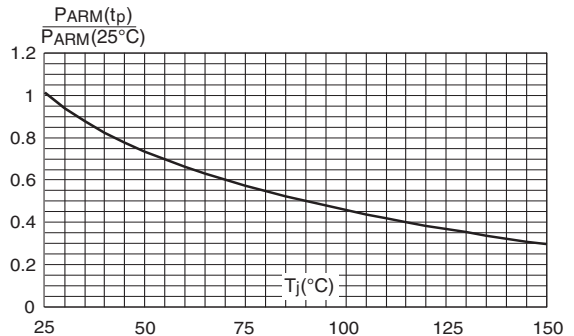


Figure 5: Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

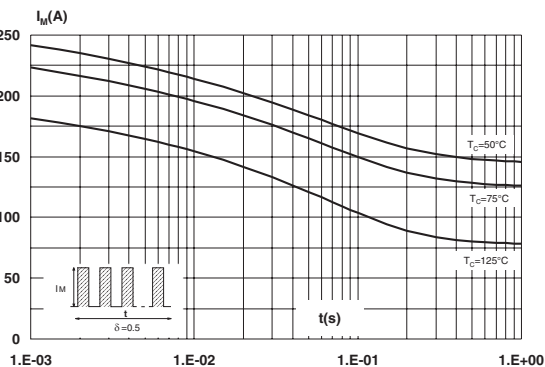


Figure 6: Relative variation of thermal impedance junction to case versus pulse duration

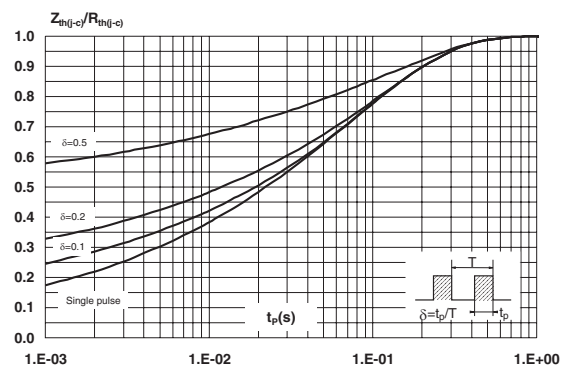


Figure 7: Reverse leakage current versus reverse voltage applied (typical values, per diode)

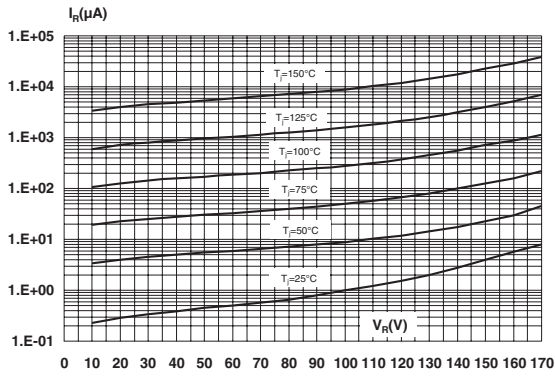


Figure 8: Junction capacitance versus reverse voltage applied (typical values, per diode)

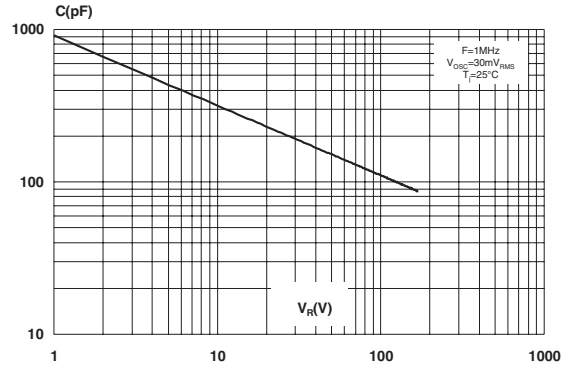


Figure 9: Forward voltage drop versus forward current (per diode, low level)

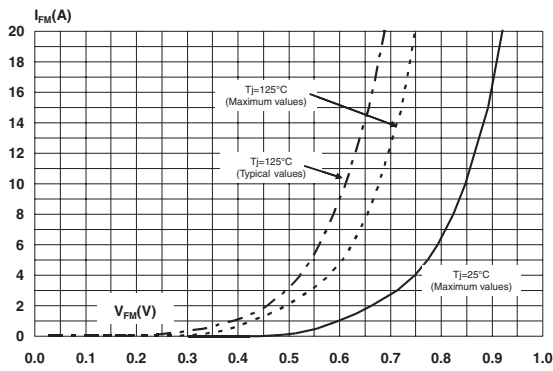


Figure 10: Forward voltage drop versus forward current (per diode, high level)

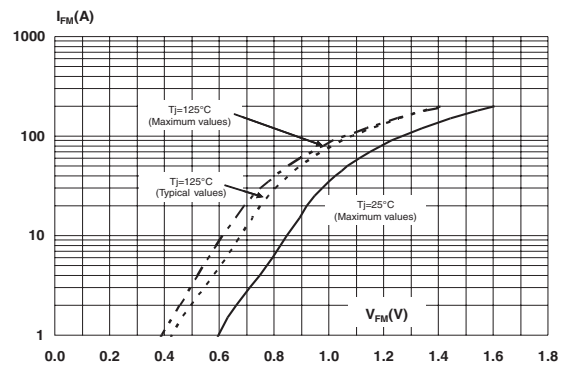


Figure 11: Thermal resistance junction to ambient versus copper surface under tab (epoxy printed board FR4, Cu = 35µm) (D<sup>2</sup>PAK)

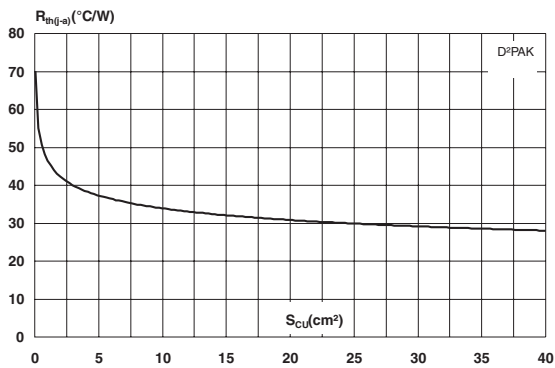


Figure 12: D<sup>2</sup>PAK Package Mechanical Data

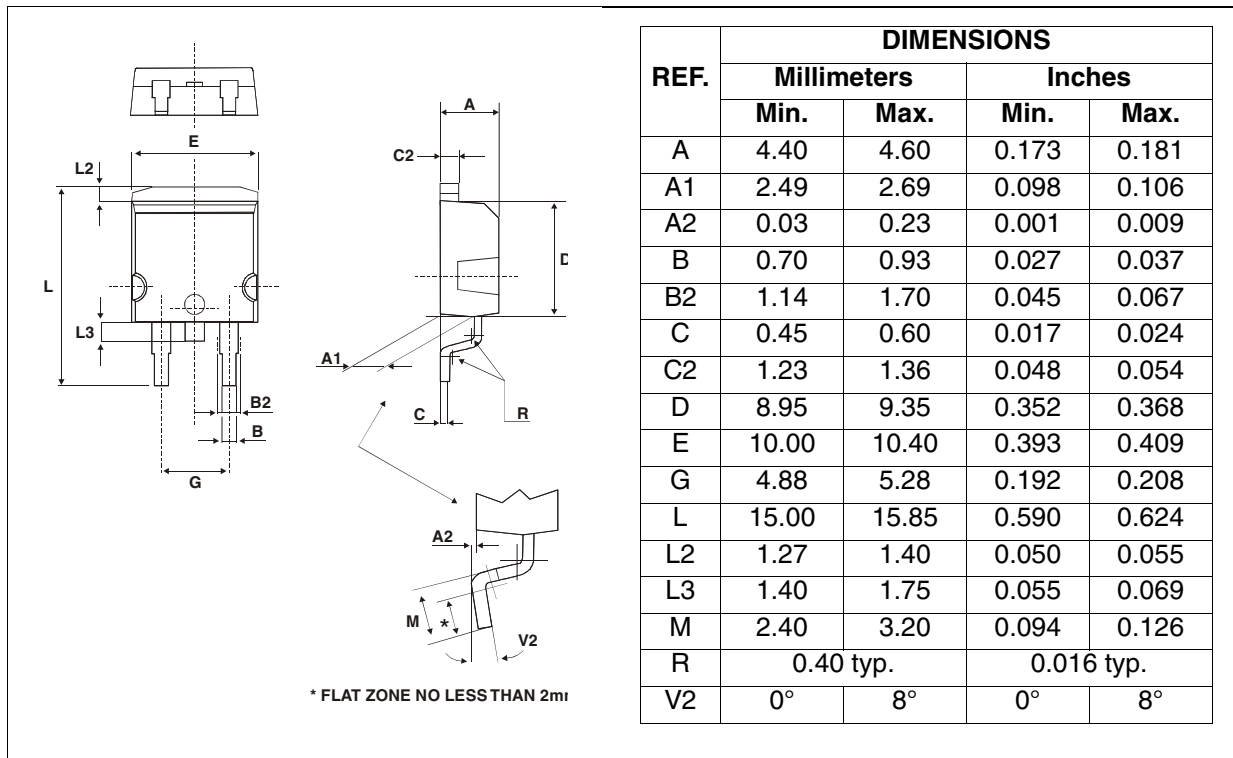


Figure 13: Foot Print Dimensions (in millimeters)

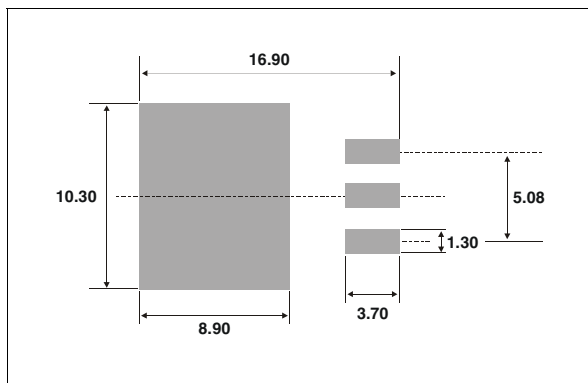


Figure 14: TO-247 Package Mechanical Data

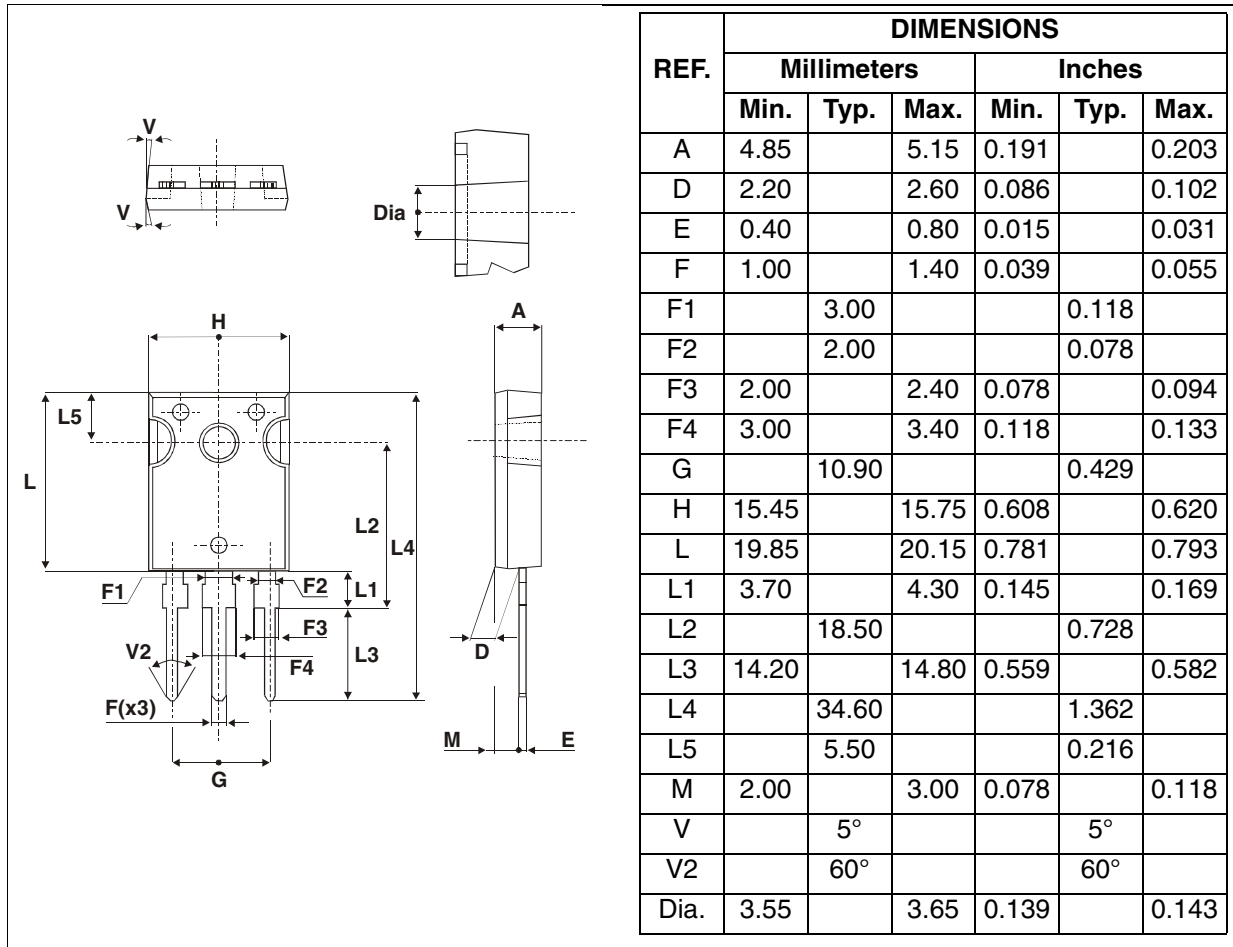
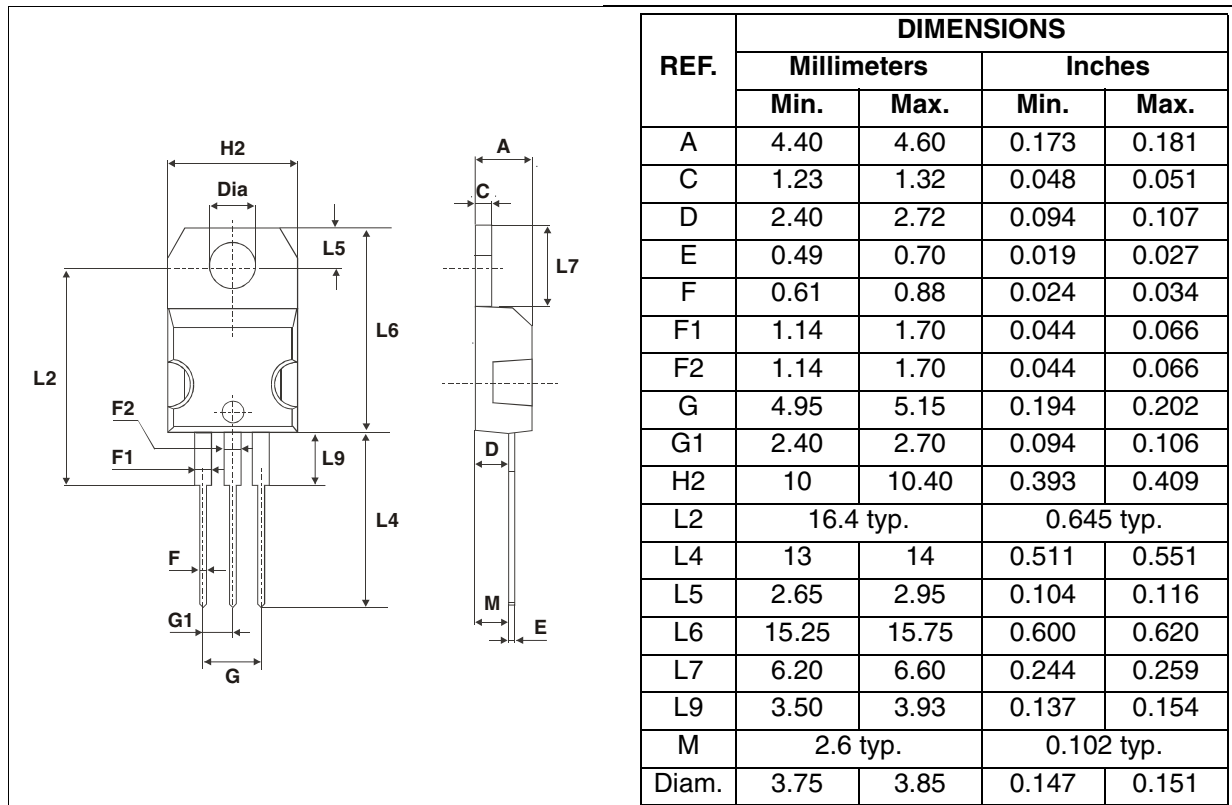


Figure 15: TO-220AB Package Mechanical Data



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: [www.st.com](http://www.st.com).

Table 6: Ordering Information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STPS40170CT	STPS40170CT	TO-220AB	2.20 g	50	Tube
STPS40170CG	STPS40170CG	D <sup>2</sup> PAK	1.48 g	50	Tube
STPS40170CG-TR	STPS40170CG			1000	Tape & reel
STPS40170CW	STPS40170CW	TO-247	4.4 g	30	Tube

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- TO-220 - Recommended torque value: 0.55 Nm, Maximum torque value: 0.7 Nm.
- TO-247 - Recommended torque value: 0.8 Nm, Maximum torque value: 1.0 Nm.

Table 7: Revision History

Date	Revision	Description of Changes
16-Sep-2005	1	First issue.

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