迪一電子®

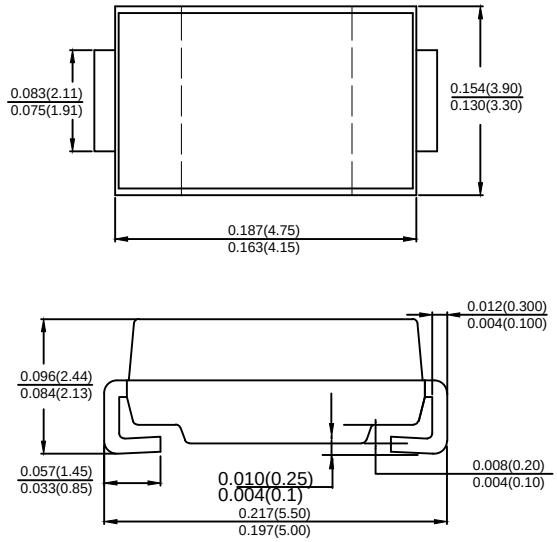
S2A THRU S2M

2.0 AMP SURFACE MOUNT PASSIVATED RECTIFIERS

Features

- Glass Passivated Die Construction
- Low forward voltage drop
- High current capability
- High reliability
- Metal silicon junction,majority carrier conduction
- Plastic Case Material has UL Flammability Classication Rating 94V-0

SMB/DO-214AA



Dimensions in inches and (millimeters)

Mechanical Data

- Case: Molded plastic SMB
- Terminals: Plated leads solderable per MIL-STD-750,Method 2026 guaranteed
- Polarity: Color band dentes cathode end
- Mounting Position: Any
- Making: Type Number

Maximum Ratings and Electrical Characteristics

Rating at 25℃ ambient temperature unless otherwise specified

Single phase,half wave,60Hz,resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	S2A	S2B	S2D	S2G	S2J	S2K	S2M	Unit
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Average Rectified Output Current @T_L =100℃	$I_{F(AV)}$	2.0							A
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	60							A
Forward Voltage @IF=2.0A	V_{FM}	1.0							V
Peak Reverse Current @T_A =25℃	I_R	5.0							uA
At Rated DC Blocking Voltage @T_A=125℃		100							
I^2t Rating for fusing (t <8.3ms)	I^2t	14.94							A^2s
Typical Junction Capacitance (Note 1)	C_J	30							pF
Typical Thermal Resistance Junction to Ambient(Note 2)	$R_{\theta JA}$	50							℃/W
Operating Temperature Range	T_J	-55 to+150							℃
Storage Temperature Range	T_{STG}	-55 to +150							℃

Note: 1. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

2.Thermal Resistance from Junction to Ambient at 0.375(9.5mm) lead length .

FIG.1MAXIMUM AVERAGE FORWARD CURRENT DERATING

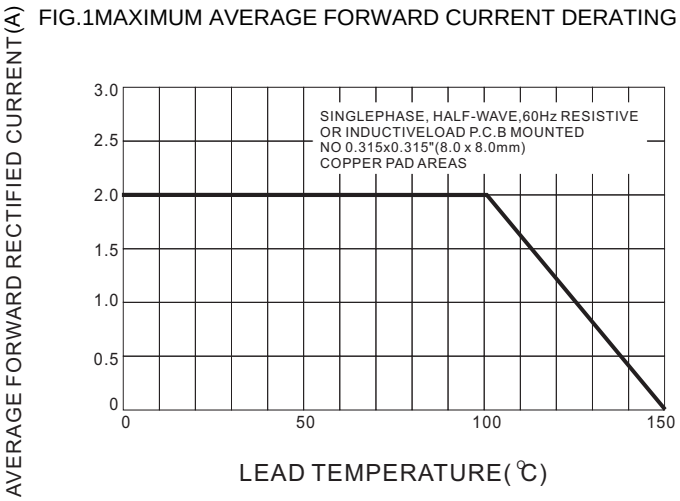


FIG.2TYPICAL FORWARD CHARACTERISTICS

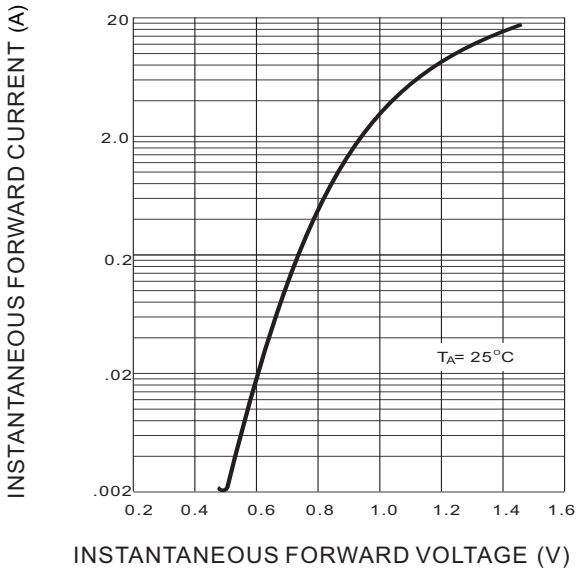


FIG.3MAXIMUM NON-REPEITIVE SURGE CURRENT

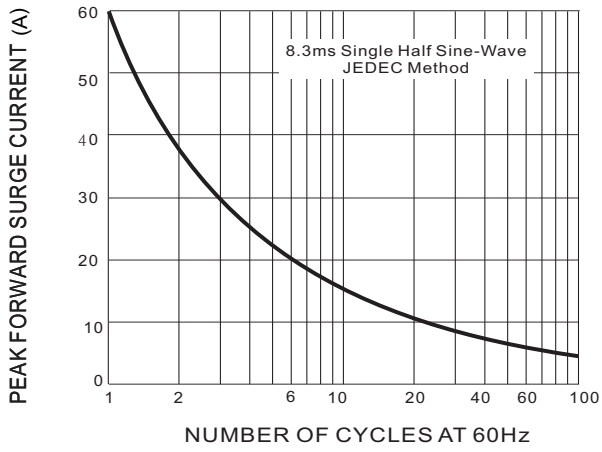


Fig. 4 T ypical Reverse Characteristics (per element)

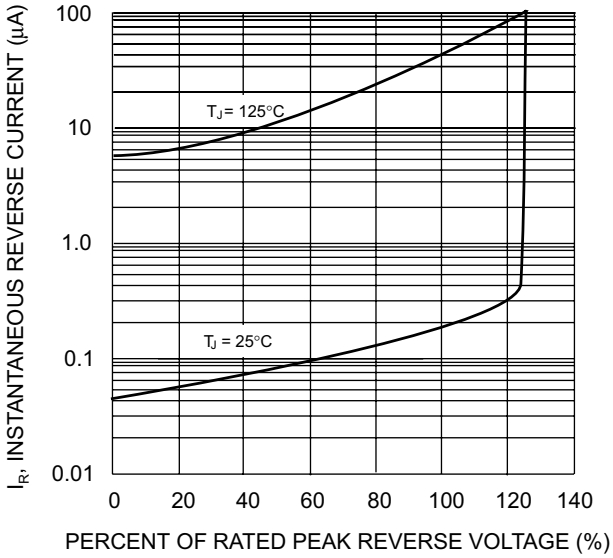
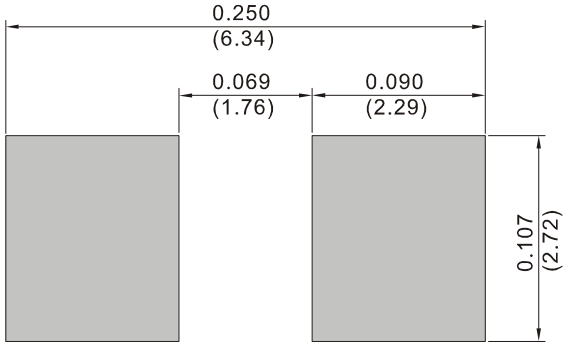


FIG.5 MOUNTING PAD LAYOUT



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