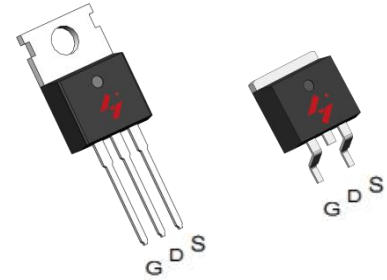


N-Channel Enhancement Mode MOSFET

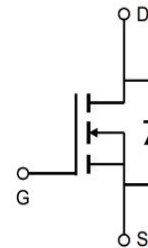
Feature Description

- 100V/120A
 $R_{DS(ON)} = 4.2m\Omega (typ.) @ V_{GS} = 10V$
- 100% Avalanche Tested
- Reliable and Rugged
- Lead Free and Green Devices Available
 (RoHS Compliant)

Pin Description



TO-220FB-3L TO-263-2L





N-Channel MOSFET

Applications

- Switch application
- DC/DC Converter

Ordering and Marking Information

 P HY045N10 YYXXXJWW G	 B HY045N10 YYXXXJWW G	Package Code P:TO-220FB-3L B:TO-263-2L Date Code YYXXX WW
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Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termination finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines “Green” to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this product and/or to this document at any time without notice.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage	100	V	
V _{GSS}	Gate-Source Voltage	±20	V	
T _J	Maximum Junction Temperature	175	°C	
T _{STG}	Storage Temperature Range	-55 to 175	°C	
I _S	Source Current-Continuous(Body Diode)	Tc=25°C	120	A
Mounted on Large Heat Sink				
I _{DM}	Pulsed Drain Current *	Tc=25°C	480	A
I _D	Continuous Drain Current	Tc=25°C	120	A
		Tc=100°C	81	A
P _D	Maximum Power Dissipation	Tc=25°C	221	W
		Tc=100°C	110	W
R _{θJC}	Thermal Resistance, Junction-to-Case**		0.68	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient		62.5	°C/W
E _{AS}	SinglePulsed-Avalanche Energy ***	L=0.3mH	662	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.
 ** Drain current is limited by junction temperature
 *** Limited by T_{Jmax}, starting T_J=25°C, L = 0.3mH, V_D= 80V, V_{GS}=10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY045N10			Unit
			Min	Typ.	Max	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100	-	-	V
I _{DSS}	Drain-to-Source Leakage Current	V _{DS} =100V, V _{GS} =0V	-	-	1	μA
		T _J =55°C	-	-	5	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2	3	4	V
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)*}	Drain-Source On-State Resistance	V _{GS} =10V, I _{DS} =50A	-	4.2	5.0	mΩ
Diode Characteristics						
V _{SD*}	Diode Forward Voltage	I _{SD} =50A, V _{GS} =0V	-	0.9	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =50A, dI _{SD} /dt=100A/μs	-	53	-	ns
Q _{rr}	Reverse Recovery Charge		-	107	-	nC

Electrical Characteristics (Cont.) (T_c =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY045N10			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1 MHz	-	1.9	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, Frequency=1.0MHz	-	4452.2	-	pF
C _{oss}	Output Capacitance					
C _{rss}	Reverse Transfer Capacitance					
t _{d(ON)}	Turn-on Delay Time	V _{DD} =40V, R _G =4Ω, I _{DS} =50A, V _{GS} =10V	-	20	-	ns
T _r	Turn-on Rise Time					
t _{d(OFF)}	Turn-off Delay Time					
T _f	Turn-off Fall Time					
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =80V, V _{GS} =10V, I _D =30A	-	67.5	-	nC
Q _{gs}	Gate-Source Charge					
Q _{gd}	Gate-Drain Charge					

Note: *Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%

Typical Operating Characteristics

Figure 1: Power Dissipation

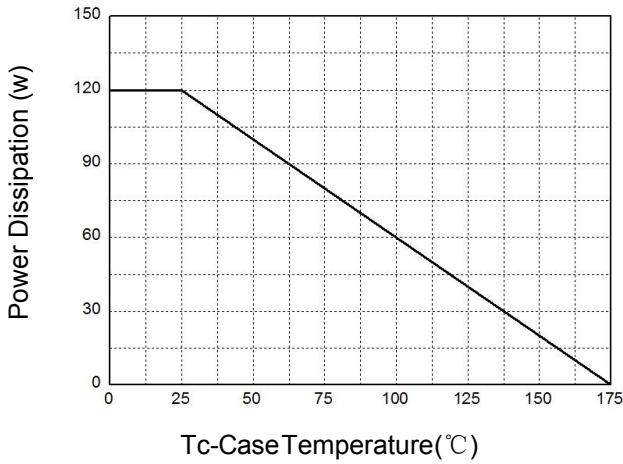


Figure 2: Drain Current

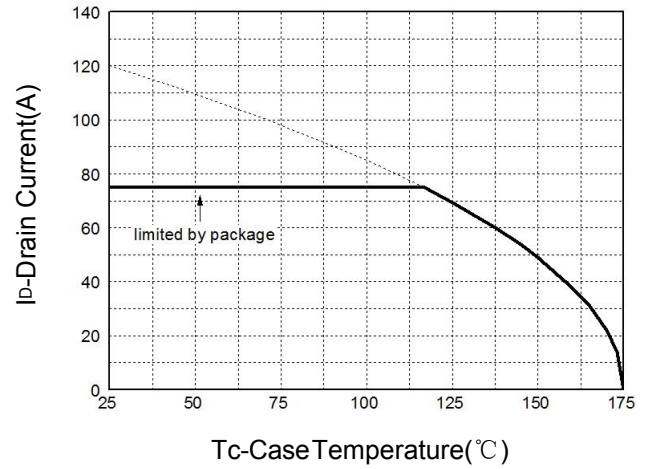


Figure 3: Safe Operation Area

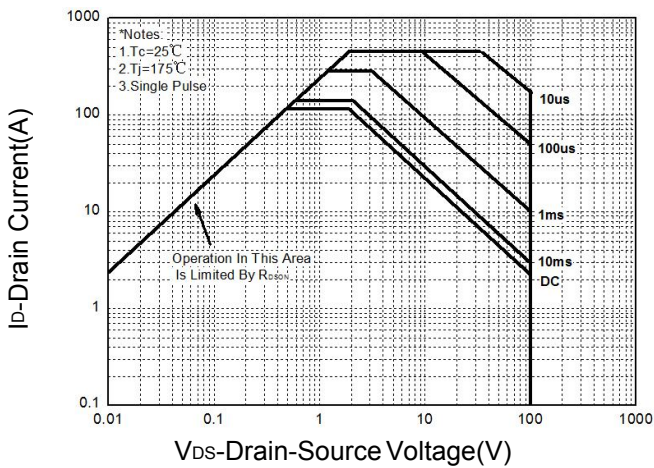


Figure 4: Thermal Transient Impedance

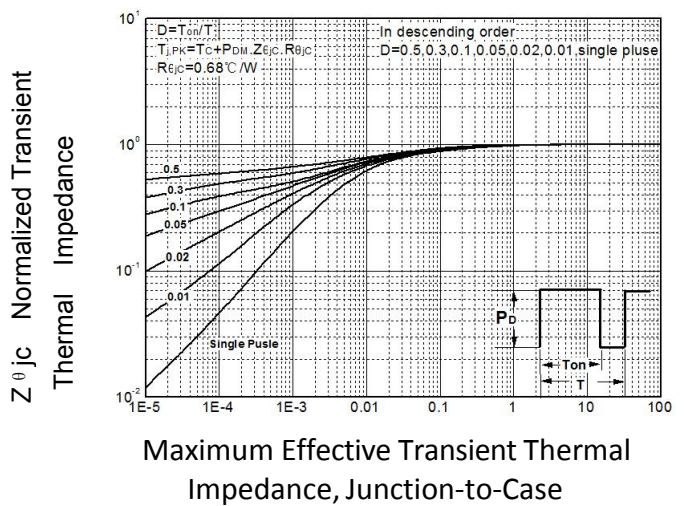


Figure 5: Output Characteristics

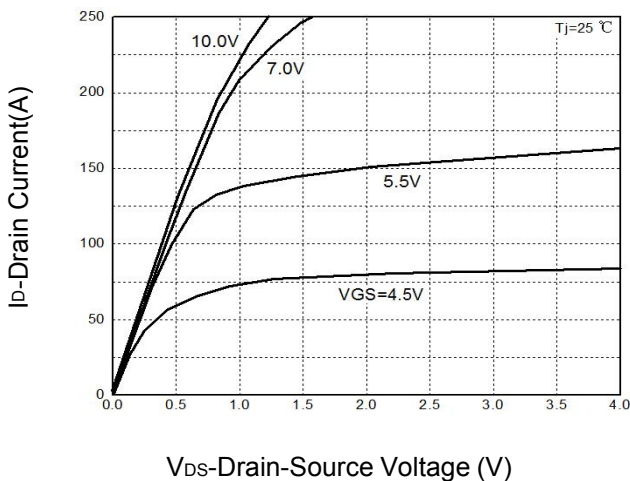
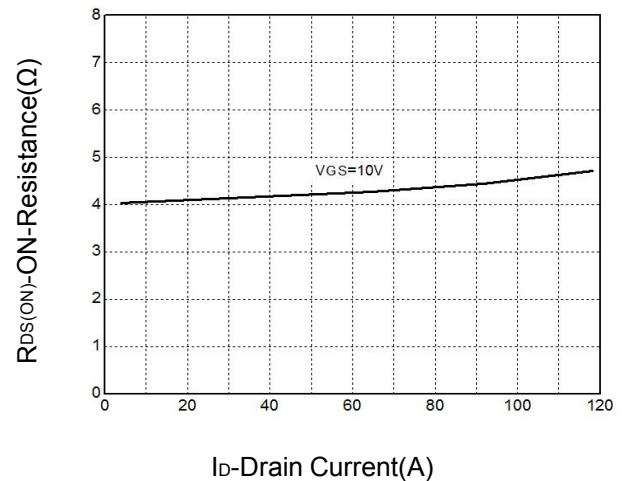


Figure 6: Drain-Source On Resistance



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

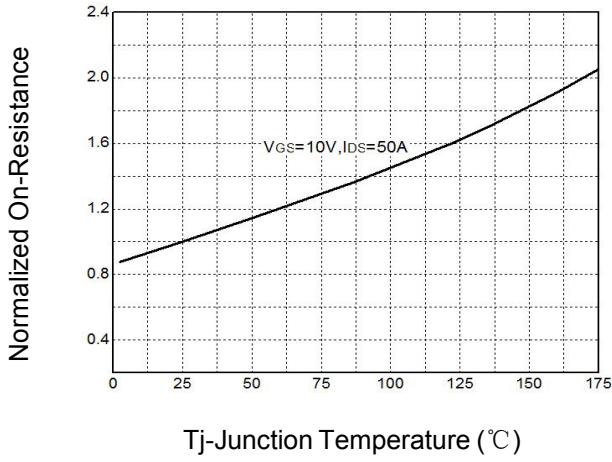


Figure 8: Source-Drain Diode Forward

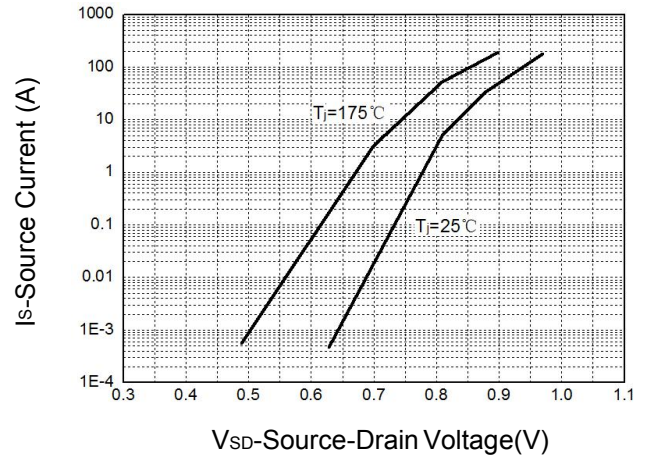


Figure 9: Capacitance Characteristics

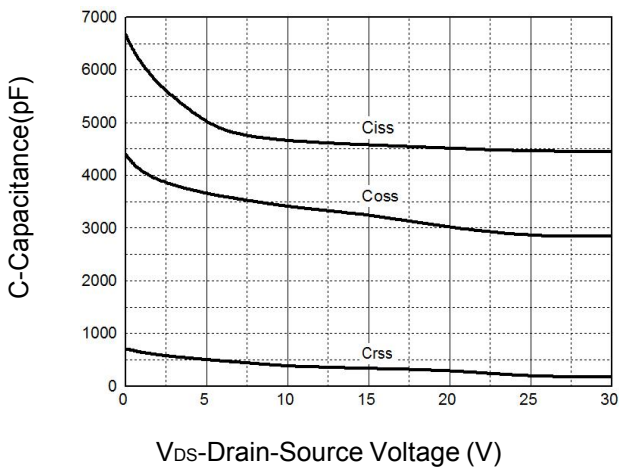
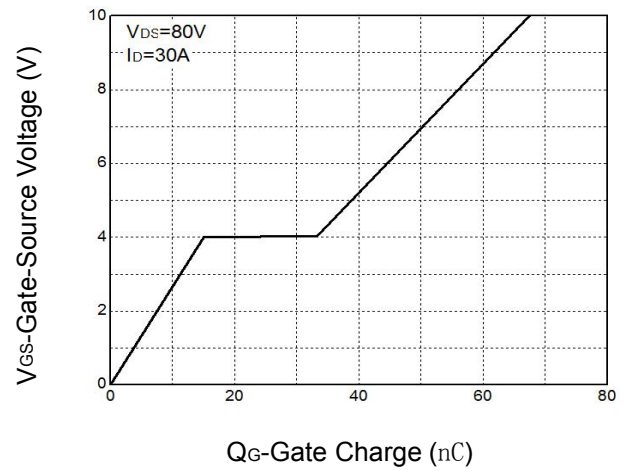
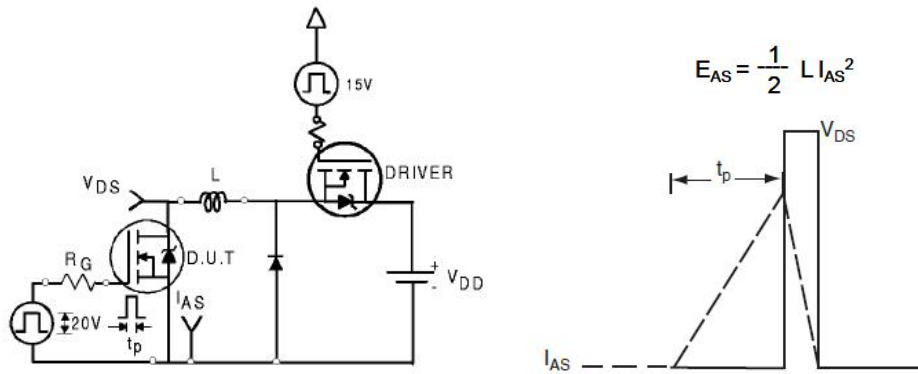


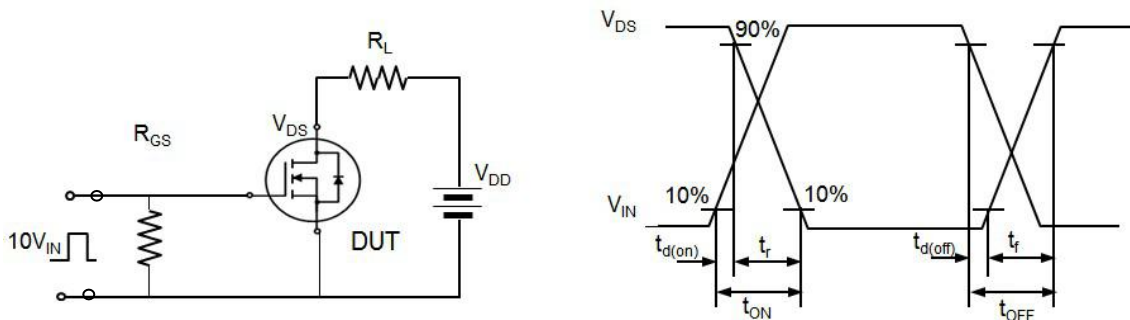
Figure 10: Gate Charge Characteristics



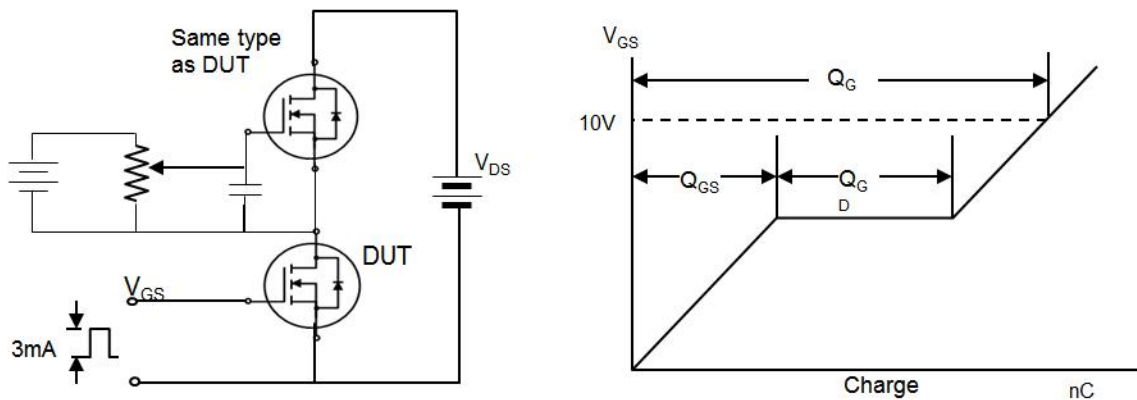
Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



Gate Charge Test Circuit and Waveforms

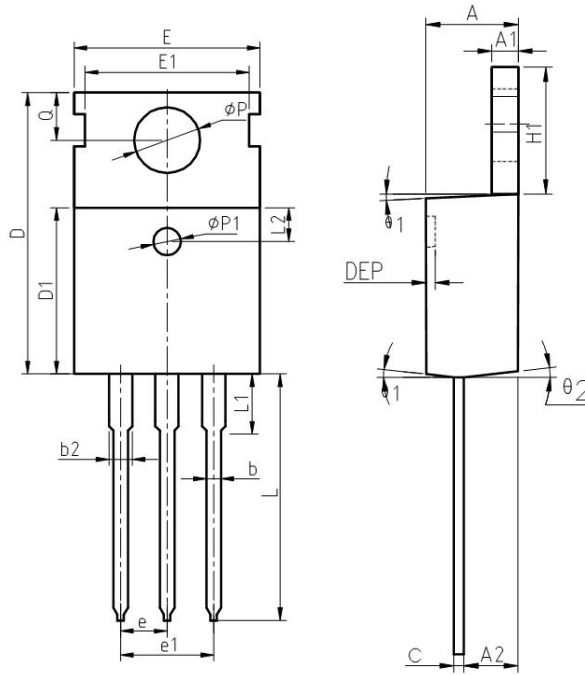


Device Per Unit

Package Type	Unit	Quantity
TO-220FB-3L	Tube	50
TO-263-2L	Tube	50

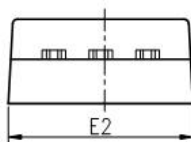
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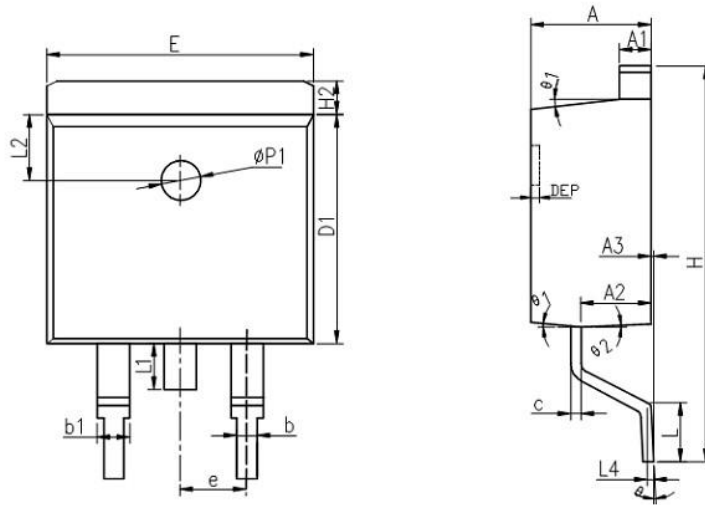
TO-220FB-3L



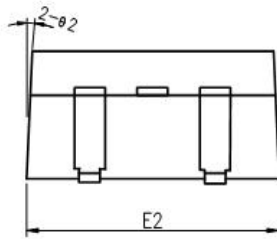
COMMON DIMENSIONS

SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185
A1	1.27	1.30	1.33	0.050	0.051	0.052
A2	2.35	2.40	2.50	0.093	0.094	0.098
b	0.77	0.80	0.90	0.030	0.031	0.035
b2	1.17	1.27	1.36	0.046	0.050	0.054
c	0.48	0.50	0.56	0.019	0.020	0.022
D	15.40	15.60	15.80	0.606	0.614	0.622
D1	9.00	9.10	9.20	0.354	0.358	0.362
DEP	0.05	0.10	0.20	0.002	0.004	0.008
E	9.80	10.00	10.20	0.386	0.394	0.402
E1	-	8.70	-	-	0.343	-
E2	9.80	10.00	10.20	0.386	0.394	0.402
e		2.54	BSC		0.100	BSC
e1		5.08	BSC		0.200	BSC
H1	6.40	6.50	6.60	0.252	0.256	0.260
L	12.75	13.50	13.65	0.502	0.531	0.537
L1	-	3.10	3.30	-	0.122	0.130
L2		2.50	REF		0.098	REF
P	3.50	3.60	3.63	0.138	0.142	0.143
P1	3.50	3.60	3.63	0.138	0.142	0.143
Q	2.73	2.80	2.87	0.107	0.110	0.113
θ 1	5°	7°	9°	5°	7°	9°
θ 2	1°	3°	5°	1°	3°	5°
θ 3	1°	3°	5°	1°	3°	5°



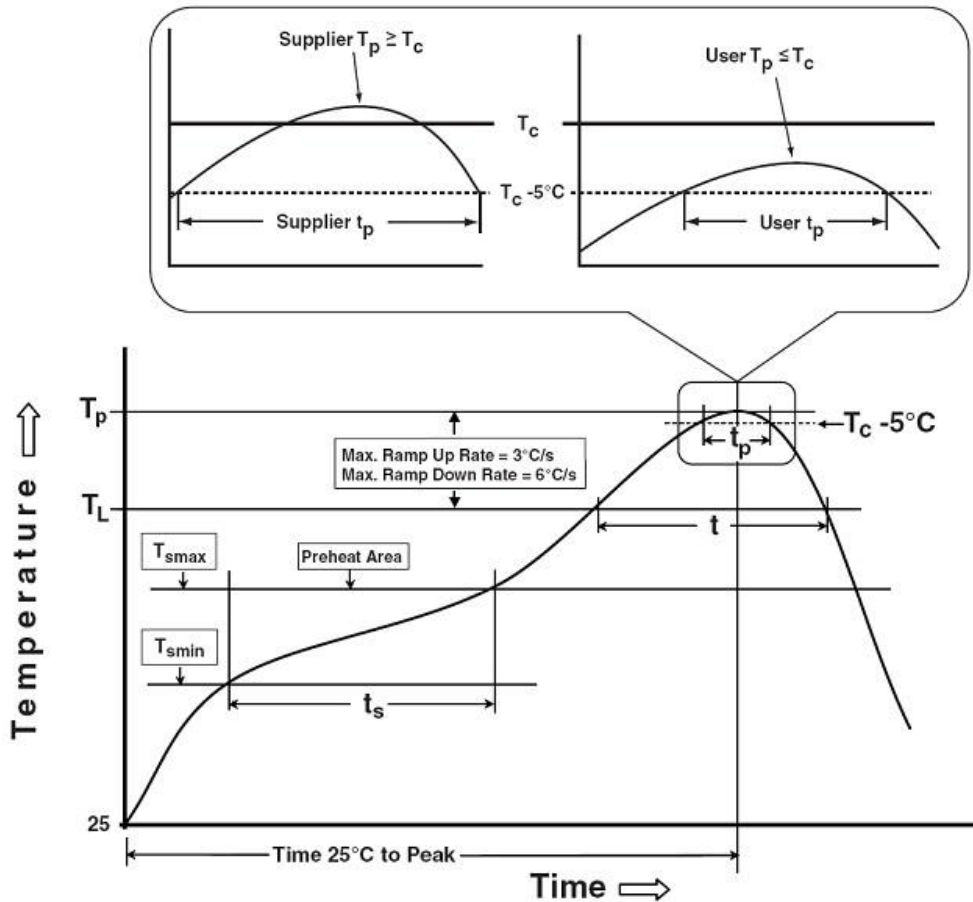


COMMON DIMENSIONS



SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185
A1	1.22	1.27	1.32	0.048	0.050	0.052
A2	2.59	2.69	2.79	0.102	0.106	0.110
A3	0.00	0.10	0.20	0.000	0.004	0.008
b	0.77	0.813	0.90	0.030	0.032	0.035
b1	1.20	1.270	1.36	0.047	0.050	0.054
c	0.34	0.381	0.47	0.013	0.015	0.019
D1	8.60	8.70	8.80	0.339	0.343	0.346
E	10.00	10.16	10.26	0.394	0.400	0.404
E2	10.00	10.10	10.20	0.394	0.398	0.402
e	2.54 BSC			0.100 BSC		
H	14.70	15.10	15.50	0.579	0.594	0.610
H2	1.17	1.27	1.40	0.046	0.050	0.055
L	2.00	2.30	2.60	0.079	0.091	0.102
L1	1.45	1.55	1.70	0.057	0.061	0.067
L2	2.50 REF			0.098 REF		
L4	0.25 BSC			0.010 BSC		
	0°	5°	8°	0°	5°	8°
1	5°	7°	9°	5°	7°	9°
2	1°	3°	5°	1°	3°	5°
ΦP1	1.40	1.50	1.60	0.055	0.059	0.063
DEP	0.05	0.10	0.20	0.002	0.004	0.008

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.

*Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.

** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ ≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500/1000 Hrs, Bias @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

Customer Service

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