



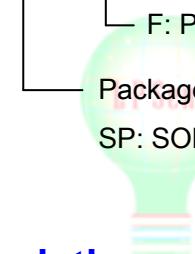
N-Ch and P-Ch Fast Switching MOSFETs

General Description

The LPM9030 is the high performance complementary N-ch and P-ch MOSFETs with high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The LPM9030 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Order Information

LPM9030 □ □ □

 F: Pb-Free
 Package Type
 SP: SOP8

Pin Description

Pin Number	Pin Description
1	Source Of NMOS
2	Gate Of NMOS
3	Source Of PMOS
4	Gate Of PMOS
5,6	Drain Of PMOS
7,8	Drain Of NMOS

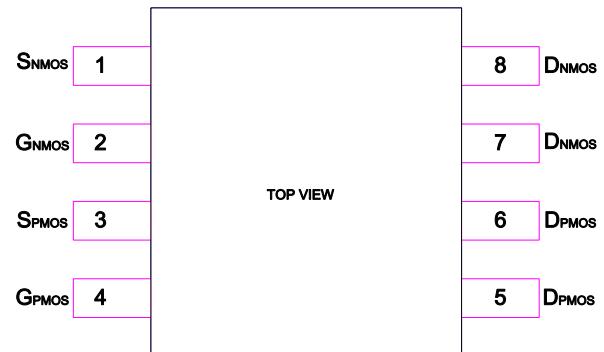
Features

- ◆ 100% EAS Guaranteed
- ◆ Green Device Available
- ◆ Super Low Gate Charge
- ◆ Excellent CdV/dt effect decline
- ◆ Advanced high cell density Trench technology

Applications

- ✧ Driver for Relay, Solenoid, Motor, LED etc.
- ✧ DC-DC converter circuit
- ✧ Power Switch
- ✧ Load Switch
- ✧ Charging

Pin Configurations





Absolute Maximum Ratings

Parameter		Symbol	NMOS	PMOS	Unit
Drain-Source Voltage		V _{DS}	30	-30	V
Gate-Source Voltage		V _{GS}	±20	±20	
Continuous Drain Current	TA=25°C		10	-7.6	A
Maximum Power Dissipation	TA=25°C		2.0	2.0	W
Pulsed Drain Current		I _{DM}	20	-15	A
Single Pulse Avalanche Energy		EAS	22	45	mJ
Avalanche Current		I _{AS}	21	-30	A
Operating Junction Temperature		T _J	-55 to 150	-55 to 150	°C
Lead Temperature		T _L	260	260	°C
Storage Temperature Range		T _{stg}	-55 to 150	-55 to 150	°C

Thermal resistance ratings

Parameter	Symbol	Typ.	Unit
Junction-to-Ambient Thermal Resistance	R _{θJA}	62	°C/W



Electrical Characteristics

N-Channel MOSFET Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted) :

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	30	---	---	V
$\Delta \text{BV}_{\text{DSS}/\Delta T_J}$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $\text{I}_D=1\text{mA}$	---	0.023	---	$^\circ\text{C}$
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=10\text{A}$	---	---	18	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=5\text{A}$	---	---	28	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D = 250\mu\text{A}$	1.0	---	2.5	V
$\Delta \text{V}_{\text{GS(th)}}$	$\text{V}_{\text{GS(th)}}$ Temperature Coefficient		---	-5.2	---	$\text{mV}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=24\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$\text{V}_{\text{DS}}=24\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=5\text{V}$, $\text{I}_D=10\text{A}$	---	16	---	S
R_g	Gate Resistance	$\text{V}_{\text{DS}}=0\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2.5	5	Ω
Q_g	Total Gate Charge (4.5V)	$\text{V}_{\text{DS}}=20\text{V}$, $\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=10\text{A}$	---	7.2	---	nC
Q_{gs}	Gate-Source Charge		---	1.4	---	
Q_{gd}	Gate-Drain Charge		---	2.2	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=15\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_g=3.3$, $\text{I}_D=5\text{A}$	---	4.1	---	ns
T_r	Rise Time		---	9.8	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	15.5	---	
T_f	Fall Time		---	6.0	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=15\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	572	---	pF
C_{oss}	Output Capacitance		---	81	---	
C_{rss}	Reverse Transfer Capacitance		---	65	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	10	A
I_{SM}	Pulsed Source Current		---	---	20	A
V_{SD}	Diode Forward Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

P-Channel MOSFET Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted) :

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	---	-0.021	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-7\text{A}$	---	---	30	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-5\text{A}$	---	---	55	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1.0	---	-2.5	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient		---	-4.2	---	$\text{mV}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-7\text{A}$	---	15	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	15	30	Ω
Q_g	Total Gate Charge (-4.5V)	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-7\text{A}$	---	9.8	---	nC
Q_{gs}	Gate-Source Charge		---	2.2	---	
Q_{gd}	Gate-Drain Charge		---	3.4	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=-15\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_{\text{G}}=3.3\text{ }\Omega$, $I_{\text{D}}=-5\text{A}$	---	16.4	---	ns
T_r	Rise Time		---	20.2	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	55	---	
T_f	Fall Time		---	10	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	930	---	pF
C_{oss}	Output Capacitance		---	148	---	
C_{rss}	Reverse Transfer Capacitance		---	115	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-7.6	A
I_{SM}	Pulsed Source Current		---	---	-15	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{s}}=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V



Typical Characteristics

N-Channel :

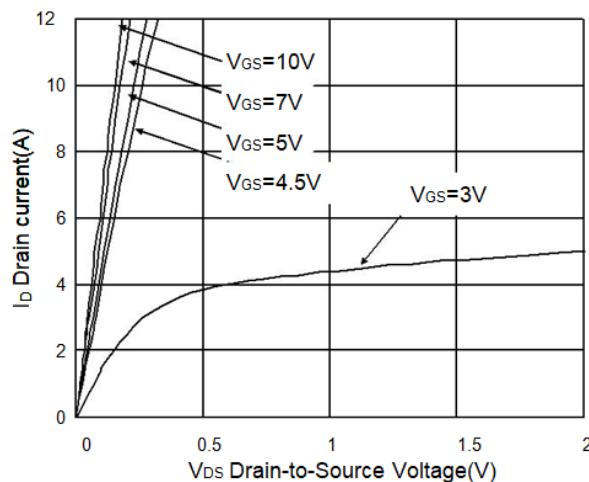


Fig.1 Typical Output Characteristics

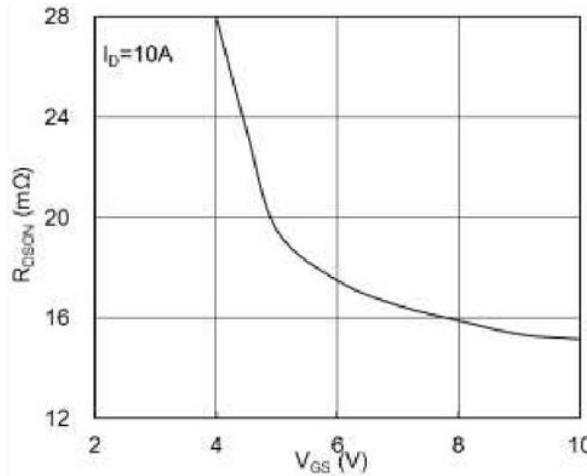


Fig.2 On-Resistance vs Gate-Source Voltage

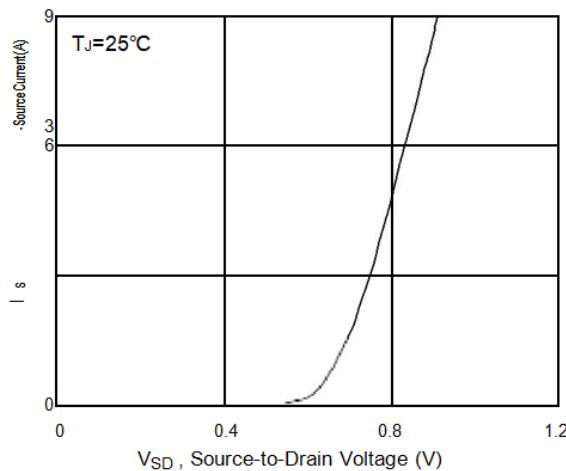


Fig.3 Forward Characteristics of Reverse

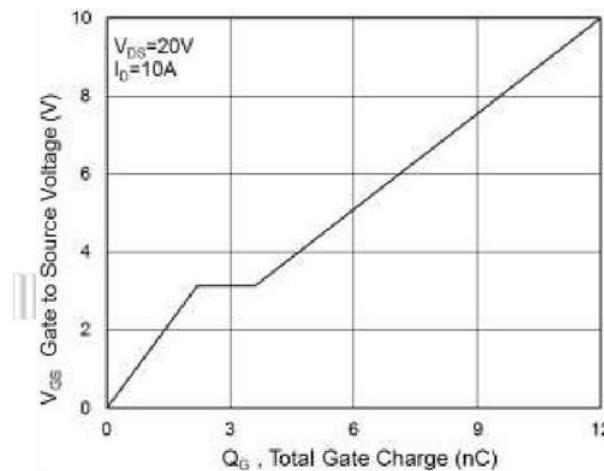


Fig.4 Gate-Charge Characteristics

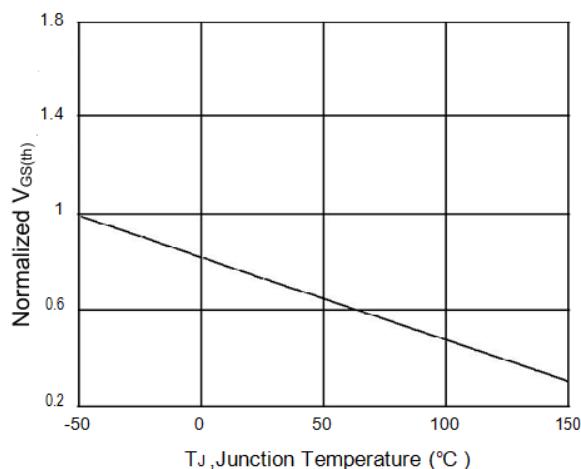


Fig.5 Normalized $V_{GS(th)}$ vs T_J

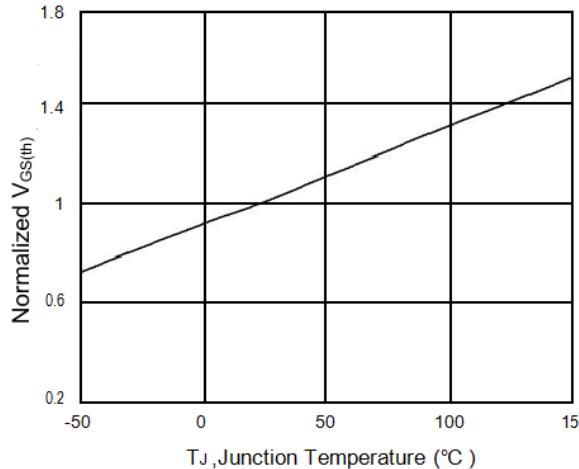


Fig.6 Normalized $R_{DS(on)}$ vs T_J

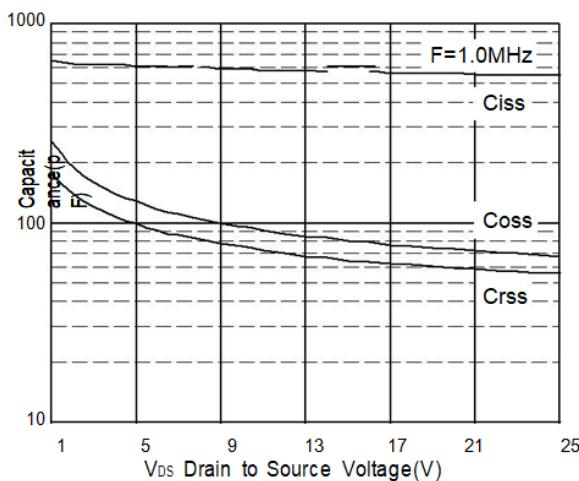


Fig.7 Capacitance

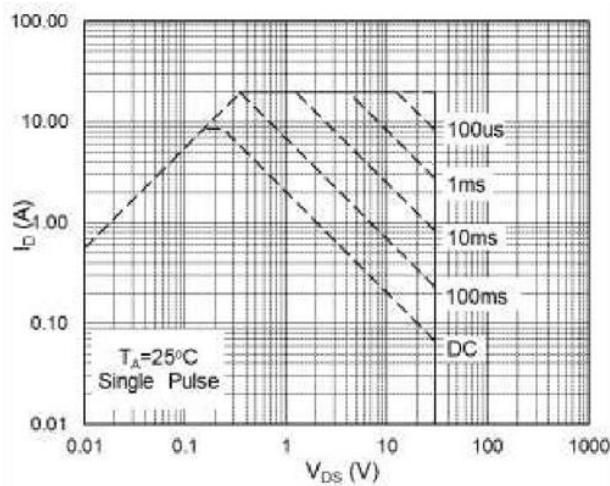


Fig.8 Safe Operating Area

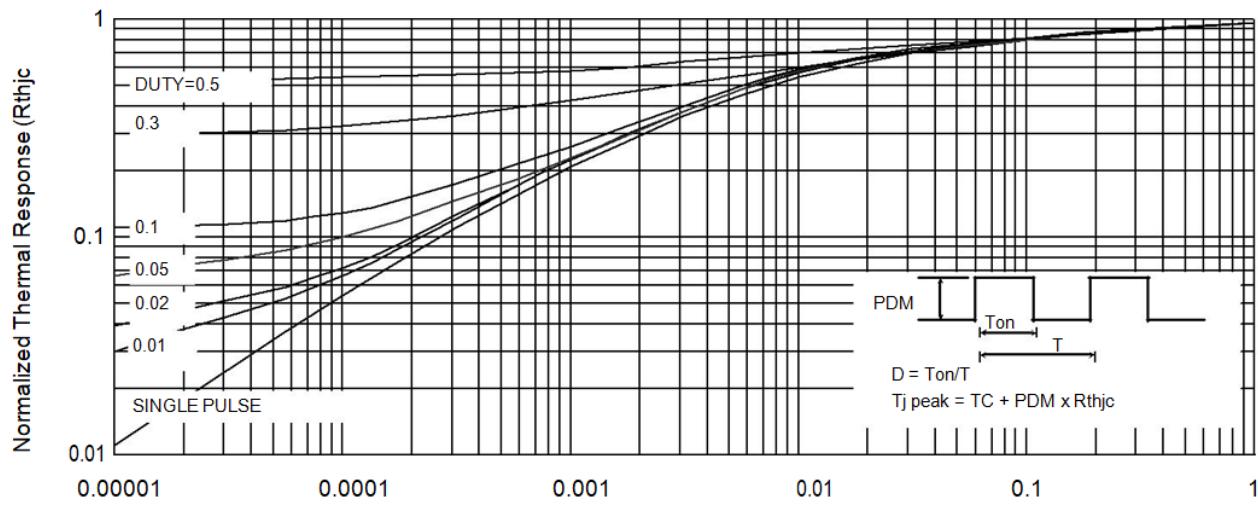


Fig.9 Normalized Maximum Transient Thermal Impedance

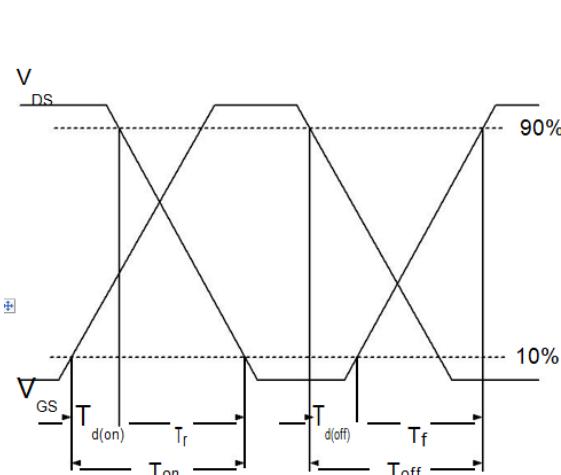


Fig.10 Switching Time Waveform

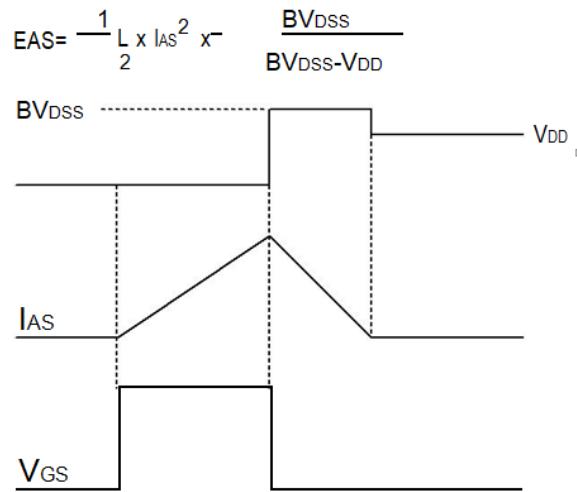


Fig.11 Unclamped Inductive Waveform



P-Channel:

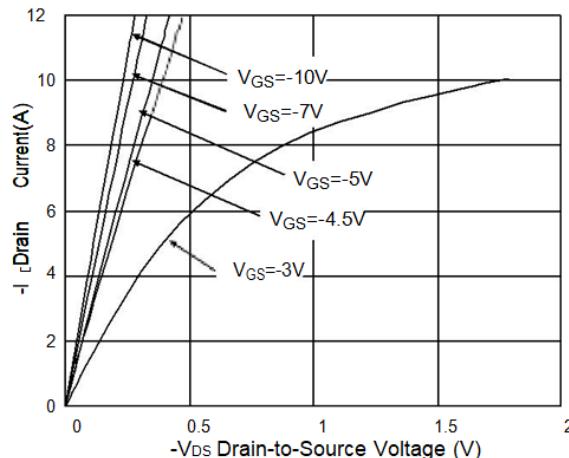


Fig.1 Typical Output Characteristics

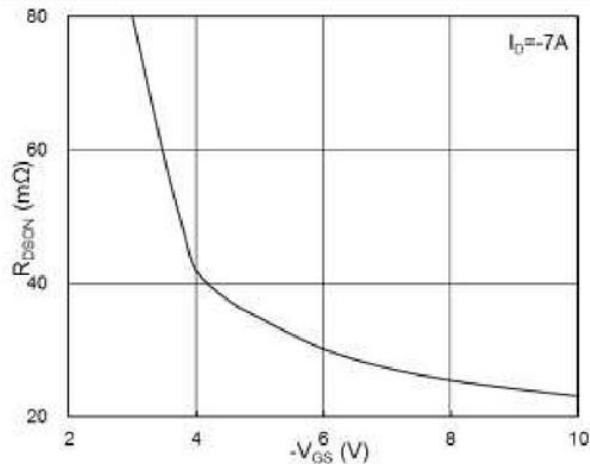


Fig.2 On-Resistance vs Gate-Source Voltage

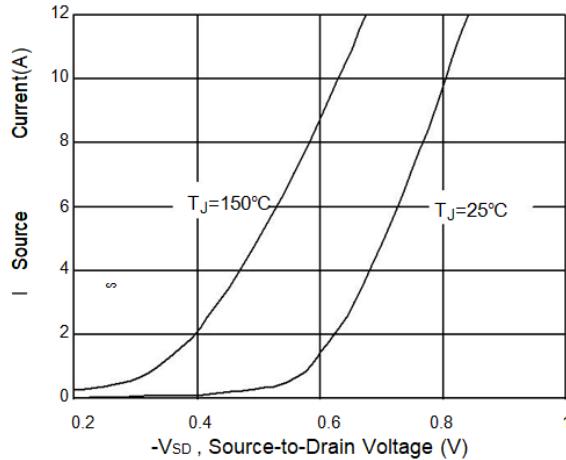


Fig.3 Forward Characteristics of Reverse

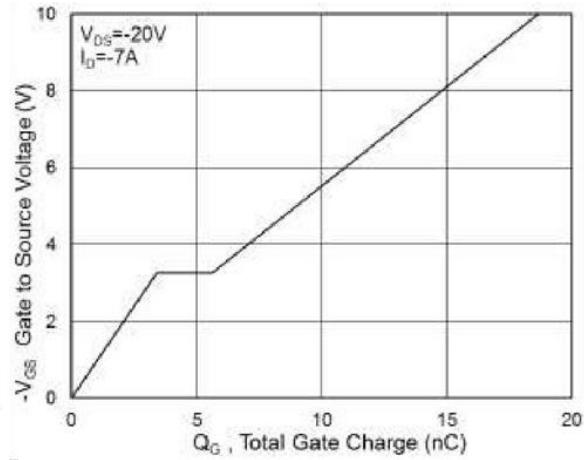


Fig.4 Gate-Charge Characteristics

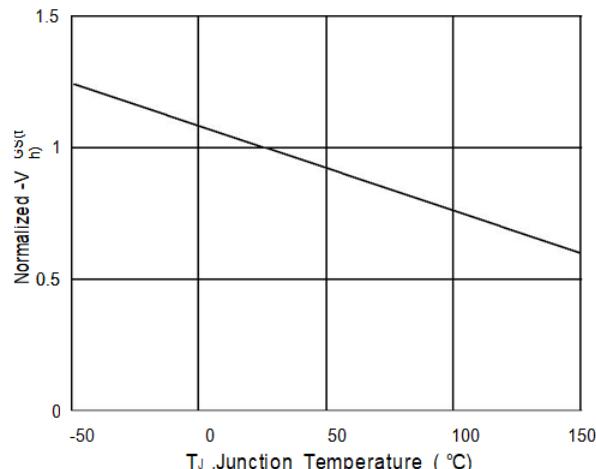


Fig.5 Normalized VGS(th) vs TJ

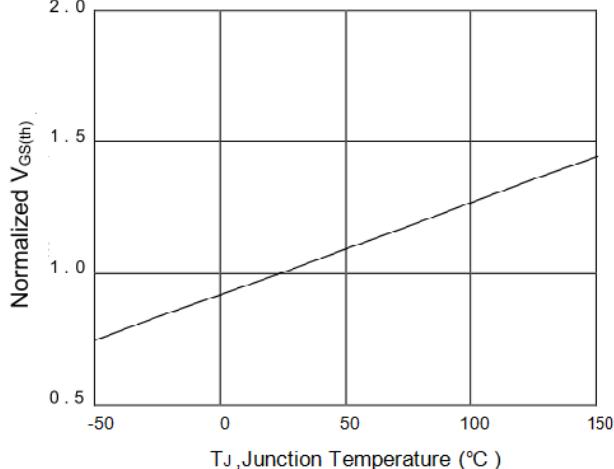


Fig.6 Normalized RDS(on) vs TJ

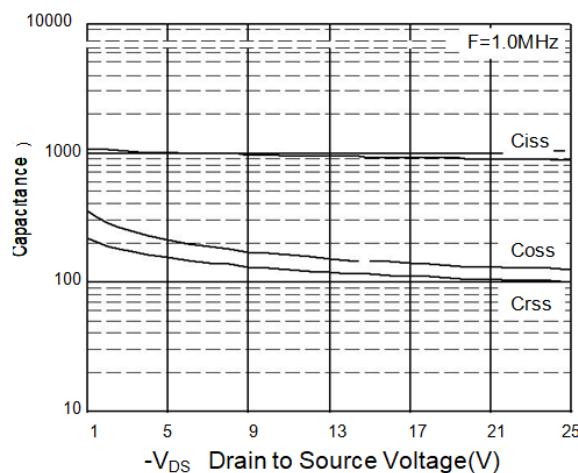


Fig.7 Capacitance

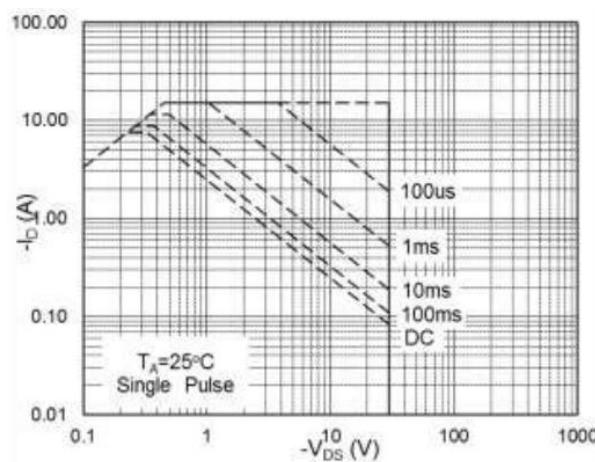


Fig.8 Safe Operating Area

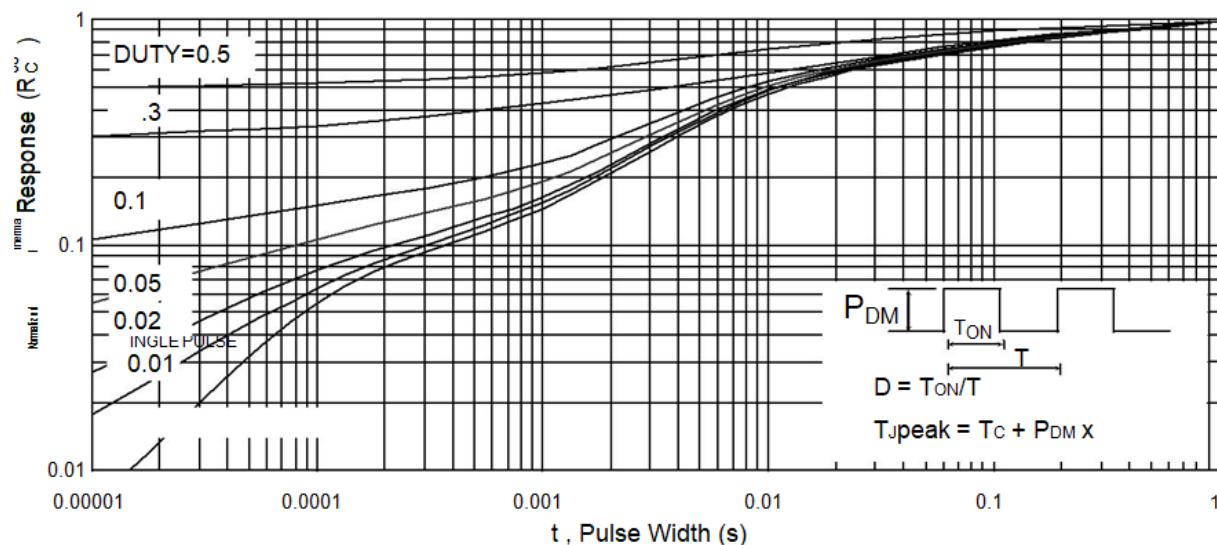


Fig.9 Normalized Maximum Transient Thermal Impedance

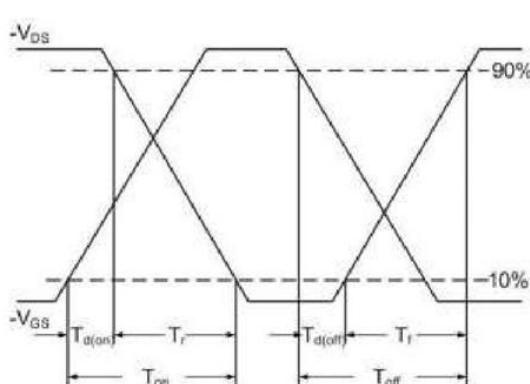


Fig.10 Switching Time Waveform

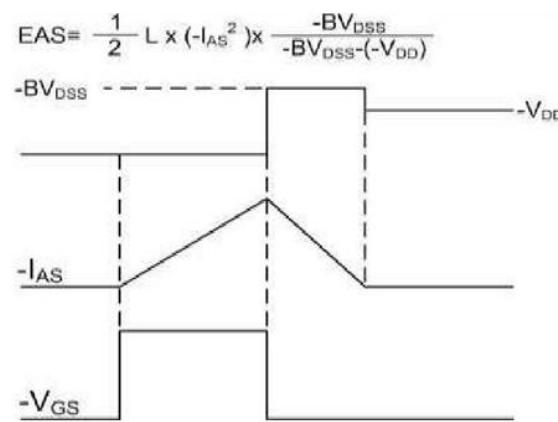
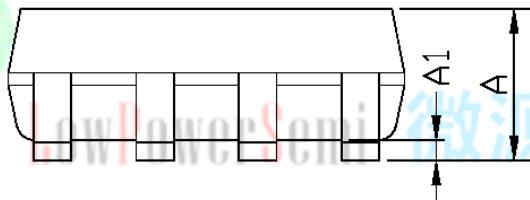
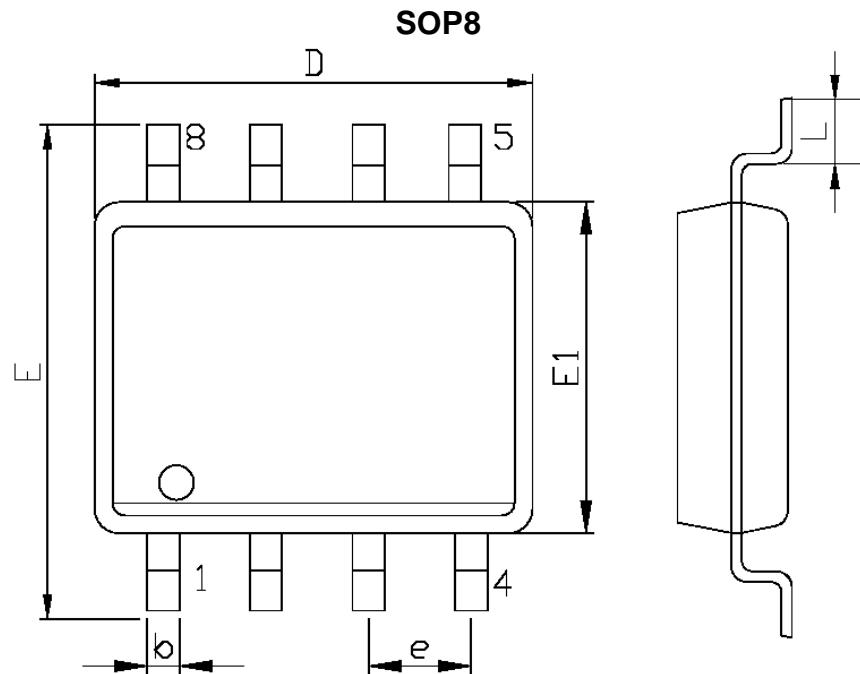


Fig.11 Unclamped Inductive Waveform



Packaging Information



微源半導體

SYMBOLS	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.90		0.193	
E	5.80	6.20	0.228	0.244
E1	3.90		0.153	
L	0.40	1.27	0.016	0.050
b	0.31	0.51	0.012	0.020
e	1.27		0.050	