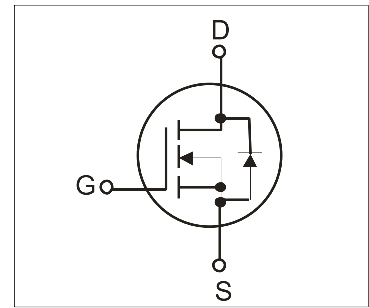
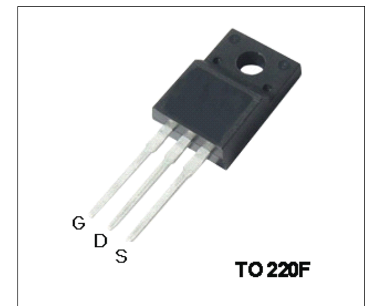
Features

- 2.0A,650V,$R_{DS(on)}$(Max5.0Ω)@V_{GS}=10V
- Ultra-low Gate charge(Typical 8.6nC)
- Fast Switching Capability
- 100%Avalanche Tested
- Maximum Junction Temperature Range(150℃)



General Description

This Power MOSFET is produced using Winsemi's advanced planar stripe,VDMOS technology.this latest technology has been especially designed to minimize on-state resistance, have a high rugged avalanche characteristics .This devices is specially well suited for half bridge and full bridge resonant topology line a electronic lamp ballast, high efficiency switched mode power supplies, active power factor correction.



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DSS}	Drain Source Voltage	650	V
I_D	Continuous Drain Current(@Tc=25℃)	2	A
	Continuous Drain Current(@Tc=100℃)	1.3	A
I_{DM}	Drain Current Pulsed (Note1)	8	A
V_{GS}	Gate to Source Voltage	±30	V
E_{AS}	Single Pulsed Avalanche Energy (Note2)	108	mJ
P_D	Total Power Dissipation(@Tc=25℃)	25	W
	Derating Factor above 25℃	0.20	W/℃
T_J,T_{stg}	Junction and Storage Temperature	-55~150	℃
T_L	Channel Temperature	300	℃

Thermal Characteristics

Symbol	Parameter	Value			Units
		Min	Typ	Max	
R_{QJC}	Thermal Resistance , Junction -to -Case	-	-	5.0	℃/W
R_{QJA}	Thermal Resistance , Junction-to -Ambient	-	-	120	℃/W

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Electrical Characteristics(Tc=25℃)

Characteristics		Symbol	Test Condition	Min	Type	Max	Unit
Gate leakage current		I_{GSS}	V_{GS}=±30V,V_{DS}=0V	-	-	±100	nA
Gate-source breakdown voltage		$V_{(BR)GSS}$	I_G=±10 μA,V_{DS}=0V	±30	-	-	V
Drain Cut -off current		I_{DSS}	V_{DS}=650V,V_{GS}=0V	-	-	1.0	μA
			V_{DS}=500V,Tc=125℃	-	-	100	μA
Drain -source breakdown voltage		$V_{(BR)DSS}$	I_D=250 μA,V_{GS}=0V	650	-	-	V
Gate threshold voltage		$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250 μA	2	-	4	V
Drain -source ON resistance		$R_{DS(ON)}$	V_{GS}=10V,I_D=1.0A	-	4.3	5.0	Ω
Input capacitance		C_{iss}	V_{DS}=25V, V_{GS}=0V, f=1MHz	-	233	-	pF
Reverse transfer capacitance		C_{rss}		-	3.1	-	
Output capacitance		C_{oss}		-	32	-	
Switching time	Turn-on Rise time	tr	V_{DD}=325V, I_D=2.0A R_G=25Ω (Note3,4)	-	23	-	ns
	Turn-on delay time	Td(on)		-	9	-	
	Turn-off Fall time	tf		-	26	-	
	Turn-off delay time	Td(off)		-	23	-	
Total gate charge(gate-source plus gate-drain)		Qg	V_{DD}=520V, V_{GS}=10V, I_D=2.0A (Note3,4)	-	8.6	-	nC
Gate-source charge		Qgs		-	1.7	-	
Gate-drain("miller") Charge		Qgd		-	4.7	-	

Source-Drain Ratings and Characteristics(Ta=25℃)

Characteristics	Symbol	Test Condition	Min	Type	Max	Unit
Continuous drain reverse current	I_{DR}	-	-	-	2.0	A
Pulse drain reverse current	I_{DRP}	-	-	-	8.0	A
Forward voltage(diode)	V_{DSF}	I_{DR}=4.0A,V_{GS}=0V	-	-	1.4	V
Reverse recovery time	trr	I_{DR}=4.0A,V_{GS}=0V,	-	367	-	ns
Reverse recovery charge	Qrr	dI_{DR} /dt =100 A /μs	-	0.96	-	μC

Note 1.Repeativity rating :pulse width limited by junction temperature

2.L=30mH I_{AS}=2.5A,V_{DD}=100V,R_G=25Ω,Starting T_J=25℃

3.Pulse Test:Pulse Width≤300us,Duty Cycle≤2%

4. Essentially independent of operating temperature.

This transistor is an electrostatic sensitive device

Please handle with caution

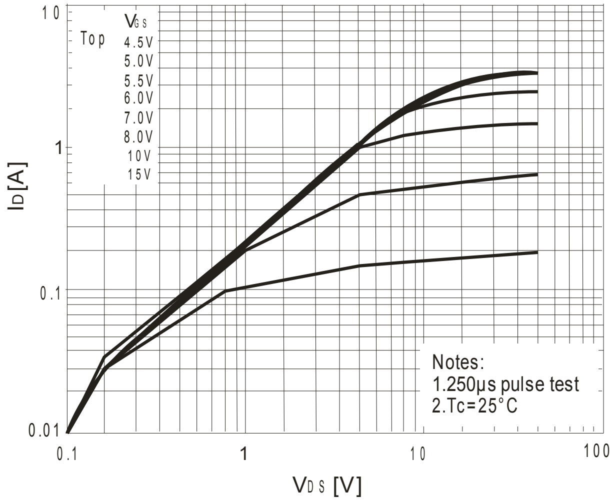


Fig.1 On Region Characteristics

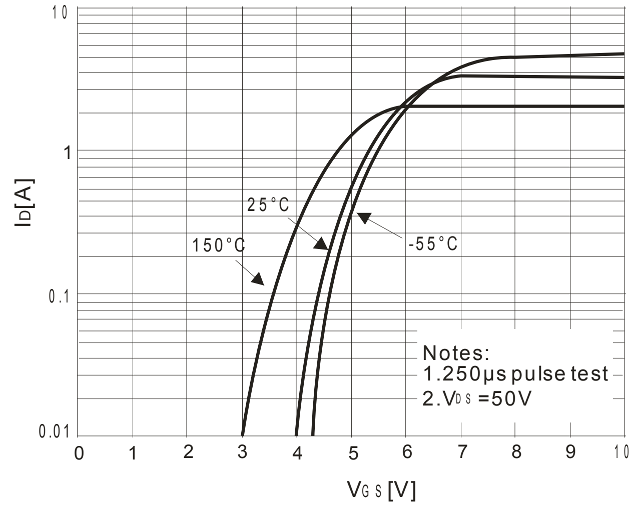


Fig.2 Transfer Characteristics

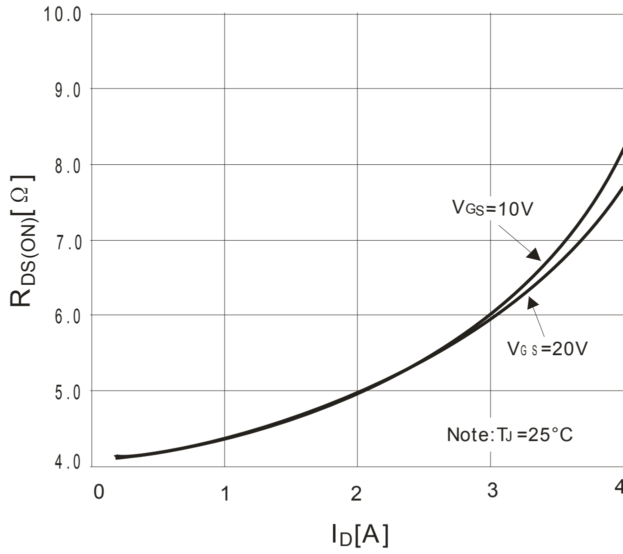


Fig.3 On-Resistance Variation vs Drain Current and Gate Voltage

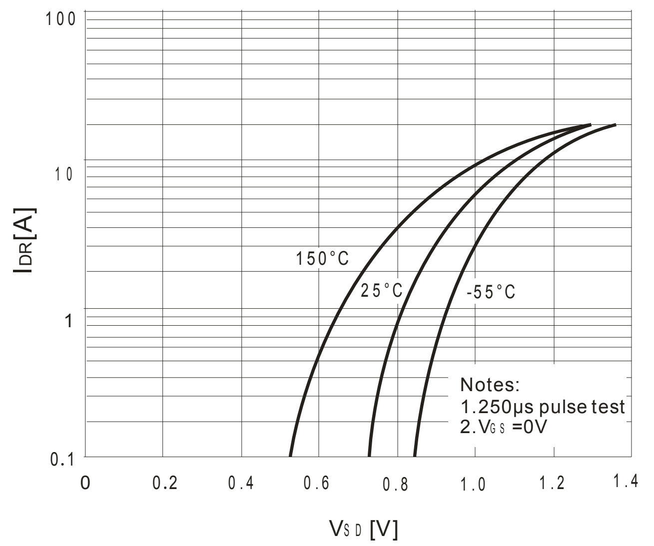


Fig.4 Body Diode Forward Voltage Variation vs. Source Current and Temperature

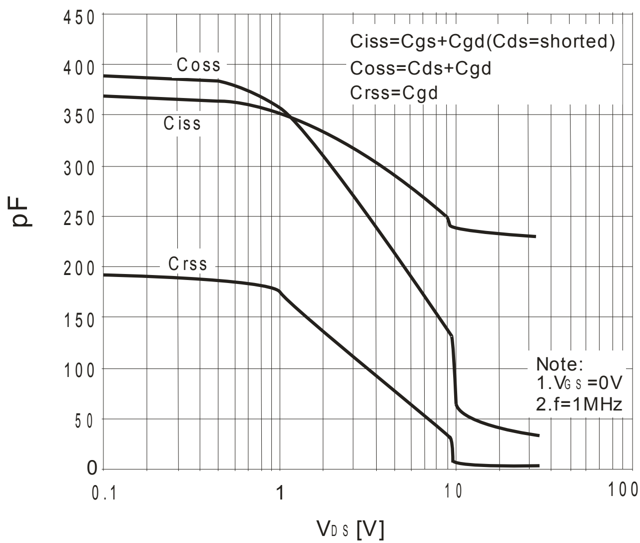


Fig.5 Capacitance Characteristics

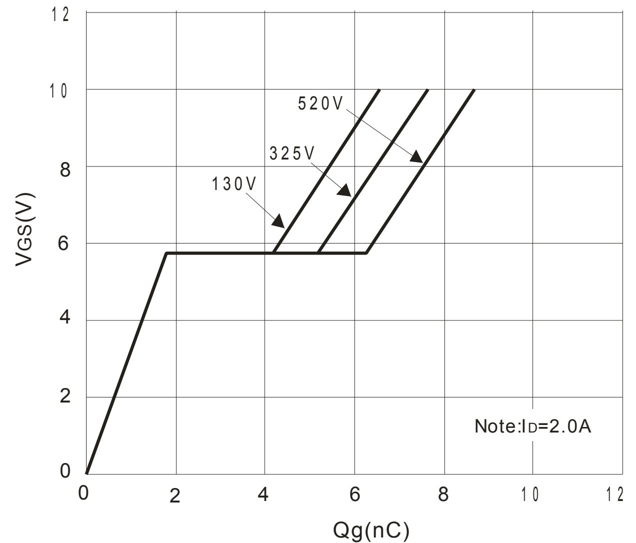


Fig.6 Gate Charge Characteristics

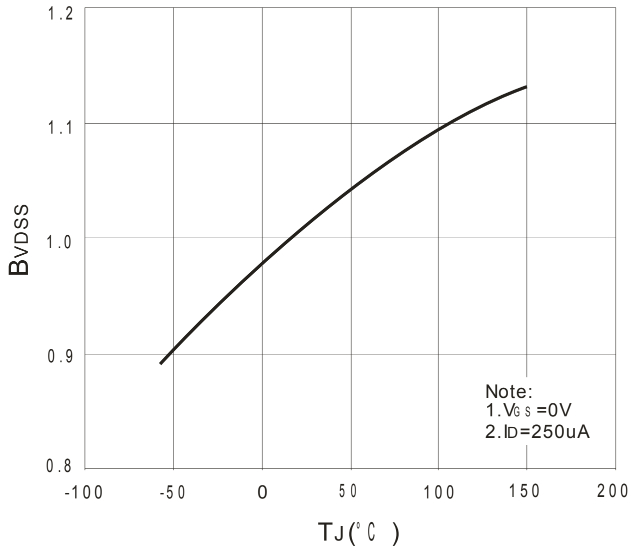


Fig.7 Breakdown Voltage Variation vs. Temperature

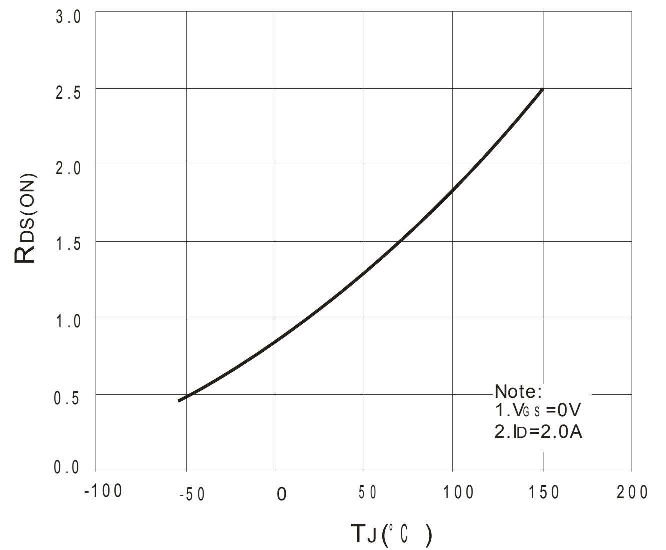


Fig.8 On-Resistance Variation vs. Temperature

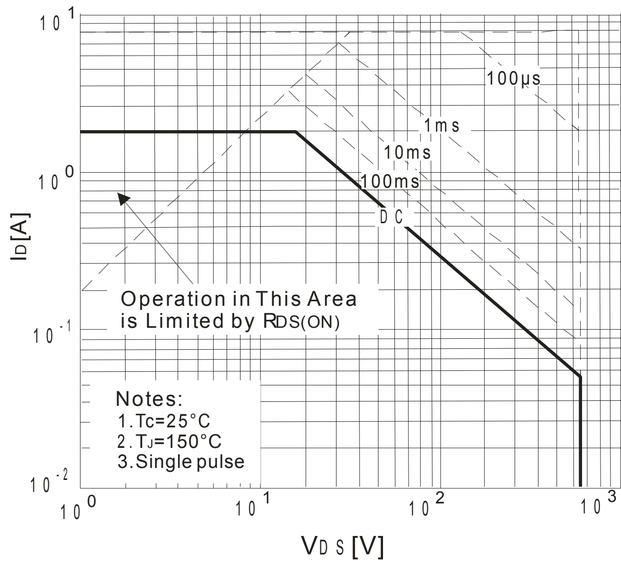


Fig.9 Maximum Safe Operation Area

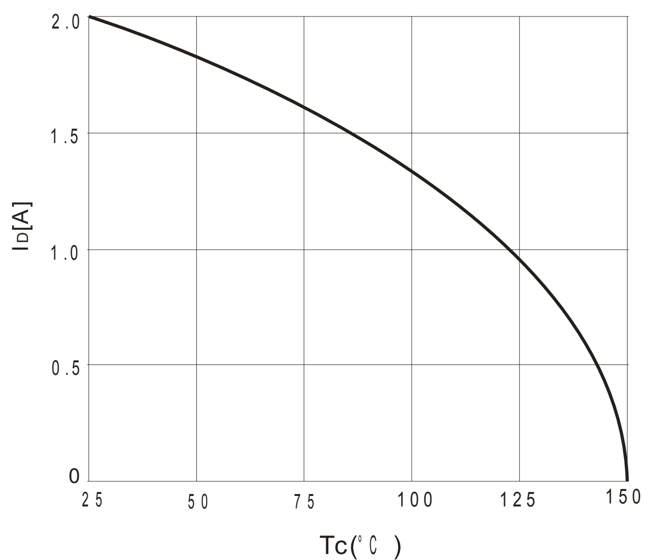


Fig.10 Maximum Drain Current vs Case temperature

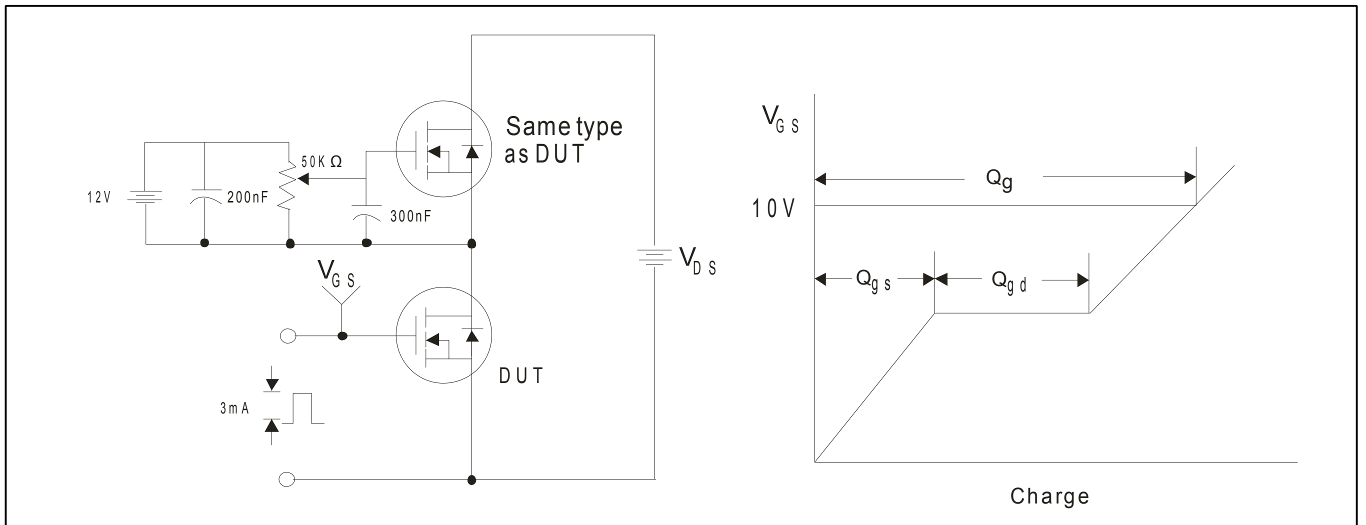


Fig.11 Gate Test circuit & Waveform

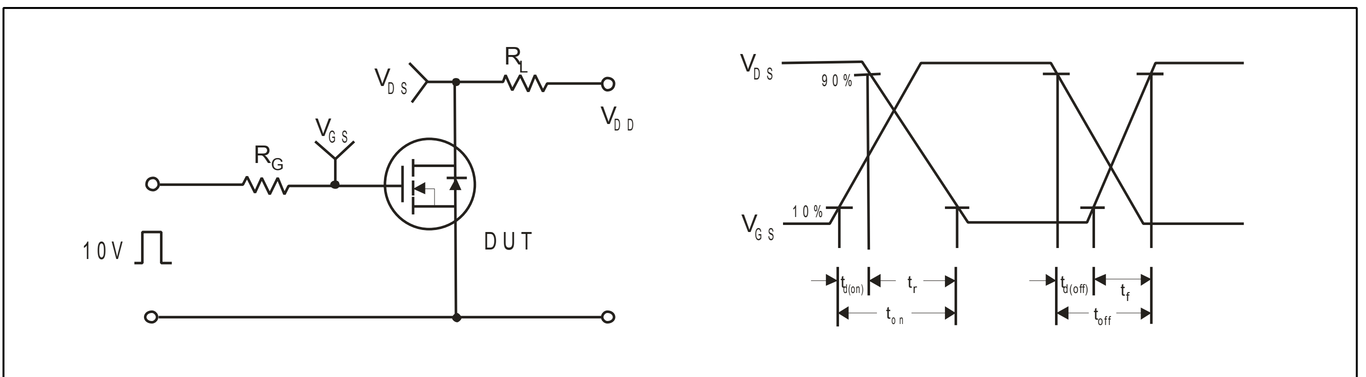


Fig.12 Resistive Switching Test Circuit & Waveform

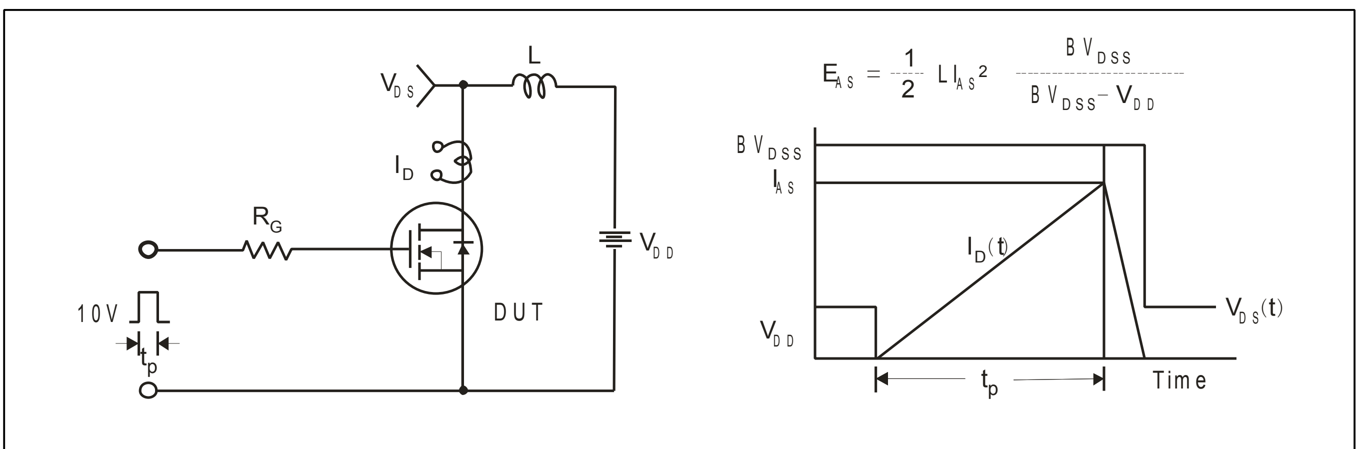


Fig.13 Unclamped Inductive Switching Test Circuit & Waveform

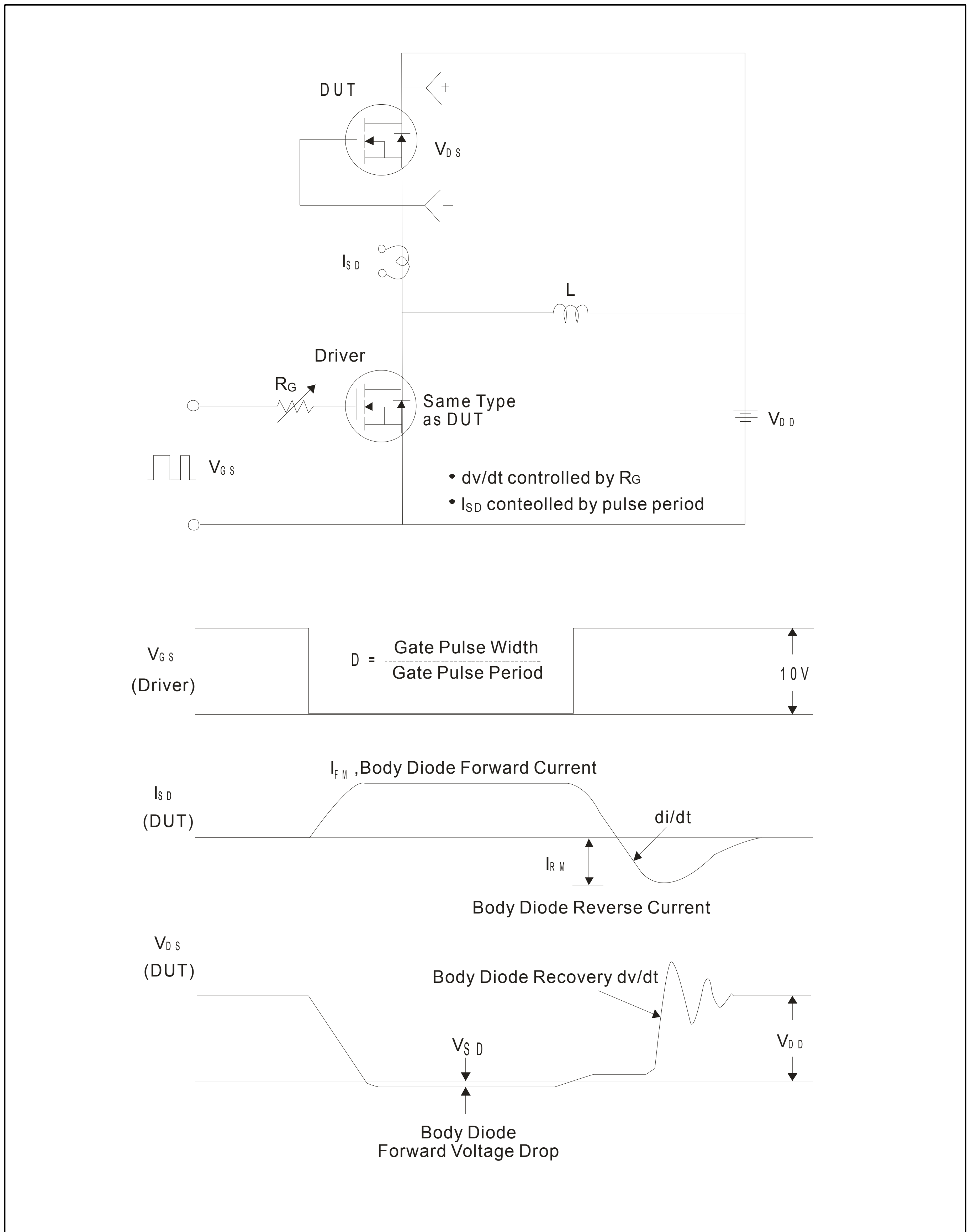
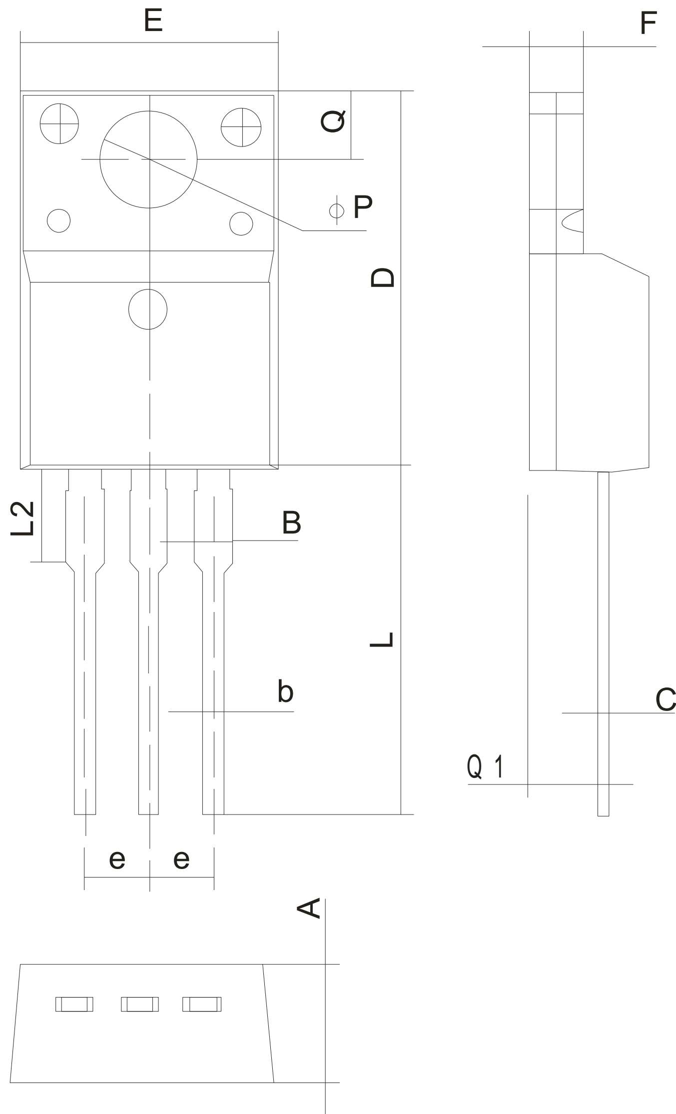


Fig.14 Peak Diode Recovery dv/dt Test Circuit & Waveform

TO-220F Package Dimension

Unit:mm



符 号 Symbol	MIN	MAX
A	4.5	4.9
B	-	1.47
b	0.7	0.9
c	0.45	0.6
D	15.67	16.07
E	9.96	10.36
e	2.54TYP.	
F	2.34	2.74
L	12.58	13.38
L2	3.13	3.33
ΦP	3.08	3.28
Q	3.2	3.4
Q1	2.56	2.96

NOTE:

1.We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.

2.Please do not exceed the absolute maximum ratings of the device when circuit designing.

3.Winsemi Microelectronics Co., Ltd reserved the right to make changes in this specification sheet and is subject to change without prior notice.

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