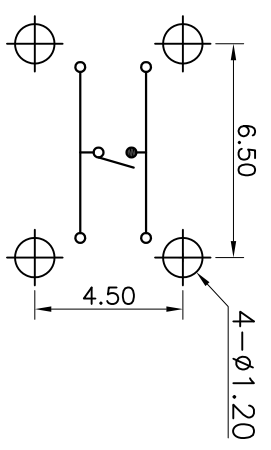
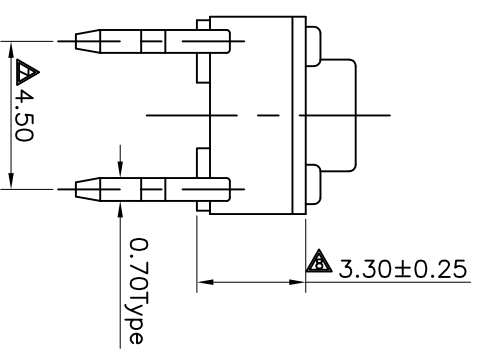
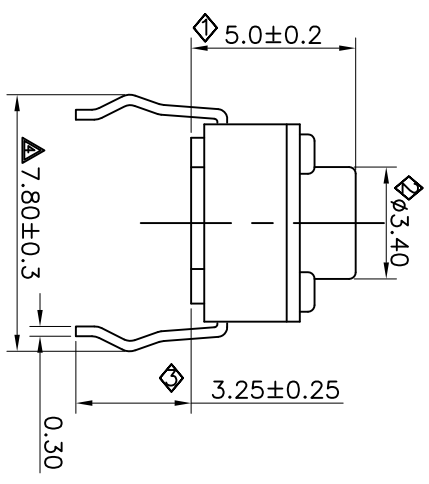
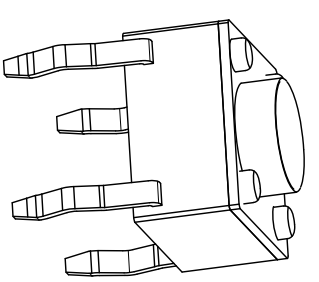
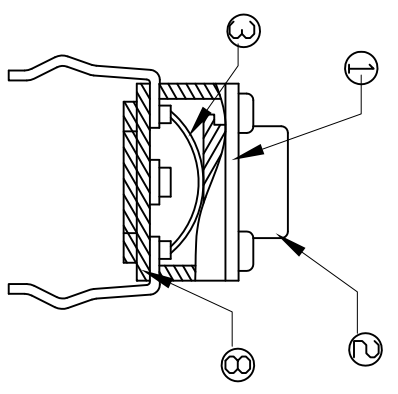
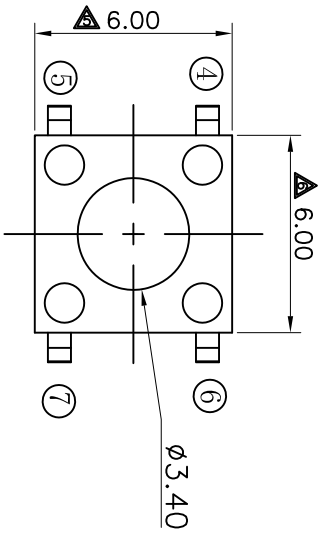
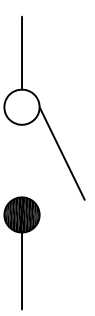


ABIDE BY ROHS



PCB Layout(Pattern Side)

Circuit Diagram



- SPECIFICATIONS
- 1. Rating: DC12V 50mA
 - 2. Contact Resistance: 50mΩ MAX
 - 3. Insulation Resistance: 100MΩ MIN
 - 4. Withstand Voltage: AC 250V for 1 minute
 - 5. Travel: 0.25±0.10mm
 - 6. Operating Force: 250±50gf
 - 7. Life: 100,000 Cycles MIN

ITEM	PART NAME	TERRNO.	QTY.	MATERIAL	FINISHING	REMARK
⑧	Base		1	PA66	Black	
⑦	Terminal	4	1	Brass	Plating Silver	
⑥	Terminal	3	1	Brass	Plating Silver	
⑤	Terminal	2	1	Brass	Plating Silver	
④	Terminal	1	1	Brass	Plating Silver	
③	Contact		1	Copper-silver Composite		
②	Keystoke		1	PA66	Black	
①	Cover		1	Iron	plating Nickel	

APPROVALS		DATE	PART NO.		CTA603201D02
DRAWN	wanwenxue	Nov.28.2005	TITLE:		Jact Switch
CHECKED			PART NO.		
APPROVALS			SCALE: 1:1		PROJ.
TOLERANCES ARE		30±1	±0.30	ANGLE	±2
		10-15±0.10	±0.20	UNIT: mm	DRAWING NO. KHA-TA6032-008EN
		5-10±0.15	±0.10	SCALE: 1:1	SHEET 1 OF 1
		±0.5	±0.10		

东莞市凯华电子有限公司
KAHUA ELECTRONICS CO.,LTD

A

A

B

B

C

C

D

D

5

4

3

2

1

ECN NO.	REV.	DATE.	DESCRIPTION.	CHANGE.	CHECK.	APPRO.
	A		NEW			

ECN NO.	REV.	DATE.	DESCRIPTION.	CHANGE.	CHECK.	APPRO.

A