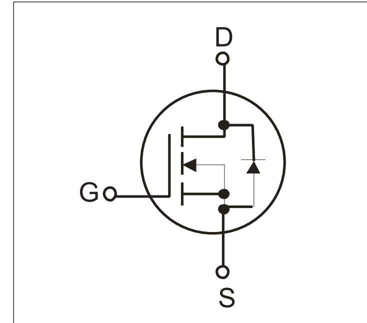
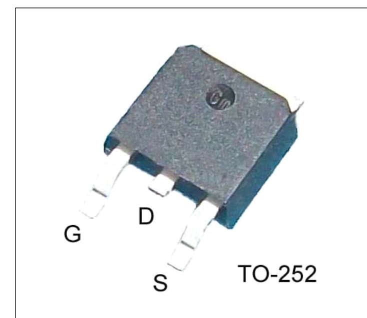
Features

- 7A,650V,$R_{DS(on)}$(TYP:1.0Ω)@V_{GS}=10V
- Ultra-low Gate Charge(Typical 21nC)
- Fast Switching Capability
- 100%Avalanche Tested
- Improved dv/dt capability



General Description

This Power MOSFET is produced using advanced planar stripe,VDMOS technology. This technology has been especially designed to minimize on -state resistance,have a high rugged avalanche characteristics. This devices is specially well suited for high efficiency switch mode power supplies , power factor correction, UPS and a electronic lamp ballast base on half bridge.



Absolute Maximum Ratings

Symbol	Parameter		Value	Units
V_{DSS}	Drain Source Voltage		650	V
I_D	Continuous Drain Current(@Tc=25℃)		7	A
	Continuous Drain Current(@Tc=100℃)		3.2	A
I_{DM}	Drain Current Pulsed	(Note1)	28	A
V_{GS}	Gate to Source Voltage		±30	V
E_{AS}	Single Pulsed Avalanche Energy	(Note2)	232	mJ
E_{AR}	Repetitive Avalanche Energy	(Note1)	12	mJ
I_{AR}	Avalanche Current	(Note1)	7.5	A
dv/dt	Peak Diode Recovery dv /dt	(Note3)	10	V/ ns
P_D	Total Power Dissipation(@Tc=25℃)		40	W
T_J,T_{stg}	Junction and Storage Temperature		-55~150	℃
T_L	Channel Temperature		300	℃

Thermal Characteristics

Symbol	Parameter	Value			Units
		Min	Typ	Max	
$R_{θJC}$	Thermal Resistance , Junction -to -Case	-	-	1.04	℃/W
$R_{θJA}$	Thermal Resistance , Junction-to -Ambient	-	-	62.5	℃/W

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Electrical Characteristics(Tc=25℃)

Characteristics		Symbol	Test Condition	Min	Type	Max	Unit
Gate leakage current		I_{GSS}	V_{GS}=±30V,V_{DS}=0V	-	-	±100	nA
Gate-source breakdown voltage		$V_{(BR)GSS}$	I_G=±10 μA,V_{DS}=0V	±30	-	-	V
Breakdown voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_J$	I_D=250μA,Referenced to 25℃	-	0.8	-	V/℃
Drain cut -off current		I_{DSS}	V_{DS}=650V,V_{GS}=0V	-	-	10	μA
			V_{DS}=520V, T_C=125℃			100	
Drain -source breakdown voltage		$V_{(BR)DSS}$	I_D=250 μA,V_{GS}=0V	650	-	-	V
Gate threshold voltage		$V_{GS(th)}$	V_{DS} = V_{GS},I_D=250 μA	2	-	4	V
Drain -source ON resistance		$R_{DS(ON)}$	V_{GS}=10V,I_D=3.5A	-	1.0	1.45	Ω
Forward Transconductance		gfs	V_{DS}=40V,I_D=7A	-	3	-	S
Input capacitance		C_{iss}	V_{DS}=25V,	-	1052	-	pF
Reverse transfer capacitance		C_{rss}	V_{GS}=0V,	-	12	-	
Output capacitance		C_{oss}	f=1MHz	-	85	-	
Switching time	Turn-On rise time	tr	V_{DD}=300V,	-	34	-	ns
	Turn-On delay time	td(on)	I_D=7A,	-	12	-	
	Turn-Off Fall time	tf	R_G=25Ω,	-	41	-	
	Turn-Off delay time	td(off)	(Note4,5)	-	46	-	
Total gate charge(gate-source plus gate-drain)		Qg	V_{DD}=480V, V_{GS}=10V,	-	21	-	nC
Gate-source charge		Qgs	I_D=7.5A	-	4.9	-	
Gate-drain("miller") Charge		Qgd	(Note4,5)	-	6.6	-	

Source-Drain Ratings and Characteristics(Ta=25℃)

Characteristics	Symbol	Test Condition	Min	Type	Max	Unit
Continuous drain reverse current	I_{DR}	-	-	-	7.5	A
Pulse drain reverse current	I_{DRP}	-	-	-	40	A
Forward voltage(diode)	V_{DSF}	I_{DR}=7.5A,V_{GS}=0V	-	-	1.4	V
Reverse recovery time	trr	I_{DR}=7.5A,V_{GS}=0V,	-	366	-	ns
Reverse recovery charge	Qrr	dI_{DR} / dt =100 A / μs	-	3.5	-	μC

Note 1.Repeativity rating :pulse width limited by junction temperature

2.L=10mH I_{AS}=10A,V_{DD}=50V,R_G=25Ω ,Starting T_J=25℃

3.I_{SD}≤10A,di/dt≤300A/us,V_{DD}<BV_{DSS},STARTING T_J=25℃

4.Pulse Test:Pulse Width≤300us,Duty Cycle≤2%

5. Essentially independent of operating temperature.

This transistor is an electrostatic sensitive device

Please handle with caution

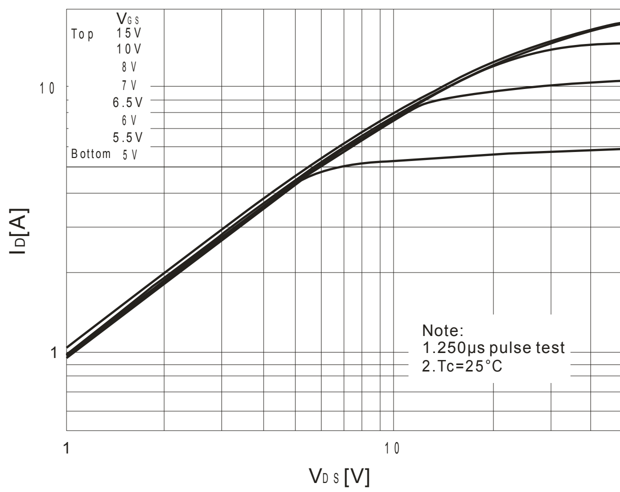


Fig.1 On-Region Characteristics

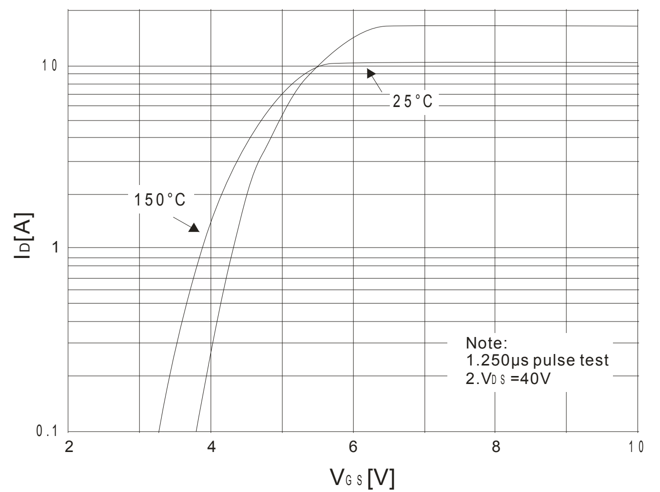


Fig.2 Transfer characteristics

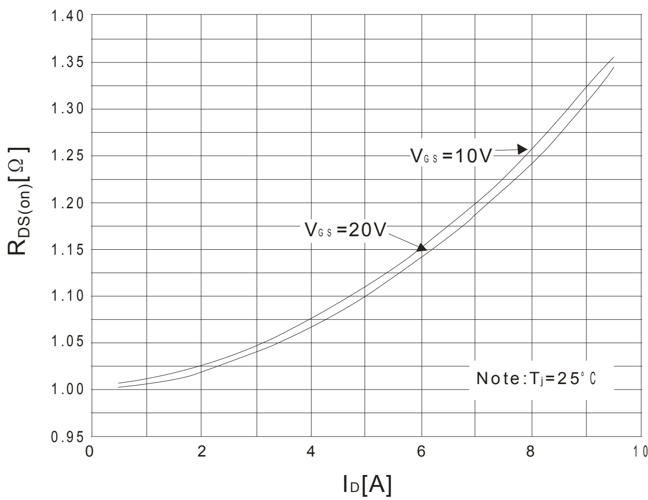


Fig.3 On-Resistance variation vs Drain Current and Gate Voltage

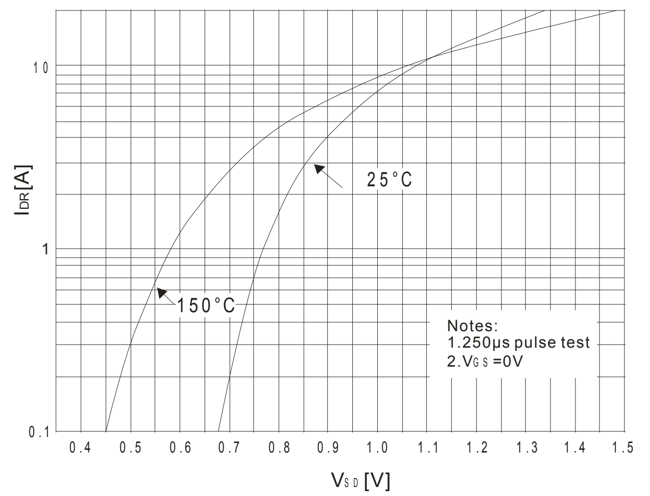


Fig.4 Body Diode Forward Voltage Variation with Source Current and Temperature

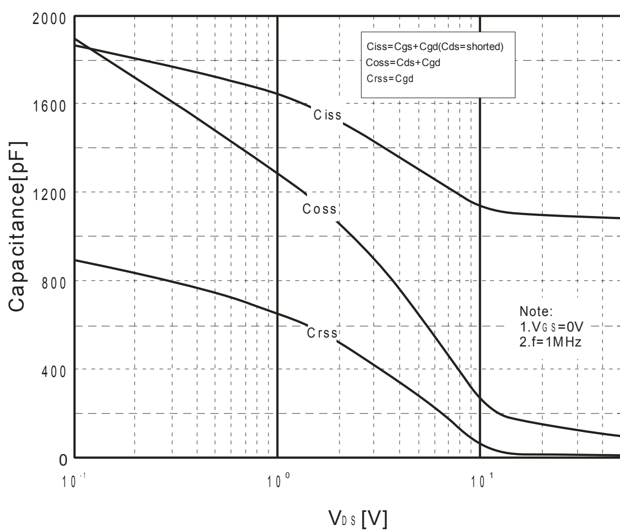


Fig.5 Capacitance Characteristics

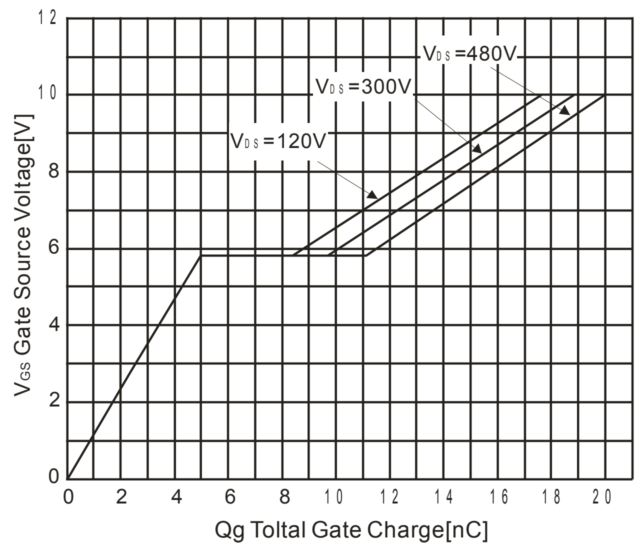


Fig.6 Gate Charge Characteristics

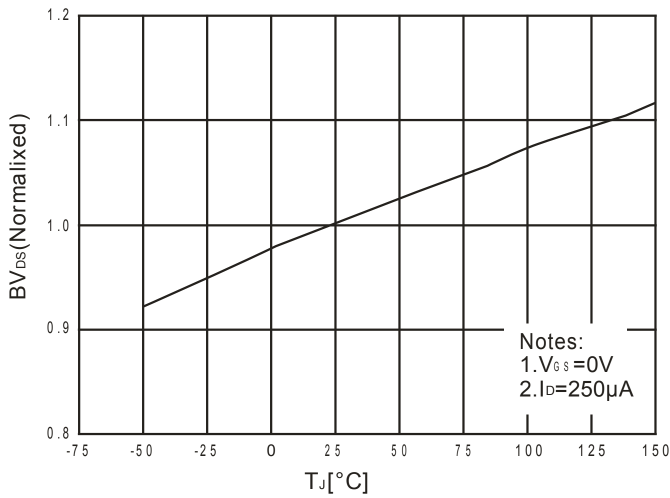


Fig.7 Breakdown Voltage Variation
vs. Temperature

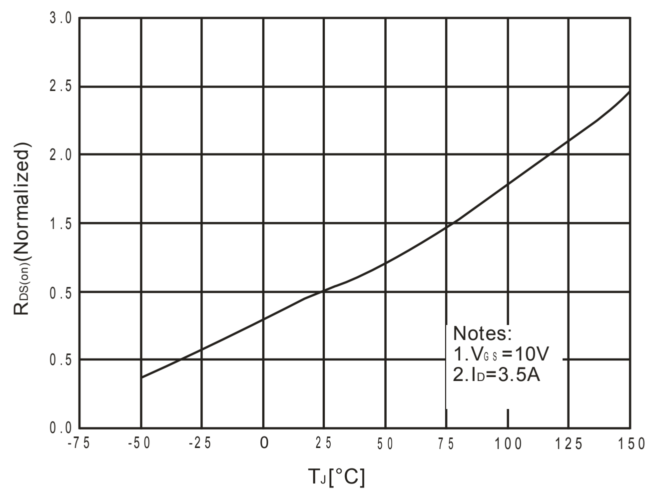


Fig.8 On-Resistance Variation
vs. Temperature

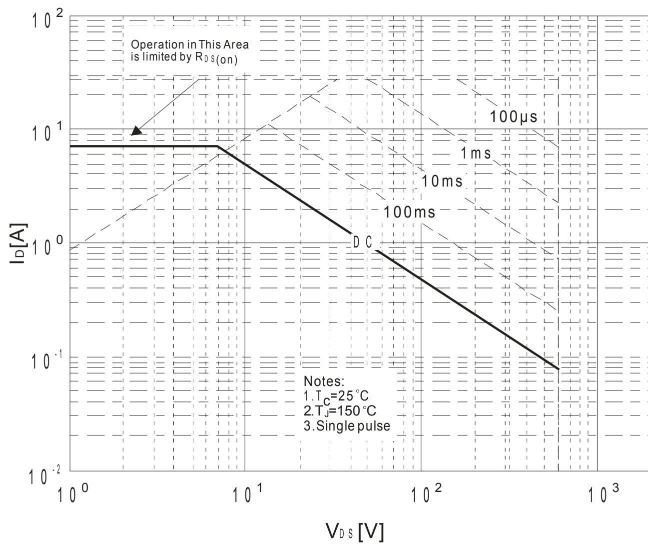


Fig.9 Maximum Safe Operation Area

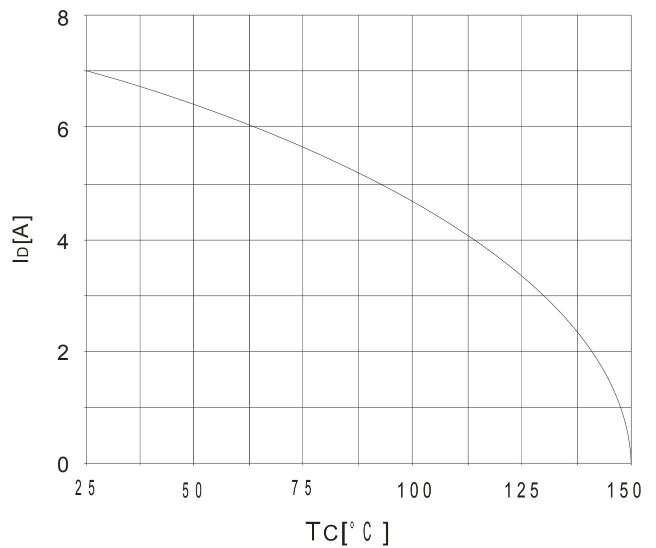


Fig.10 Maximum Drain Current vs
Case Temperature

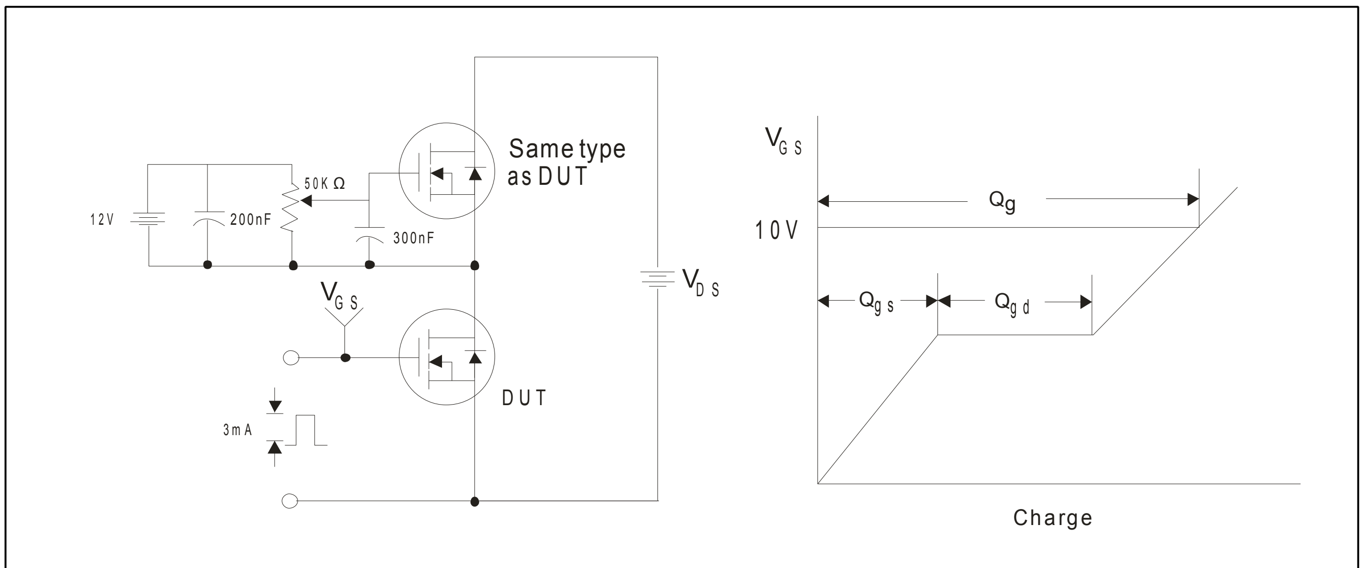


Fig.11 Gate Test circuit & Waveform

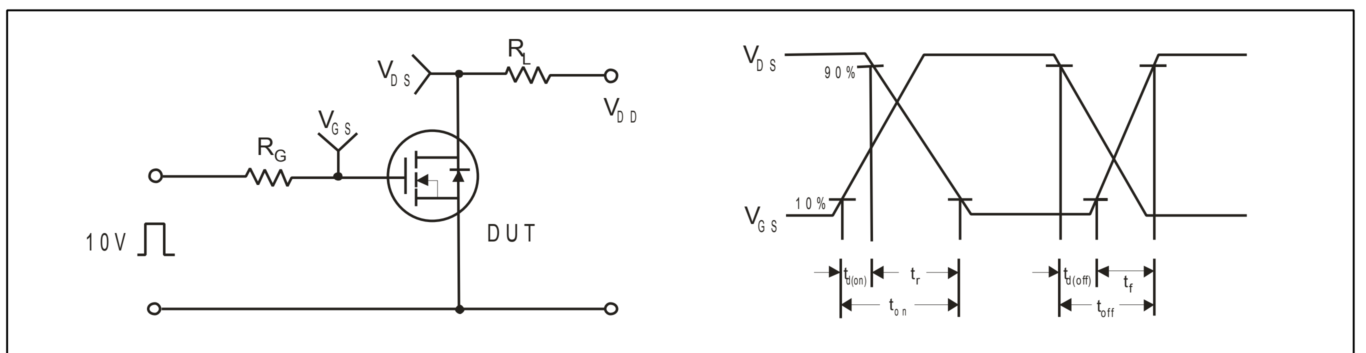


Fig.12 Resistive Switching Test Circuit & Waveform

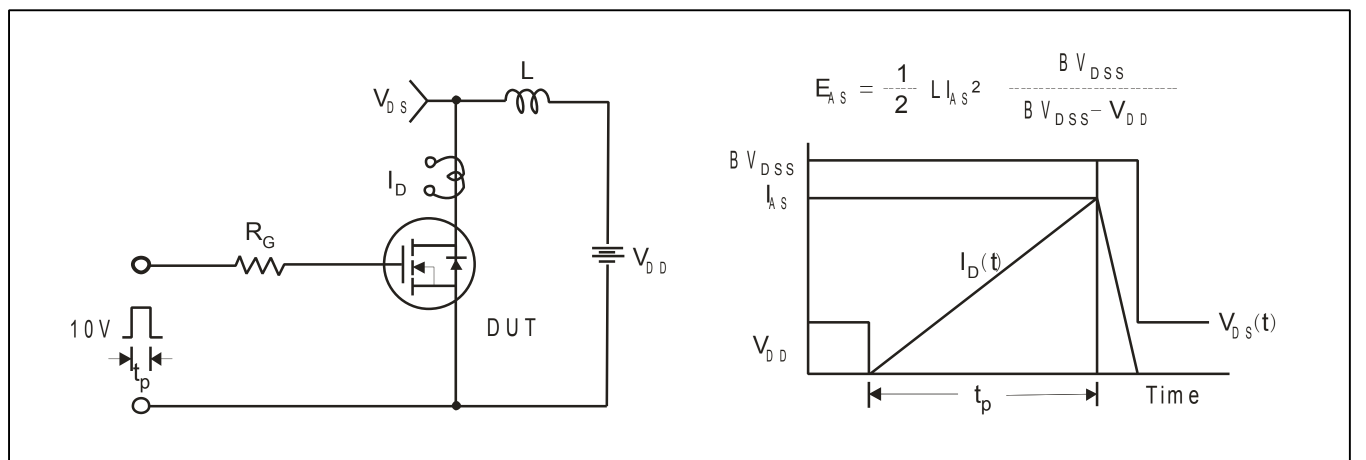


Fig.13 Uncamped Inductive Switching Test Circuit & Waveform

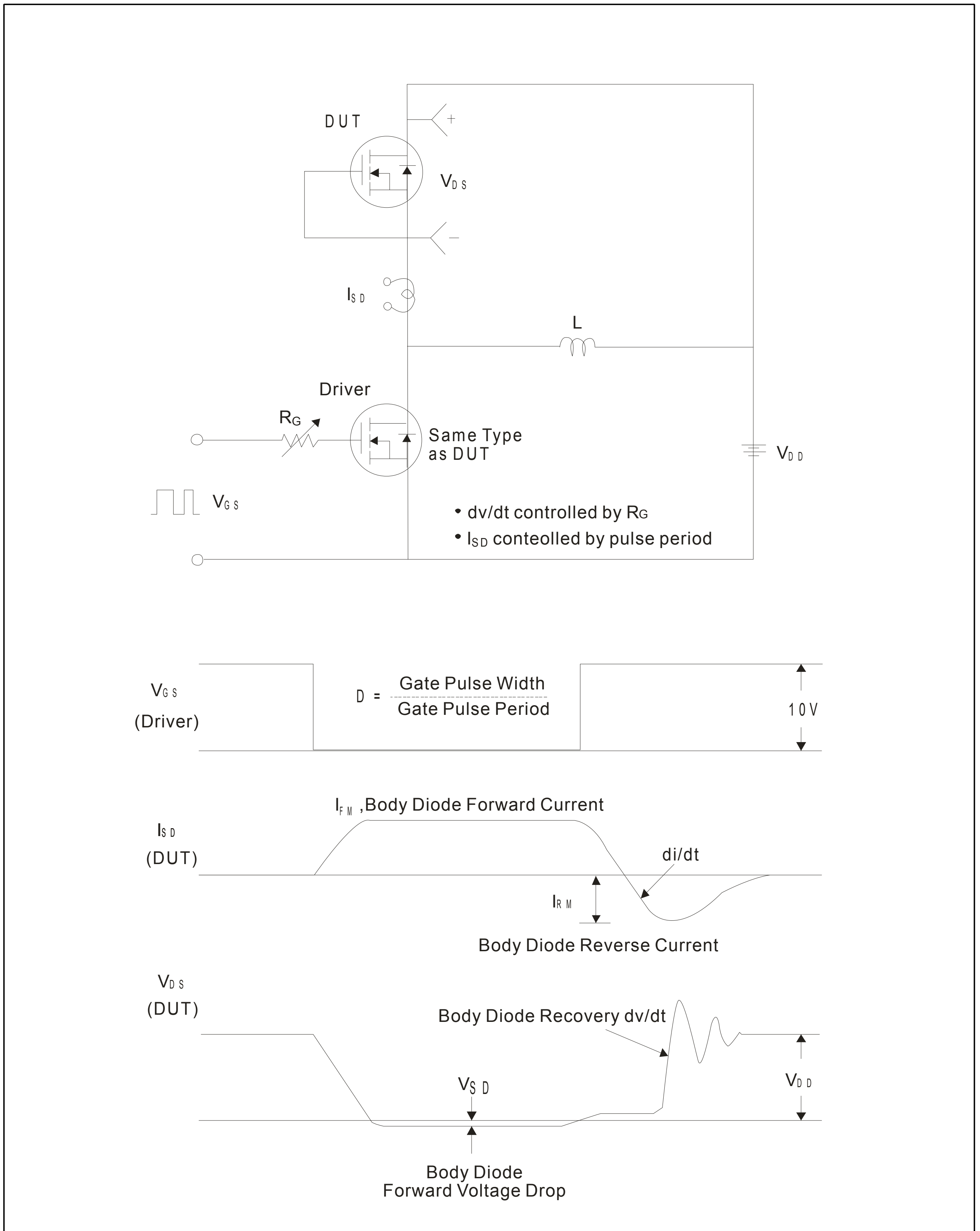
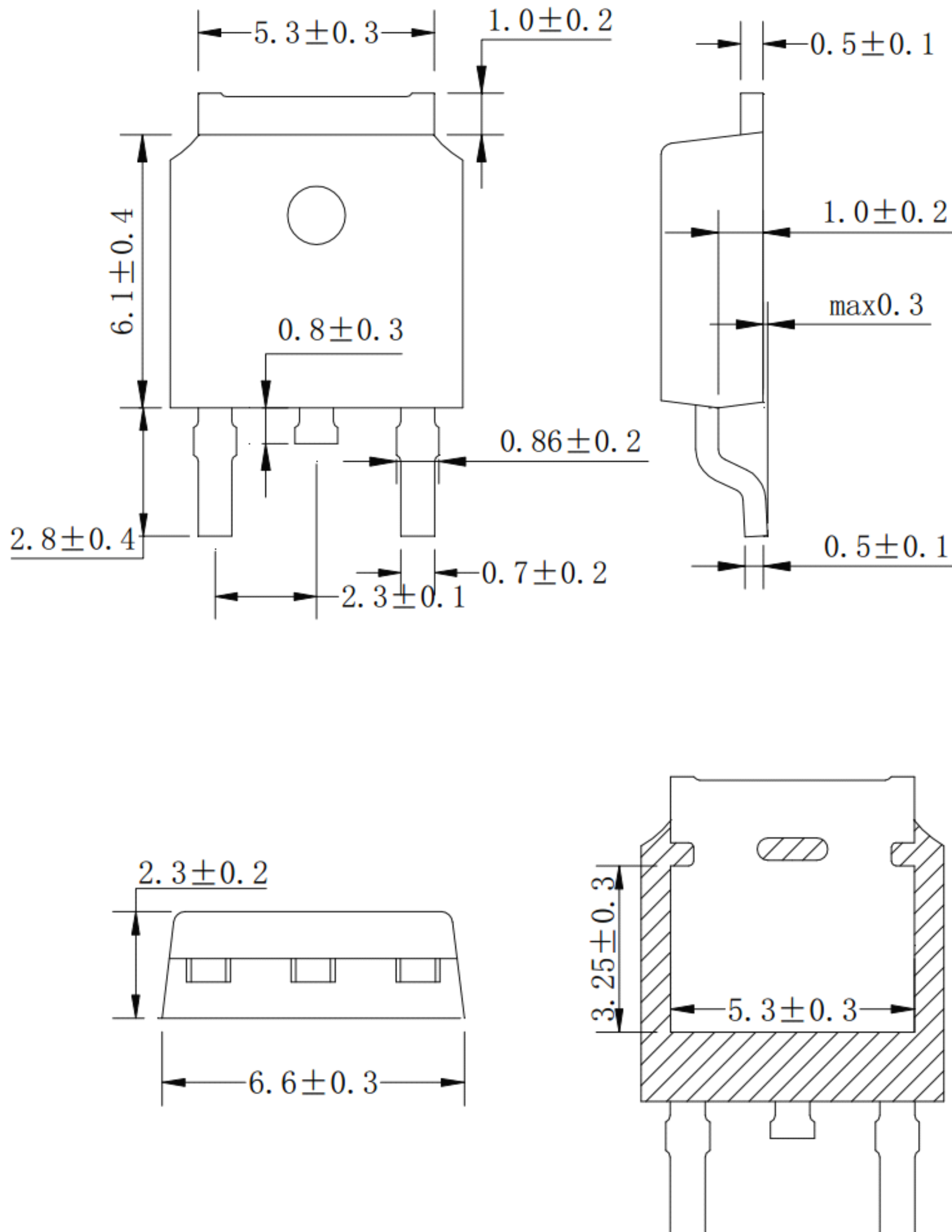


Fig.15 Peak Diode Recovery dv/dt Test Circuit & Waveform

TO-252 Package Dimension



NOTE:

1.We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.

2.Please do not exceed the absolute maximum ratings of the device when circuit designing.

3.Winsemi Microelectronics Co., Ltd reserved the right to make changes in this specification sheet and is subject to change without prior notice.

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