

P-Channel Enhancement Mode MOSFET with Schottky Diode

- Features

P-MOSFET

VDS	VGS	RDSon TYP	ID
-20V	$\pm 8V$	105mR@-4V5	-2A
		130mR@-2V5	
		180mR@-1V8	

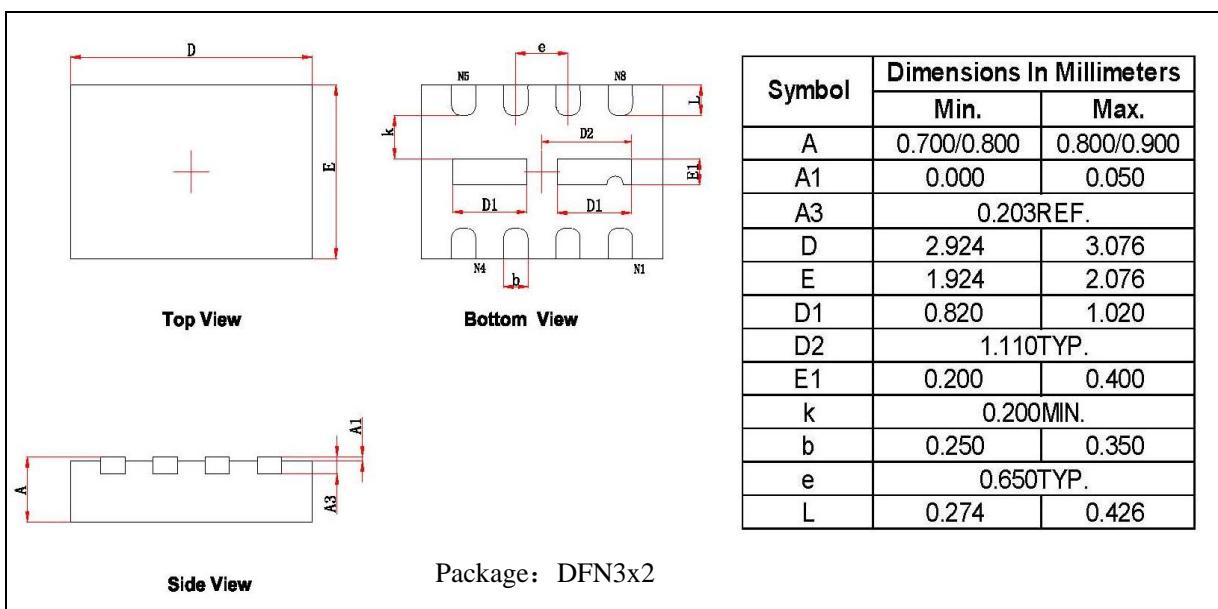
Schottky

VR	IR	VF TYP	IO
20V	35uA	410Mv@0.5A	1A

- General Description

SSC8K21GN3 combines a P-Channel enhancement mode power MOSFET which is produced with high cell density and DMOS trench technology and a low forward voltage schottky diode. the tiny and thin outline saves PCB consumption.

- Package Information





SSC8K21GN3

- Absolute Maximum Ratings @ TA = 25°C unless otherwise specified**

Parameter		Symbol	Ratings		Unit
Drain-Source Voltage		V _{DS}	-20		V
Gate-Source Voltage		V _{GS}	±8		
Drain Current ^(Note 1)	Continuous Pulsed	I _D	-2 -8		A
Schottky Reverse Voltage		V _R	20		V
Schottky Continuous Forward Current		I _F	1		A
Power Dissipation Derating above T _A = 25°C ^(Note 1)		P _d	1.2		W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150		°C

Note:1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inches. The rating is for each chip in the package.

- Electrical Characteristics @ TA = 25°C unless otherwise specified**

Parameter ^(Note 2)	Symbol	Test Conditions	Min	Typ	Max	Unit
P-channel Enhancement Mode MOSFET						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250uA	-20	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V	--	--	-1	uA
Gate-Body Leakage	I _{GSS}	V _{GS} = ±8V, V _{DS} = 0V	--	--	±100	nA
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} = V _{GS} , I _D = -250uA	-0.5	-0.75	-1.2	V
Static Drain-Source On-Resistance	R _{DS(ON)}	I _D = -0.5A, V _{GS} = -4.50V	--	105	180	mR
		I _D = -0.5A, V _{GS} = -2.50V	--	130	200	
		I _D = -0.5A, V _{GS} = -1.80V	--	180	260	
Turn-On Delay Time	t _{d(on)}	V _{DD} = -6V, R _L = 6R, I _D = -1A, V _{GEN} = -4.5V, R _G = 6R	--	13	--	ns
Turn-On Rise Time	t _r		--	18	--	
Turn-Off Delay Time	t _{d(off)}		--	42	--	
Turn-Off Fall Time	t _f		--	120	--	
Input Capacitance	C _{iss}	V _{DS} = -6V, V _{GS} = 0V, f = 1.0 MHz	--	376	--	pF
Output Capacitance	C _{oss}		--	187	--	
Reverse Transfer Capacitance	C _{rss}		--	78	--	
Schottky Diode						
Breakdown Voltage	V _R	I _R =300uA	20	--	--	V
Forward Voltage Drop	V _F	I _F =0.5A	--	0.41	0.48	V
Maximum reverse leakage current	I _R	V _R =20V	--	15	200	uA

Note : 2. Short duration test pulse used to minimize self-heating effect.

- P-channel MOSFET Typical Performance Characteristics

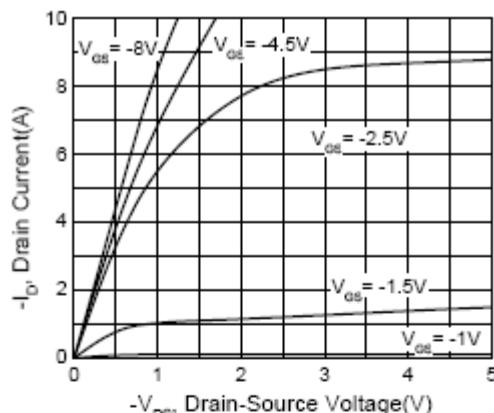


Fig1. Output Characteristics

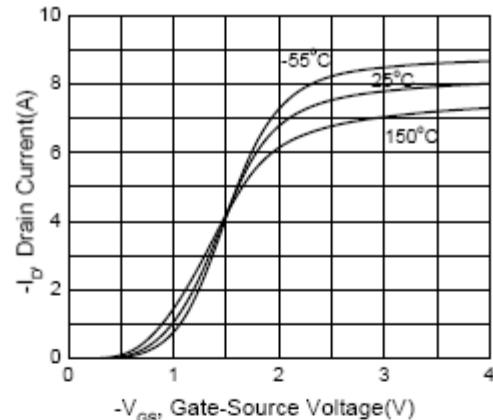


Fig2. Transfer Characteristics

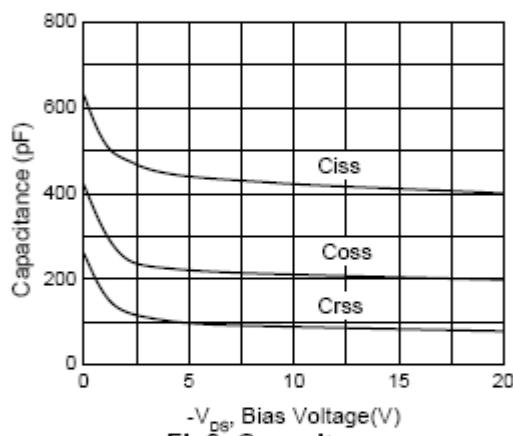


Fig3. Capacitance

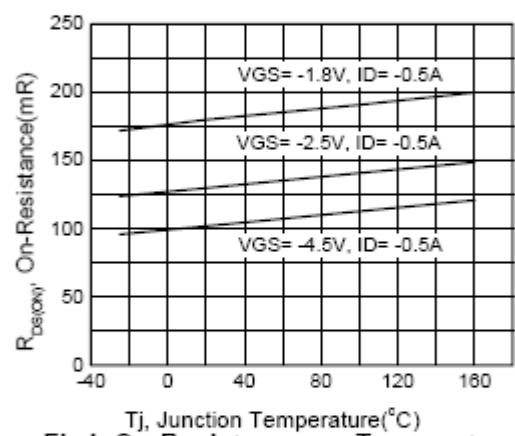


Fig4. On-Resistance vs. Temperature

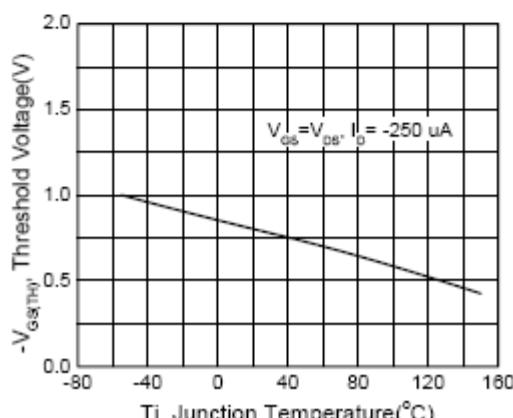


Fig 5. Threshold Voltage

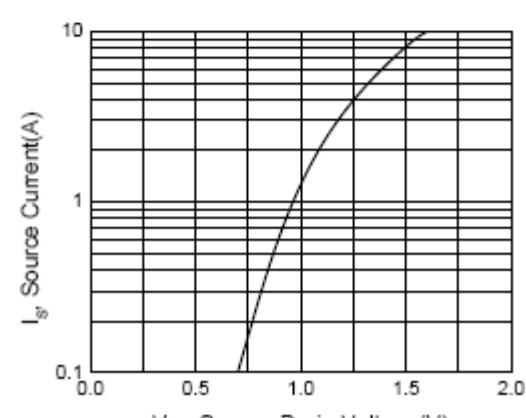


Fig6. Diode Forward Characteristics

- Schottky Diode Typical Performance Characteristics

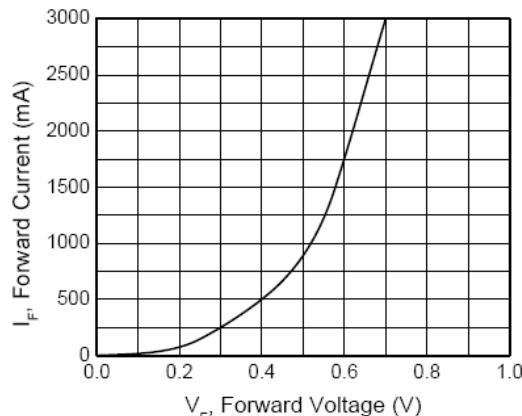


Fig7. Forward Characteristics

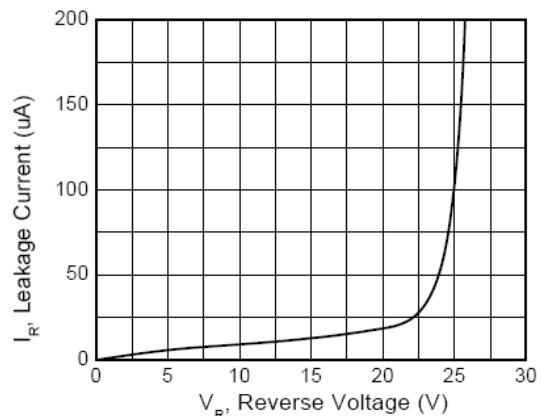


Fig8. Reverse Characteristics

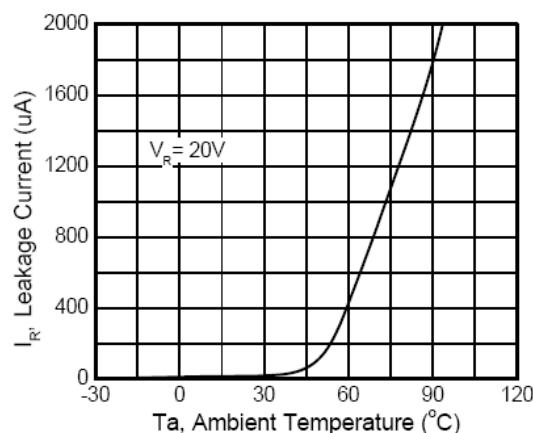


Fig9. Leakage vs. Temperature



SSC8K21GN3

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