

Specification for Approval

Date: 2017/08/10

Customer : 深圳台慶

TAI-TECH P/N: **TMPC0624H-Series-D**
CUSTOMER P/N:
DESCRIPTION:
QUANTITY: pcs

REMARK:

Customer Approval Feedback

☐ 西北臺慶科技股份有限公司
 TAI-TECH Advanced Electronics Co., Ltd
 Headquarter:
 NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI,
 TAO-YUAN HSIEN, TAIWAN, R.O.C.
 TEL: +886-3-4641148 FAX: +886-3-4643565
 http://www.tai-tech.com.tw
 E-mail: sales@tai-tech.com.tw

☐ Office:
 深圳辦公室
 11BC, Building B Fortune Plaza, NO.7002, Shennan Avenue, Futian
 District Shenzhen
 TEL: +86-755-23972371 FAX: +86-755-23972340

☐ 臺慶精密電子(昆山)有限公司
 TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD
 SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN,
 JIANG-SU, CHINA
 TEL: +86-512-57619396 FAX: +86-512-57619688
 E-mail: sales@tai-tech.cn

☒ 慶邦電子元件(泗洪)有限公司
 TAIPAQ ELECTRONICS(SIHONG) CO., LTD
 JIN SHA JIANG ROAD, CONOMIC DEVELOPMENT ZONE SIHONG,
 JIANGSU, CHINA.
 TEL: +86-527-88601191 FAX: +86-527-88601190
 E-mail: sales@taipaq.cn

Sales Dep.

APPROVED	CHECKED
管哲碩 Eric Kuan	劉璦瑄 Aries Liu

R&D Center

APPROVED	CHECKED	DRAWN
羅宜春	梁周虎	卜文娟

SMD Power Inductor

TMPC0624H-Series-D

ECN HISTORY LIST					
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	17/08/10	新發行	羅宜春	梁周虎	卜文娟
備 注					

SMD Power Inductor

TMPC0624H-Series-D

1. Features

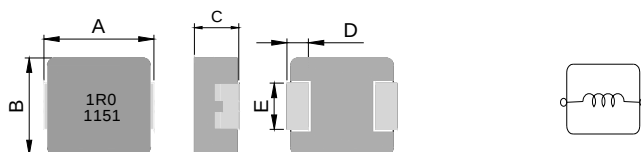
1. Carbonyl Powder.
2. Compact design.
3. High current , low DCR , high efficiency.
4. Very low acoustic noise and very low leakage flux noise.
5. High reliability.
6. 100% Lead(Pb)-Free and RoHS compliant.



2. Applications

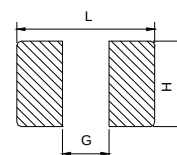
Note PC power system , incl. IMVP-6
DC/DC converter .

3. Dimensions



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
TMPC0624H	7.0±0.3	6.6±0.3	2.2±0.2	1.8±0.3	3.0±0.3

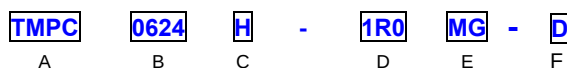
Recommend PC Board Pattern



L(mm)	G(mm)	H(mm)
7.7	2.5	3.5

Note: 1. The above PCB layout reference only.
2. Recommend solder paste thickness at 0.15mm and above.

4. Part Numbering



A: Series
B: Dimension
C: Type
D: Inductance
E: Inductance Tolerance
F: 印 D/C

BxC
Carbonyl Powder.
1R0=1.00uH
M=±20%
印字:黑色. 1R0 及 D/C 1151(11 年,51 週期)(依實際生產日期而定)

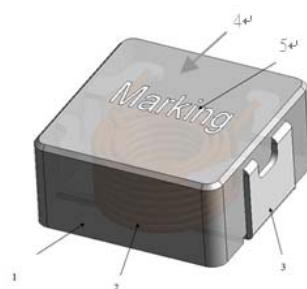
5. Specification

Part Number	Inductance L0 (uH) @ 0 A	I rms (A) Typ.	I sat (A) Typ.	DCR (mΩ) Typ. @25℃	DCR (mΩ) Max. @25℃
TMPC0624H-R10YG-D	0.10±30%	30	70	1.4	1.7
TMPC0624H-R15YG-D	0.15±30%	30	45	1.8	2.3
TMPC0624H-R20MG-D	0.20±20%	23	40	1.9	2.8
TMPC0624H-R22MG-D	0.22±20%	21	34	2.0	3.2
TMPC0624H-R33MG-D	0.33±20%	18	30	3.6	4.4
TMPC0624H-R36MG-D	0.36±20%	17	29	3.8	4.6
TMPC0624H-R47MG-D	0.47±20%	15	26	4.8	5.1
TMPC0624H-R56MG-D	0.56±20%	13	24	5.5	6.5
TMPC0624H-R60MG-D	0.60±20%	13	22	5.7	6.9
TMPC0624H-R68MG-D	0.68±20%	13	21	6.4	7.2
TMPC0624H-R82MG-D	0.82±20%	11	17	8.0	9.5
TMPC0624H-1R0MG-D	1.00±20%	11	16	10.5	13.5
TMPC0624H-1R5MG-D	1.50±20%	9	15	17	20
TMPC0624H-2R2MG-D	2.20±20%	7	14	23	28
TMPC0624H-3R3MG-D	3.30±20%	6	10	34	39
TMPC0624H-4R7MG-D	4.70±20%	5.5	9	41	50
TMPC0624H-5R6MG-D	5.60±20%	5	8	56	62
TMPC0624H-6R8MG-D	6.80±20%	4	7	65	72
TMPC0624H-8R2MG-D	8.20±20%	3.6	6.0	81	95
TMPC0624H-100MG-D	10.0±20%	3.2	5.0	92	101
TMPC0624H-150MG-D	15.0±20%	2.5	3.5	150	180
TMPC0624H-220MG-D	22.0±20%	1.8	3.0	185	215

Note:

1. Test frequency : Ls : 100KHz /1.0V.
2. All test data referenced to 25℃ ambient.
3. Testing Instrument(or equ) : L: HP4284A,CH11025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.
4. Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40℃
5. Saturation Current (Isat) will cause L0 to drop approximately 20%.
6. The part temperature (ambient + temp rise) should not exceed 125℃ under worst case operating conditions.Circuit design,component,PCB trace size and thickness,airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
7. Special inquiries besides the above common used types can be met on your requirement.

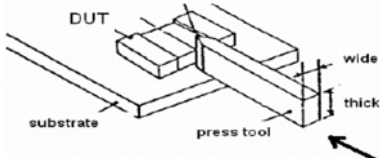
6. Material List



NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+Sn---Plating)
4	paint	Epoxy resin
5	Ink	Halogen-free ketone

7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	1. -10~+40℃,50~60%RH (Product without taping) 2. -40~+125℃ (on board)	
Electrical Performance Test		
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately $\Delta L20\%$.	Saturation DC Current (Isat) will cause L0 to drop $\Delta L(\%)$
Heat Rated Current (Irms)	Approximately $\Delta T40^{\circ}\text{C}$	Heat Rated Current (Irms) will cause the coil temperature rise $\Delta T(^{\circ}\text{C})$. 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test	Appearance : No damage. Impedance : within $\pm 15\%$ of initial value Inductance : within $\pm 10\%$ of initial value Q : Shall not exceed the specification value. RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature : $125\pm 2^{\circ}\text{C}$ (Inductor) Applied current : rated current Duration : 1000 $\pm$ 12hrs Measured at room temperature after placing for 24 $\pm$ 2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity : 85 $\pm$ 2% R.H. Temperature : $85^{\circ}\text{C}\pm 2^{\circ}\text{C}$ Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24 $\pm$ 2 hrs.
Moisture Resistance		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) 1. Baked at 50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to $65\pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs,keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1 : $-40\pm 2^{\circ}\text{C}$ 30 $\pm$ 5min Step2 : $25\pm 2^{\circ}\text{C}$ ≤ 0.5 min Step3 : $125\pm 2^{\circ}\text{C}$ 30 $\pm$ 5minNumber of cycles : 500 Measured at room tempraturc after placing for 24 $\pm$ 2 hrs.
Vibration		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:1.52mm $\pm 10\%$ Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations)

Item	Performance	Test Condition														
Bending	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.														
Shock		<table><tr><th>Type</th><th>Peak value (g's)</th><th>Normal duration (D) (ms)</th><th>Wave form</th><th>Velocity change (Vi)ft/sec</th></tr><tr><td>SMD</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr><tr><td>Lead</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr></table>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	SMD	50	11	Half-sine	11.3	Lead	50	11	Half-sine
Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec												
SMD	50	11	Half-sine	11.3												
Lead	50	11	Half-sine	11.3												
Solder ability	More than 95% of the terminal electrode should be covered with solder ◦	Preheat: 150℃,60sec. ◦ Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5℃ ◦ Flux for lead free: Rosin. 9.5% ◦ Dip time: 4±1sec ◦ Depth: completely cover the termination														
Resistance to Soldering Heat		Depth: completely cover the termination <table><tr><th>Temperature(℃)</th><th>Time(s)</th><th>Temperature ramp/immersion and emersion rate</th><th>Number of heat cycles</th></tr><tr><td>260 ±5 (solder temp)</td><td>10 ±1</td><td>25mm/s ±6 mm/s</td><td>1</td></tr></table>	Temperature(℃)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1						
Temperature(℃)		Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles												
260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1													
Terminal Strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 														

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

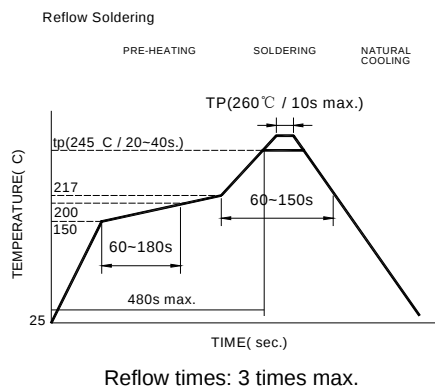


Fig.1

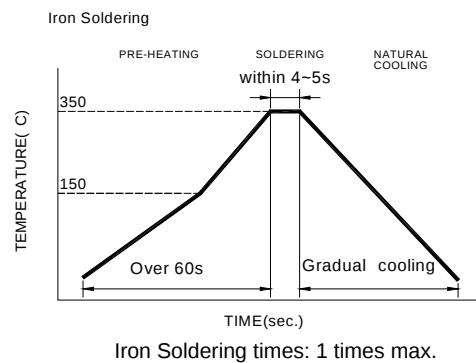
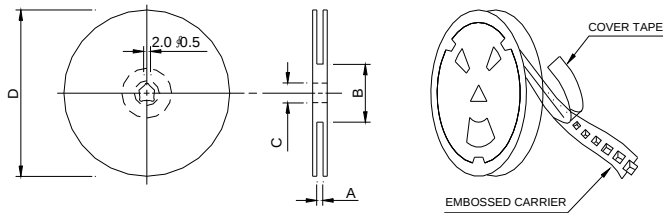


Fig.2

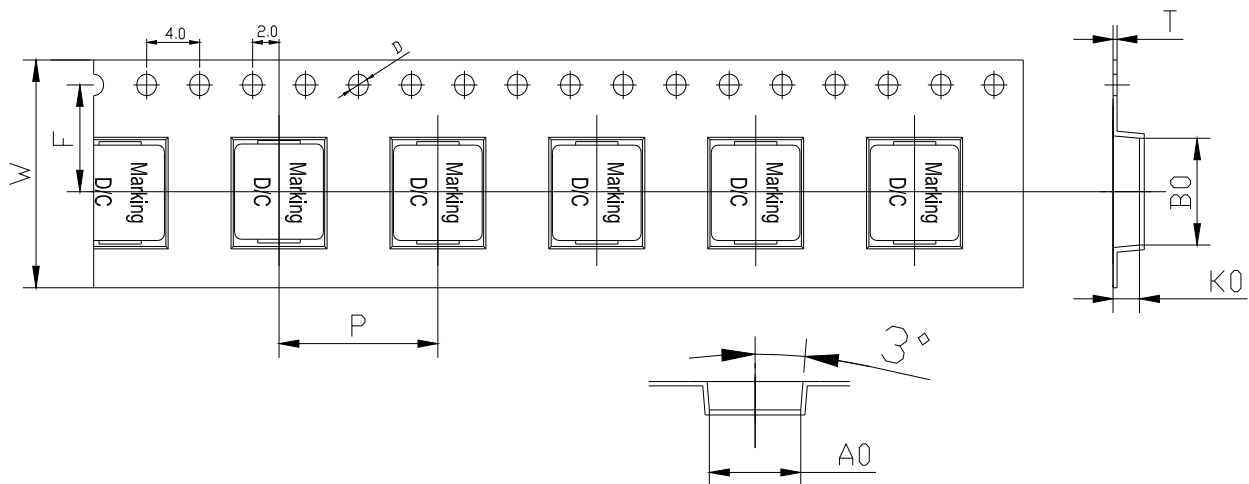
9. Packaging Information

(1) Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x16mm	16.4±2/-0	100±2	13±0.5/-0.2	330

(2) Tape Dimension

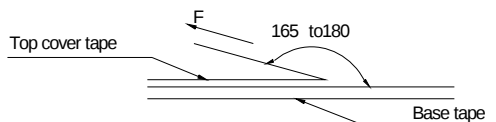


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
TMPC	0624	7.7±0.1	7.0±0.1	2.7±0.1	12.0±0.1	16±0.3	7.5±0.1	0.35±0.05	1.5±0.1

(3) Packaging Quantity

TMPC	0624
Chip / Reel	1500
Inner box	3000
Carton	12000

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-C-2003 of 4.11 stadnard).

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

- Storage Conditions
 - To maintain the solderability of terminal electrodes:
 - 1. TAIPAQ products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
 - 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
 - 3. Recommended products should be used within 12 months form the time of delivery.
 - 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

10. Typical Performance Curves

