



PJEC5V0M1FN2

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage	V_{RWM}	-	-	-	5	V
Snap-Break Voltage	V_{SB}	$I_{SB}=50\text{mA}$	5	-	8	V
Reverse Leakage Current	I_R	$V_R=5.0\text{V}$	-	-	0.1	μA
Clamping Voltage	V_{CL}	$I_{PP}=1\text{A}$, $t_p=8/20\mu\text{s}$	-	-	9	V
		$I_{PP}=3.5\text{A}$, $t_p=8/20\mu\text{s}$	-	-	12.5	
Clamping Voltage TLP (Note 1)	V_{CL}	$I_{PP}=4\text{A}$, $t_p=100\text{ns}$	-	8.6	-	V
		$I_{PP}=8\text{A}$, $t_p=100\text{ns}$	-	9.7	-	
Dynamic Resistance	R_{DYN}	$t_p=100\text{ns}$	-	0.27	-	Ω
Off State Junction Capacitance	C_J	0Vdc Bias $f=1\text{MHz}$	-	-	10	pF

NOTES :

1. Testing using Transmission Line Pulse (TLP) conditions: $Z_0 = 50\Omega$, $t_p = 100\text{ ns}$.



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TYPICAL CHARACTERISTIC CURVES

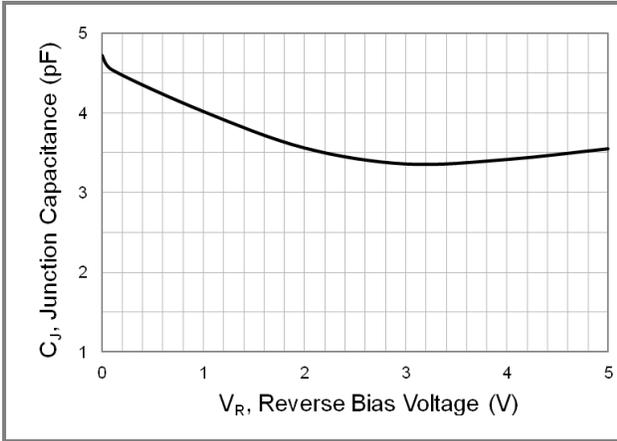


Fig.1 Typical Junction Capacitance

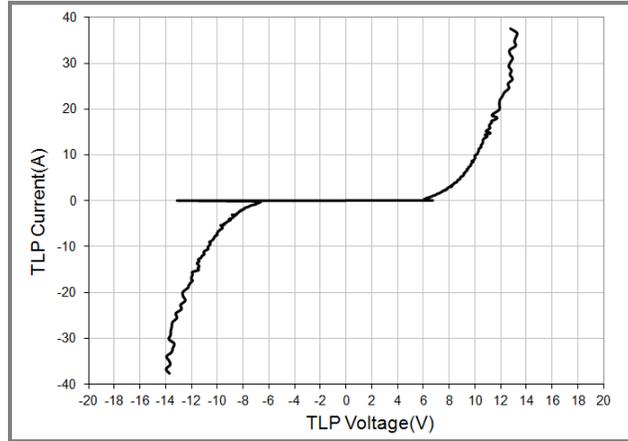


Fig.2 Transmission Line Pulsing (TLP) Measurement

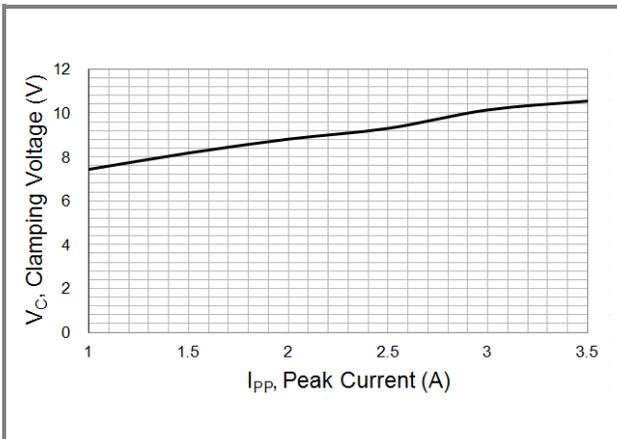


Fig.3 Typical Peak Clamping Voltage(8/20 μ s)

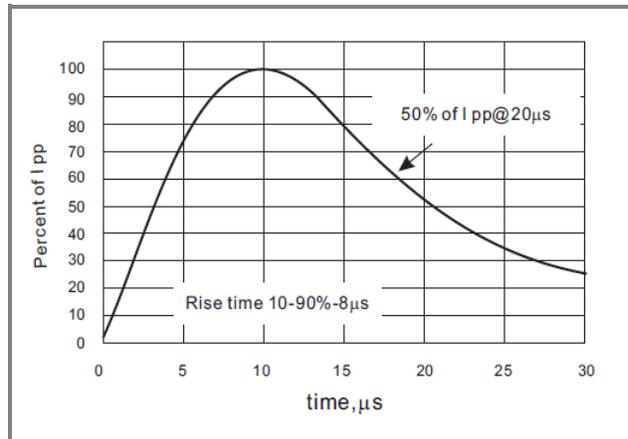


Fig.4 8/20 μ s Pulse Waveform

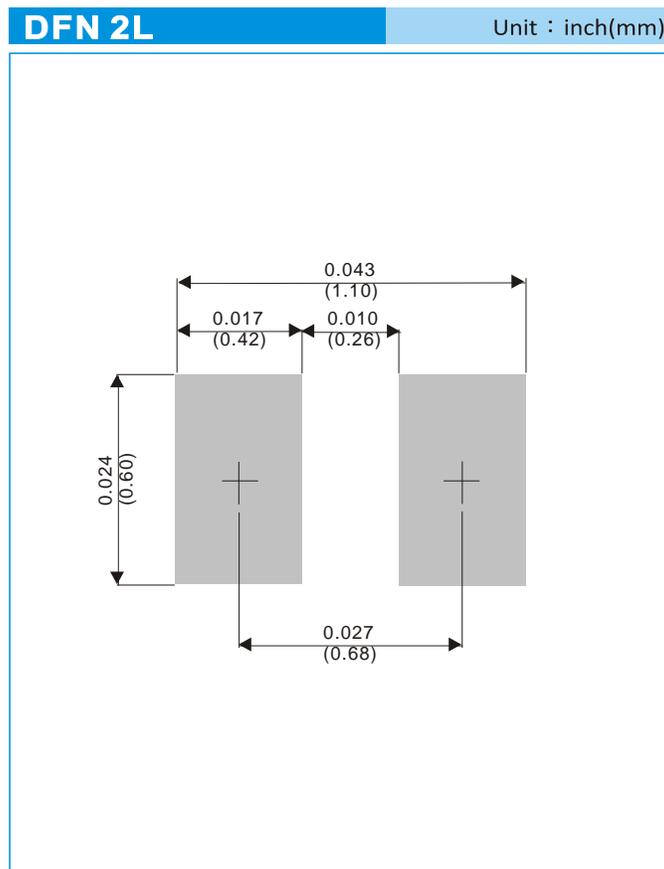


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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJEC5V0M1FN2_R1_00001	DFN 2L	8K pcs / 7" reel	6B	Halogen free

MOUNTING PAD LAYOUT





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