



N 沟道增强型场效应晶体管  
N-CHANNEL MOSFET  
**FHS80N08B/FHD80N08B**

**主要参数 MAIN CHARACTERISTICS**

ID	80 A
VDSS	80 V
Rdson-typ (@Vgs=10V)	8mΩ
Qg-typ	73nC

**用途 APPLICATIONS**

电池管理系统	BMS
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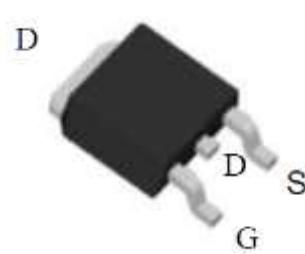
**产品特性 FEATURES**

低栅极电荷	Low gate charge
低 Crss (典型值 230pF)	Low Crss (typical 230pF )
开关速度快	Fast switching
100% 经过雪崩测试	100% avalanche tested
高抗 dv/dt 能力	Improved dv/dt capability
RoHS 产品	RoHS product

**封装形式 Package**

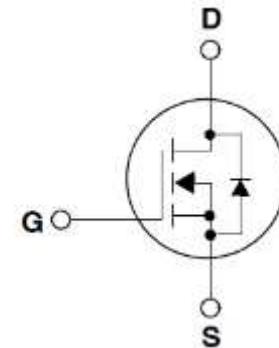


TO-263  
FHS series



TO-252  
FHD series

**等效电路 Equivalent Circuit**



**绝对最大额定值 ABSOLUTE RATINGS (Tc=25°C)**

项目 Parameter	符号 Symbol	数值 Value		单位 Unit
		FHS80N08B	FHD80N08B	
最高漏极—源极直流电压 Drain-Source Voltage	VDS	80		V
连续漏极电流* Drain Current -continuous *	I <sub>D</sub> (T <sub>c</sub> =25°C)	80		A
	I <sub>D</sub> (T <sub>c</sub> =100°C)	56		A
最大脉冲漏极电流 (注 1) Drain Current – pulse (note 1)	I <sub>DM</sub>	320		A
最高栅源电压 Gate-Source Voltage	V <sub>Gs</sub>	±25		V
单脉冲雪崩能量 (注 2) Single Pulsed Avalanche Energy (note 2)	E <sub>AS</sub>	180		mJ
雪崩电流 (注 1) Avalanche Current (note 1)	I <sub>AR</sub>	12		A
重复雪崩能量 (注 1) Repetitive Avalanche Current (note 1)	E <sub>AR</sub>	10		mJ
二极管反向恢复最大电压变化速率 (注 3) Peak Diode Recovery dv/dt (note 3)	dv/dt	5.0		V/ns
耗散功率 Power Dissipation	P <sub>D</sub> (T <sub>C</sub> =25°C)	110	45	W
	-Derate above 25°C	0.73	0.23	W/°C
最高结温及存储温度 Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55~+175		°C
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	T <sub>L</sub>	300		°C

\*漏极电流由最高结温限制

\*Drain current limited by maximum junction temperature

## 电特性 ELECTRICAL CHARACTERISTICS

项目 Parameter	符号 Symbol	测试条件 Tests conditions	最小 Min	典型 Typ	最大 Max	单位 Units	
<b>关态特性 Off -Characteristics</b>							
漏一源击穿电压 Drain-Source Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	80	-	-	V	
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	ΔBV <sub>DSS</sub> /Δ TJ	I <sub>D</sub> =250μA, referenced to 25°C	-	0.06	-	V/°C	
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =63V, V <sub>GS</sub> =0V, T <sub>C</sub> =25°C	-	-	1	μA	
		V <sub>DS</sub> =50V, T <sub>C</sub> =125°C	-	-	100	μA	
栅极体漏电流 Gate-body leakage current	I <sub>GSS</sub> (F/R)	V <sub>DS</sub> =0V, V <sub>GS</sub> =±25V	-	-	±100	nA	
<b>通态特性 On-Characteristics</b>							
阈值电压 Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V	
静态导通电阻 Static Drain-Source On-Resistance	R <sub>Ds(ON)</sub>	V <sub>GS</sub> =10V , I <sub>D</sub> =40A	-	8	9.5	mΩ	
正向跨导 Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> = 20V, I <sub>D</sub> =40A (note 4)	-	70	-	S	
<b>动态特性 Dynamic Characteristics</b>							
栅电阻 Gate Resistance	R <sub>g</sub>	f=1.0MHz, V <sub>DS</sub> OPEN	-	1.7	-	Ω	
输入电容 Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	4900	-	pF	
输出电容 Output capacitance	C <sub>oss</sub>		-	300	-		
反向传输电容 Reverse transfer capacitance	C <sub>rss</sub>		-	230	-		
<b>开关特性 Switching Characteristics</b>							
延迟时间 Turn-On delay time	t <sub>d(on)</sub>	V <sub>DS</sub> =40V, I <sub>D</sub> =30A, R <sub>G</sub> =6Ω V <sub>GS</sub> =10V (note 4, 5)	-	50	-	ns	
上升时间 Turn-On rise time	t <sub>r</sub>		-	50	-	ns	
延迟时间 Turn-Off delay time	t <sub>d(off)</sub>		-	130	-	ns	
下降时间 Turn-Off Fall time	t <sub>f</sub>		-	40	-	ns	
栅极电荷总量 Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V , I <sub>D</sub> =40A , V <sub>GS</sub> =10V (note 4, 5)	-	73	-	nC	
栅一源电荷 Gate-Source charge	Q <sub>gs</sub>		-	20	-	nC	
栅一漏电荷 Gate-Drain charge	Q <sub>gd</sub>		-	20	-	nC	
<b>漏一源二极管特性及最大额定值 Drain-Source Diode Characteristics and Maximum Ratings</b>							
正向最大连续电流 Maximum Continuous Drain -Source Diode Forward Current	I <sub>s</sub>		-	-	80	A	
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current	I <sub>SM</sub>		-	-	320	A	
正向压降 Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =30A	-	0.82	1.3	V	
反向恢复时间 Reverse recovery time	t <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =30A ,dI <sub>F</sub> /dt=100A/μs (note 4)	-	60	-	ns	
反向恢复电荷 Reverse recovery charge	Q <sub>rr</sub>		-	100	-	nC	

## 热特性 THERMAL CHARACTERISTIC

项目 Parameter	符号 Symbol	FHS80N08B	FHD80N08B	单位 Unit
结到管壳的热阻 Thermal Resistance, Junction to Case	R <sub>th(j-c)</sub>	1.3	2.67	°C/W
结到环境的热阻 Thermal Resistance, Junction to Ambient	R <sub>th(j-A)</sub>	62.5	125	°C/W

注释:

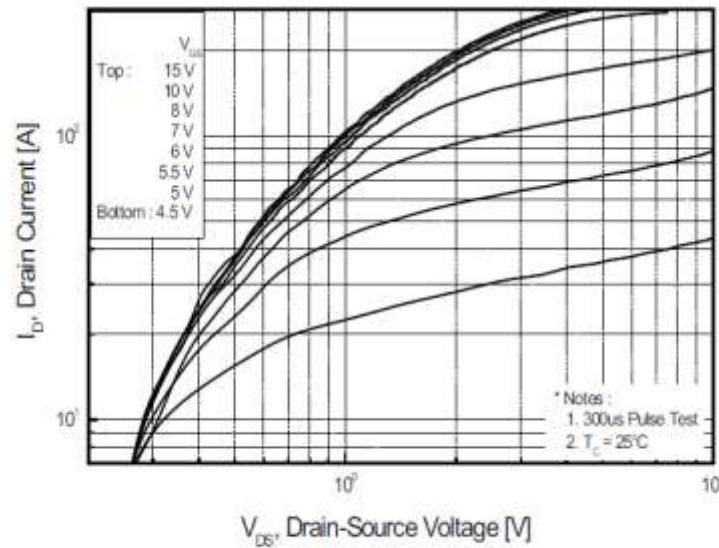
- 1: 脉冲宽度由最高结温限制
- 2: L=1mH, I<sub>AS</sub>=12A, V<sub>DD</sub>=48V, R<sub>G</sub>=25 Ω, 起始结温 T<sub>J</sub>=25°C
- 3: I<sub>SD</sub> ≤ 80A, di/dt ≤ 300A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, 起始结温 T<sub>J</sub>=25°C
- 4: 脉冲测试: 脉冲宽度 ≤ 300μs, 占空比 ≤ 2%
- 5: 基本与工作温度无关

Notes:

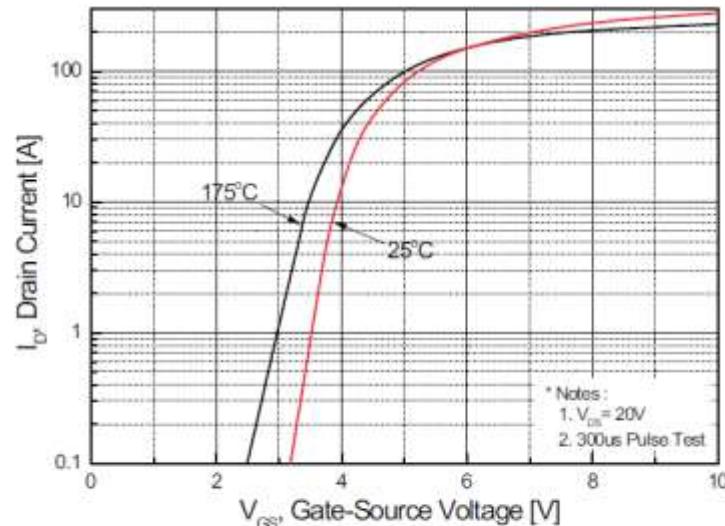
- 1: Pulse width limited by maximum junction temperature
- 2: L=1mH, I<sub>AS</sub>=12A, V<sub>DD</sub>=48V, R<sub>G</sub>=25 Ω, Starting T<sub>J</sub>=25°C
- 3: I<sub>SD</sub> ≤ 80A, di/dt ≤ 300A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub>=25°C
- 4: Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%
- 5: Essentially independent of operating temperature

## Typical Characteristics

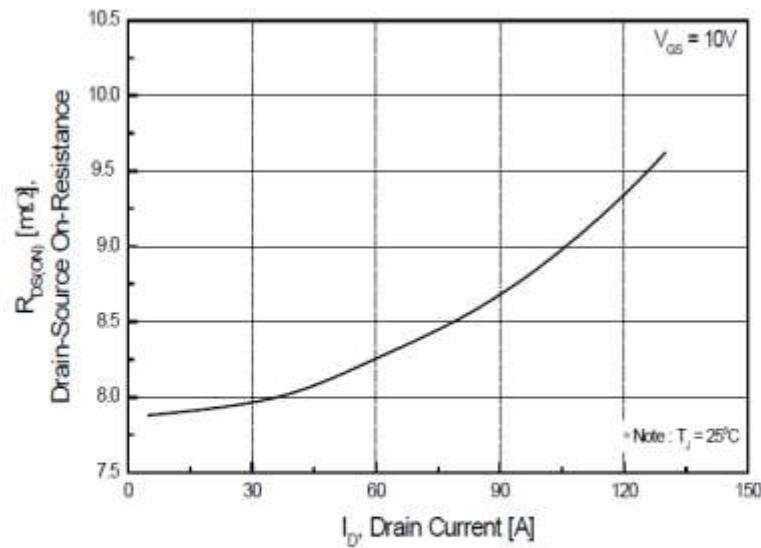
### 典型特性曲线



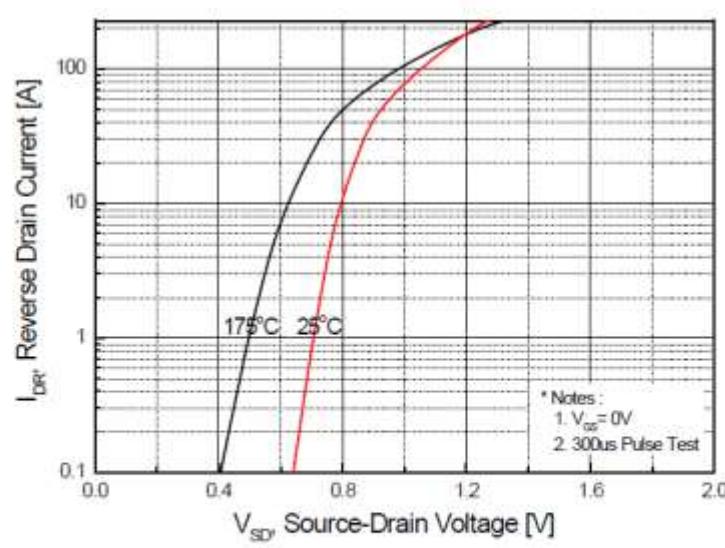
**Figure 1. On Region Characteristics**



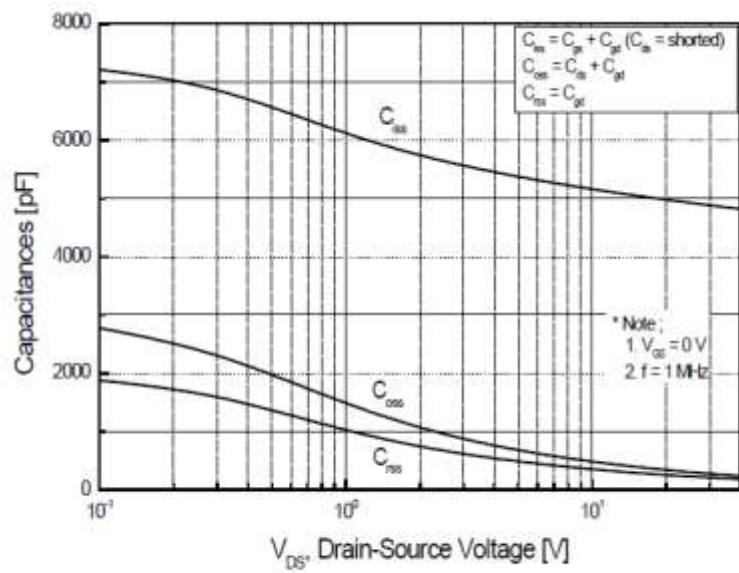
**Figure 2. Transfer Characteristics**



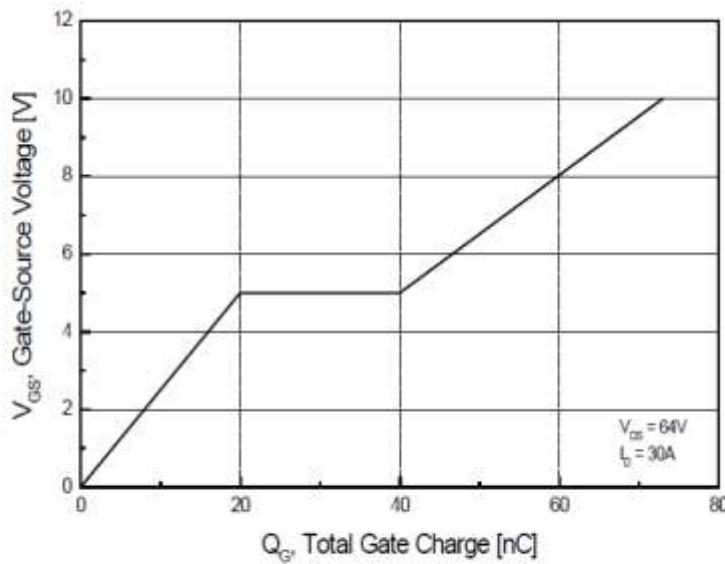
**Figure 3. On Resistance Variation vs. Drain Current and Gate Voltage**



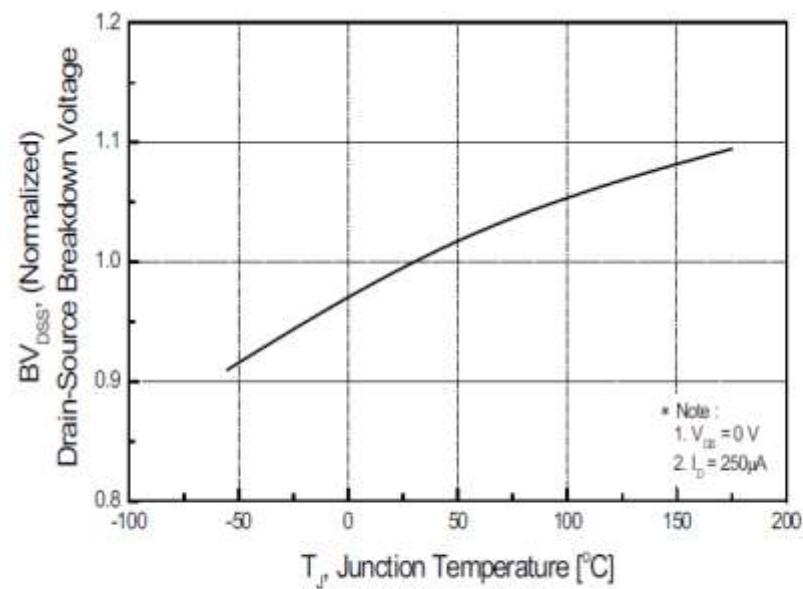
**Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature**



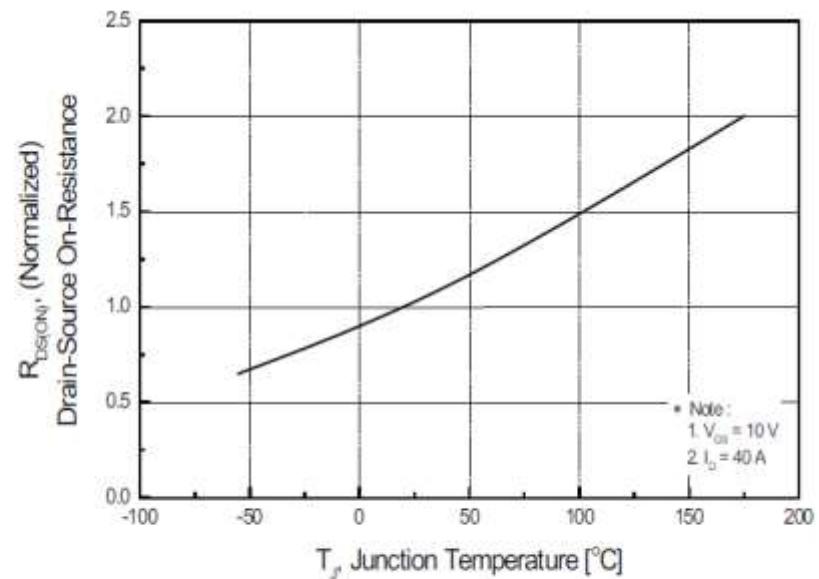
**Figure 5. Capacitance Characteristics**



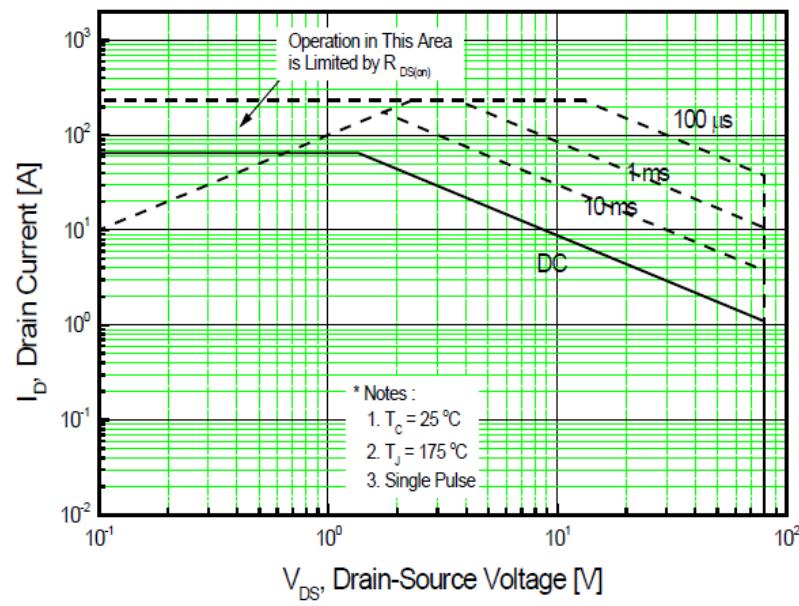
**Figure 6. Gate Charge Characteristics**



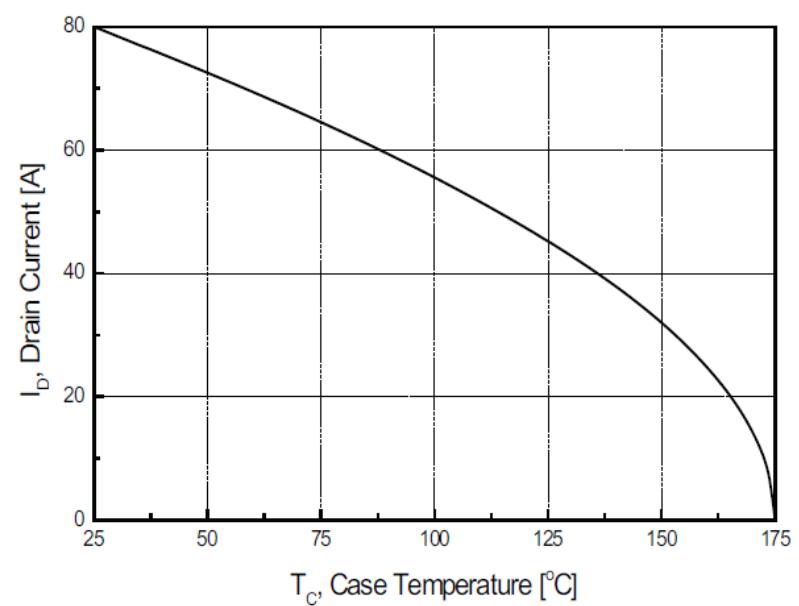
**Figure 7. Breakdown Voltage Variation vs Temperature**



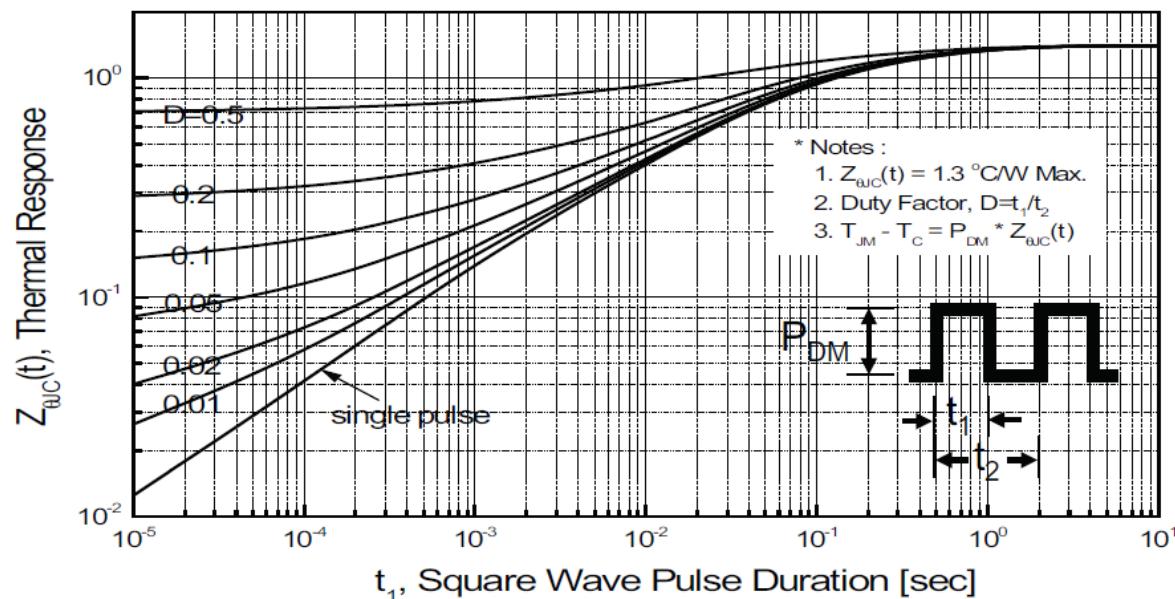
**Figure 8. On-Resistance Variation vs Temperature**



**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs Case Temperature**



**Figure 11. Transient Thermal Response Curve**

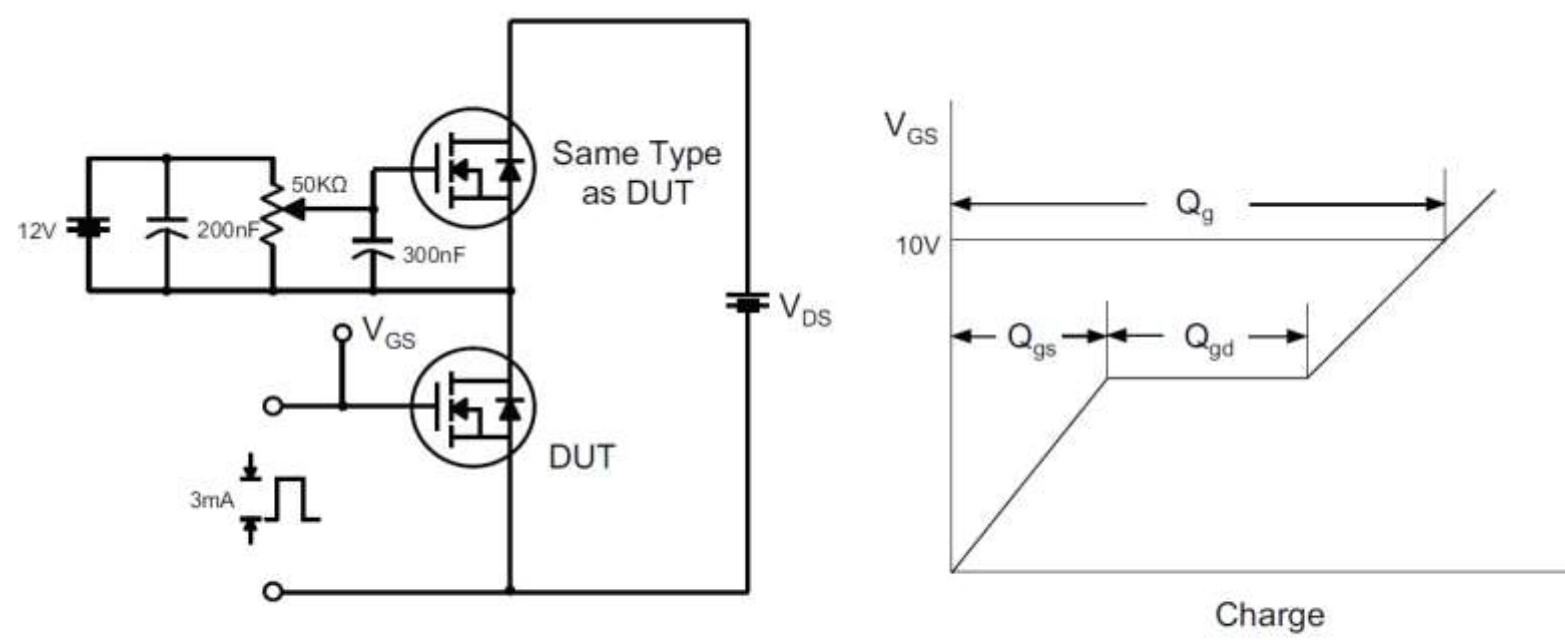


Fig 12. Gate Charge Test Circuit & Waveform

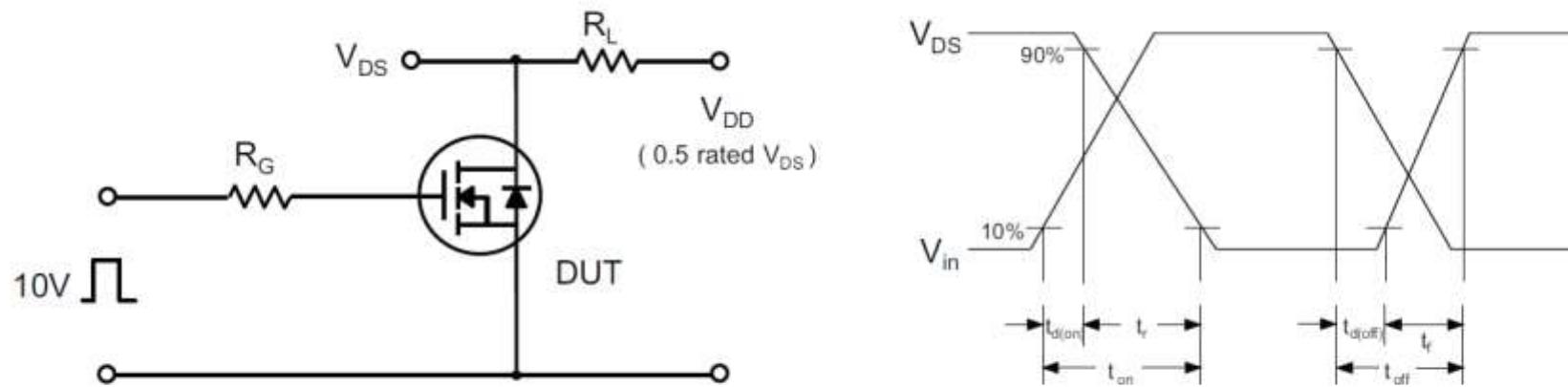


Fig 13. Resistive Switching Test Circuit & Waveforms

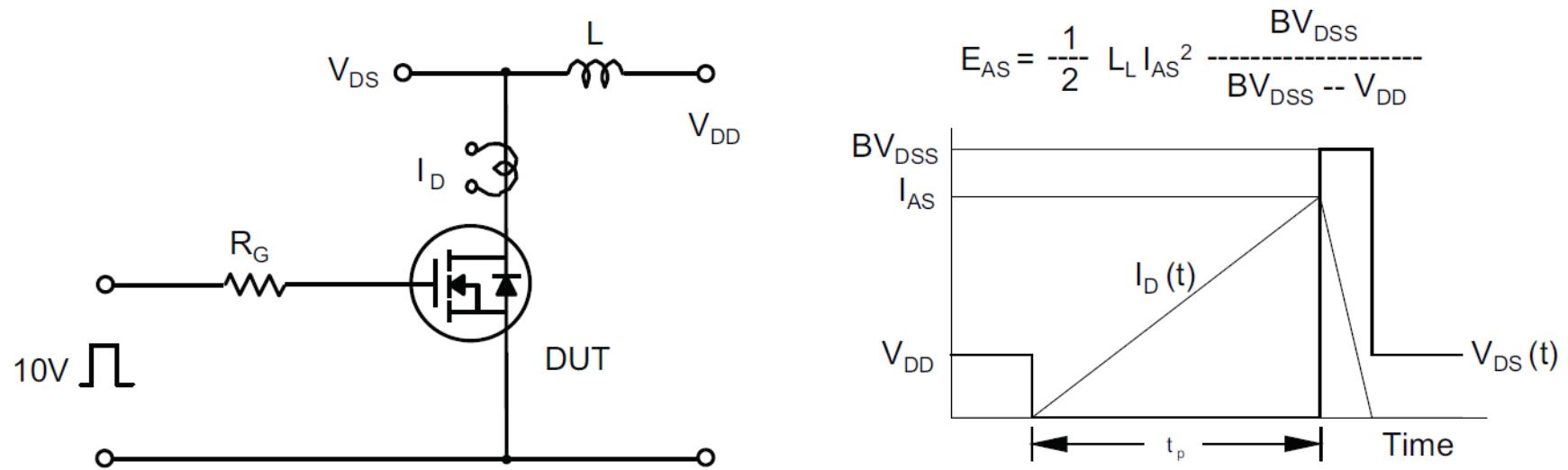


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms

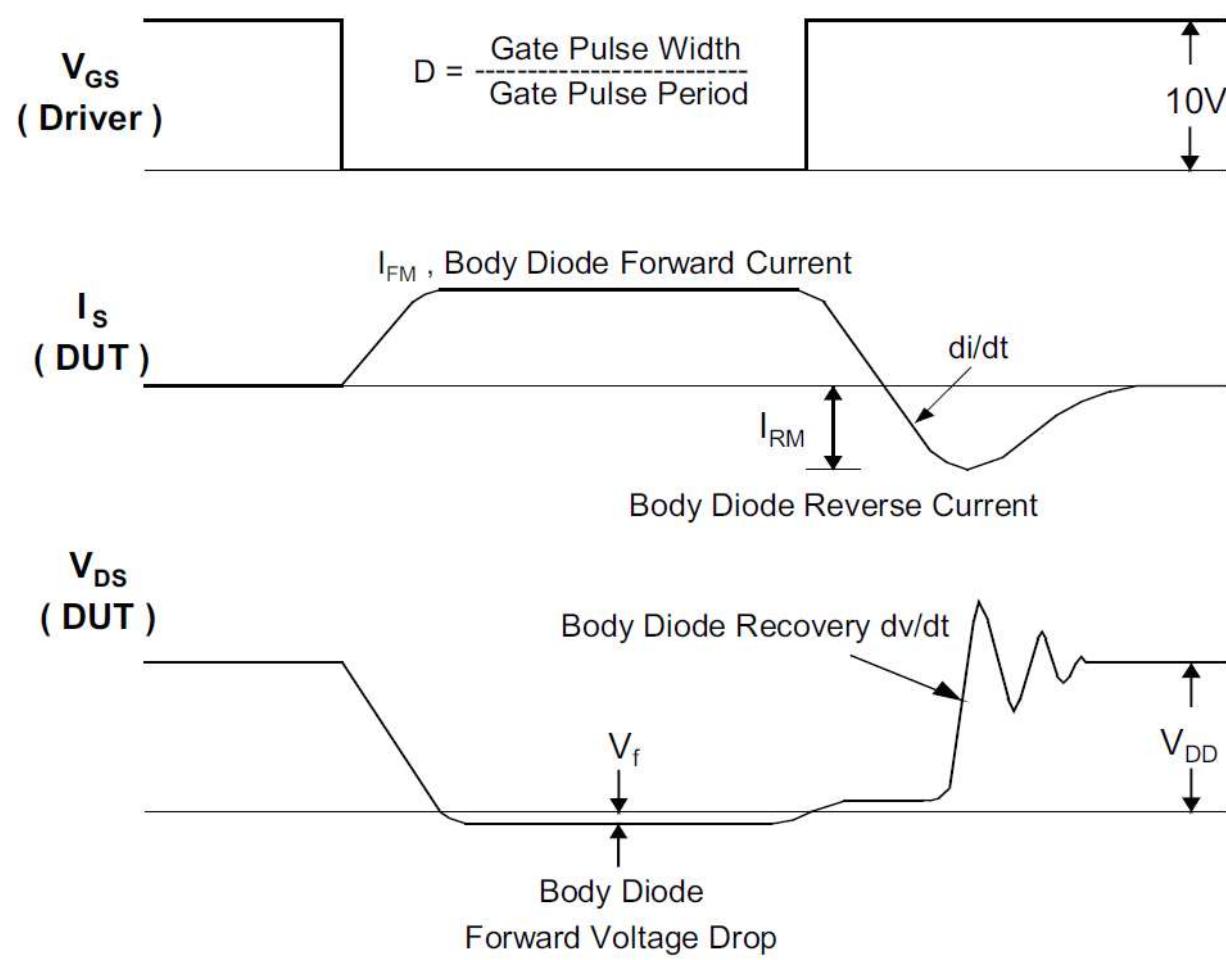
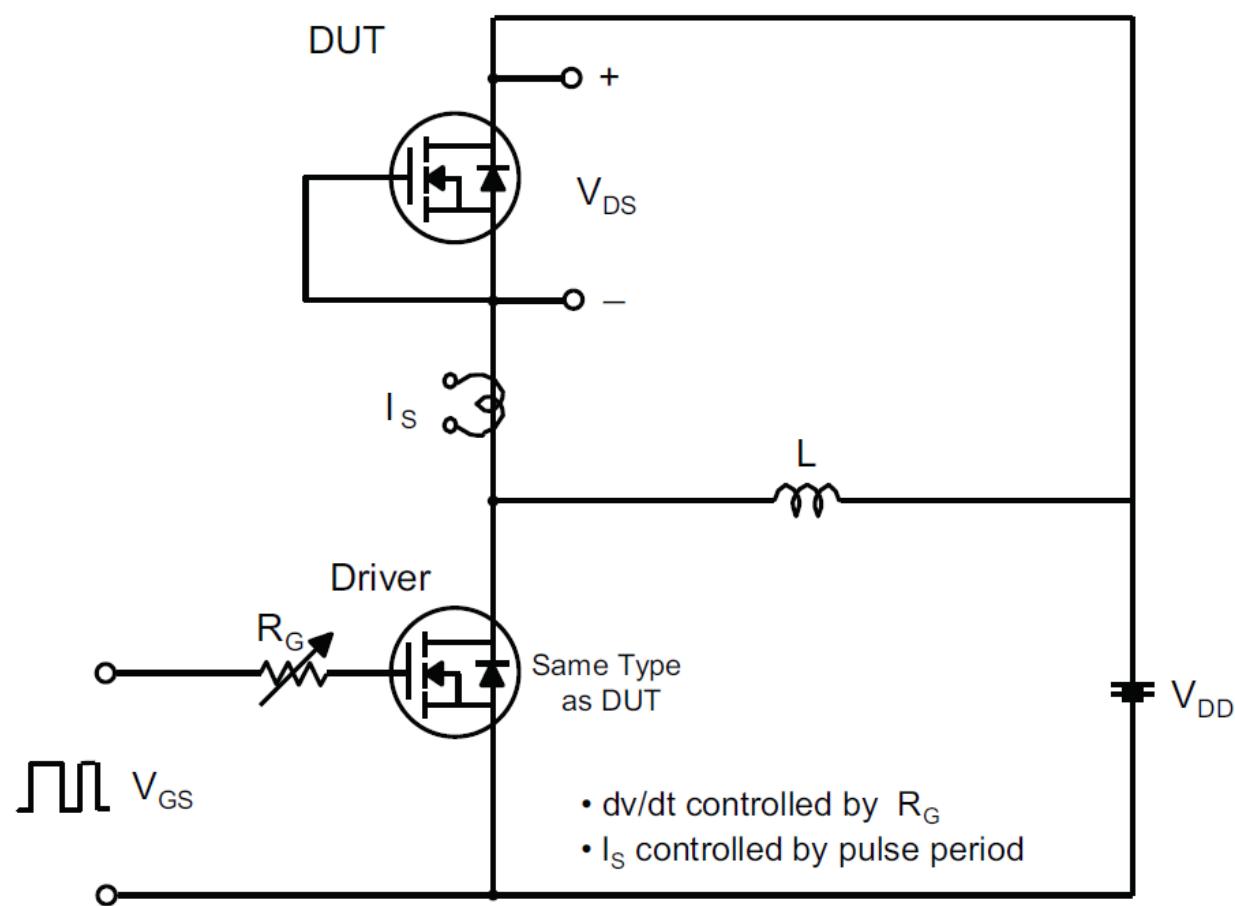
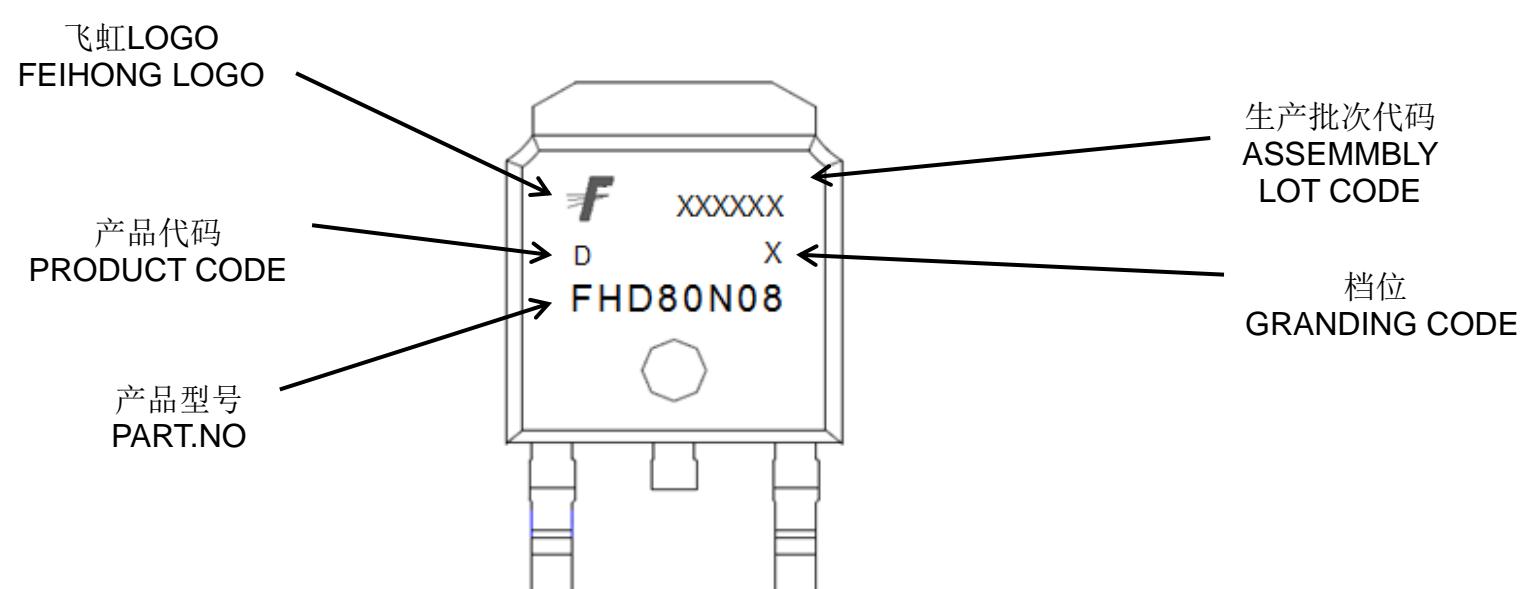
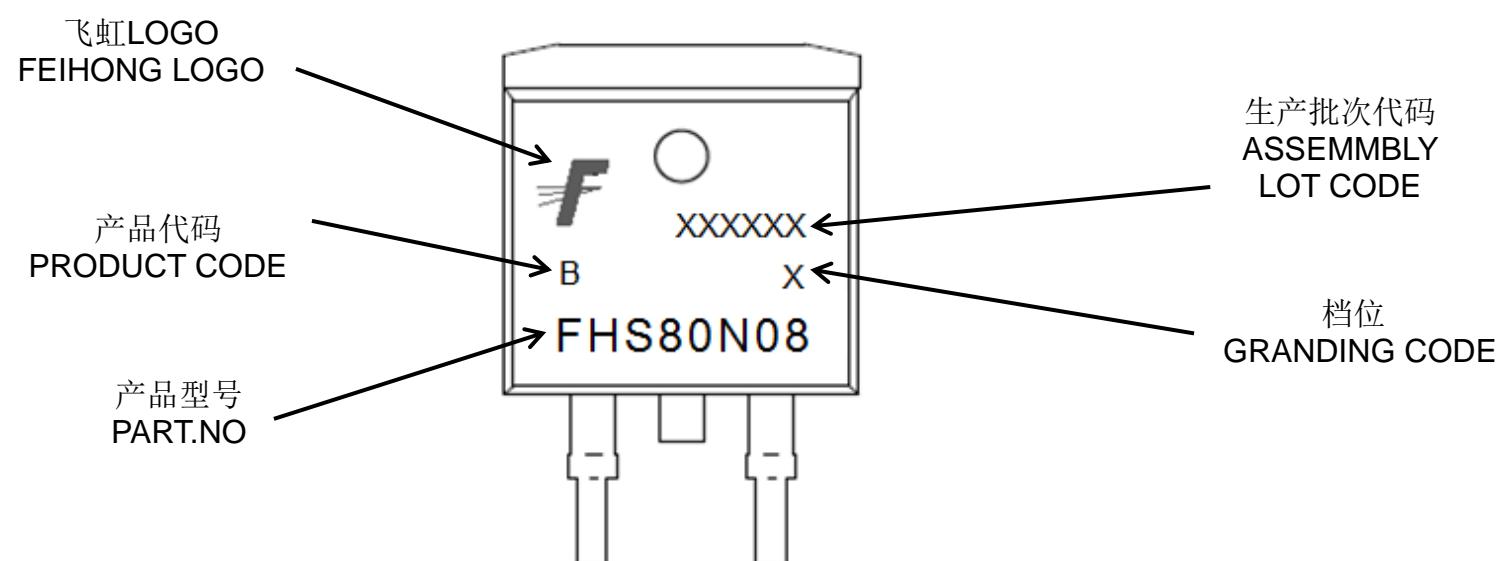


Fig 15. Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms

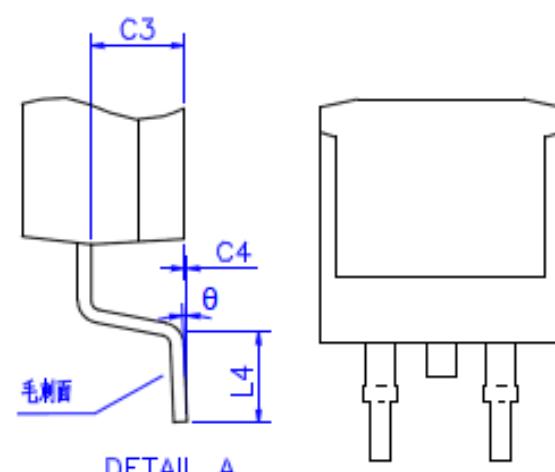
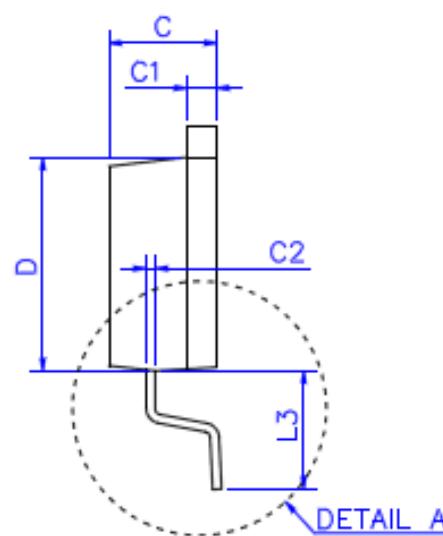
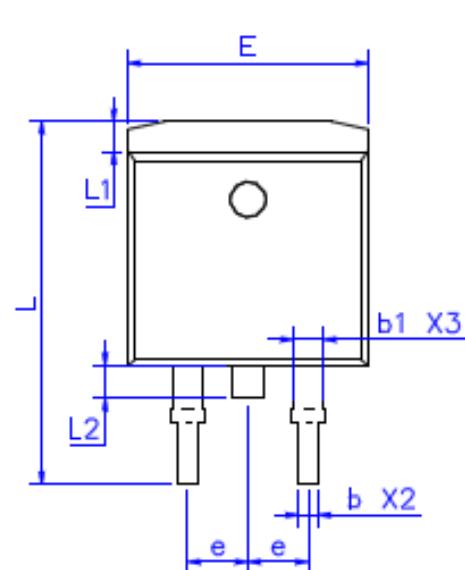
## 印记 Marking:



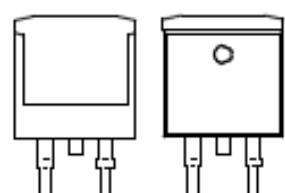
外形尺寸：

Package Dimension:

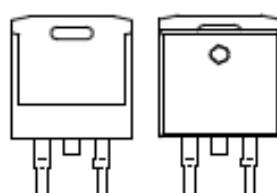
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DIM	MILLIMETERS	
	MIN	MAX
E	9.80	10.50
L	14.60	15.80
L1	1.00	1.55
L2	1.30	1.70
L3	4.50	5.50
L4	2.10	2.90
b	0.60	0.99
b1	1.00	1.50
C	4.30	4.80
C1	1.10	1.45
C2	0.25	0.52
C3	2.40	2.80
C4	0	0.25
D	8.50	9.50
θ	0°	8°
e	Typical	2.54



框架不带锁料孔



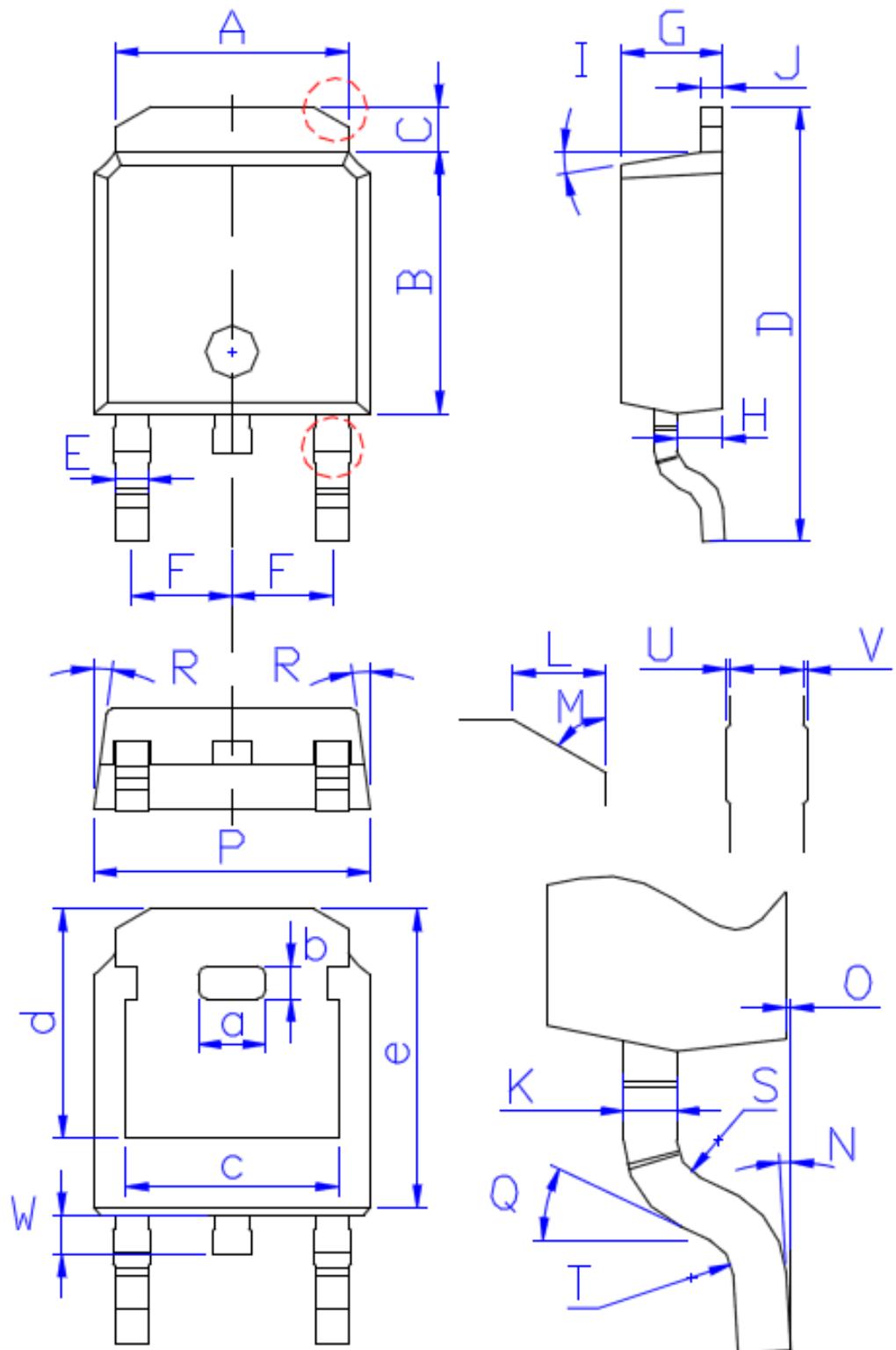
框架带锁料孔

(Unit: mm)

外形尺寸:

Package Dimension:

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DIM	MILLIMETERS
A	5.34±0.30
B	6.00±0.30
C	1.05±0.30
D	9.95±0.30
E	0.76±0.15
F	2.28±0.15
G	2.30±0.30
H	1.06±0.30
I	(4-10)°
J	0.51±0.15
K	0.52±0.15
L	0.80±0.30
M	60°
N	(0-10)°
O	0.05±0.05
P	6.60±0.30
Q	25°
R	(4-8.5)°
S	R0.40
T	R0.40
U	0.05±0.05
V	0.05±0.05
W	0.90±0.30
a	1.80±0.30
b	0.75±0.30
c	4.85±0.30
d	5.30±0.30
e	6.90±0.30

(Units: mm)