

Current Mode PWM Power Switch

Features

- Current Mode PWM
- Very Low Startup Current
- Under-Voltage Lockout (UVLO)
- Non-Audible-Noise Green-Mode Control
- Fixed Switching Frequency of 65KHz
- Cycle-by-Cycle Peak Current Limiting
- Internal Leading-Edge Blanking
- Internal Slope Compensation
- Less than 0.1W of Power Saving
- Over-Voltage Protection (OVP) on VCC Pin
- Over-Load Protection (OLP)

Applications

- Switching AC/DC Power Battery Charger
- Digital Camera
- Open-Frame SMPS
- PDA Power Supply

Ordering and Marking Information

Description

GR8937L integrates a PWM controller and high voltage power MOSFET of 650V. GR8937L has the features of very low startup current and current mode PWM control with green-mode function at light load. The integrated functions of GR8937L also include the leading-edge blanking of the current sensing, internal slope compensation, cycle-by--cycle peak current limiting and soft start. OCP, OVP and OLP provide protection performance for fault conditions. These functions enable the power supply to easily meet even the strictest power requirements.

GR8937L improves the performance and reduces the cost of power supplies.



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Pin Configuration



Pin Description

Pin No.	Symbol	Description
1	VCC-G	Power supply input for internal gate driver
2	VCC	Power supply input for IC
3	FB	Voltage feedback pin, by connecting a photo-coupler to control the duty cycle
4	SENSE	Current sense pin
5, 6	DRAIN	Drain of internal HV MOS
7, 8	GND	Ground

Recommended Operating Conditions

Item	Min.	Max.	Unit
Supply voltage VCC	11	25	V
VCC-G pin series resistor	51	510	Ohm
VCC pin series resistor	10	75	Ohm
FB pin capacitor	1	100	nF

Note: 1. FB pin parallel one 6.5V Zener diode for Safety Regulation on abnormal test.

2. Sense pin resistor recommended to use SMD type for avoiding stray inductor interference issue.



Recommended Output Power Range

Port No	90~264Vac				
Part No.	Open frame	Adaptor			
GR8937L	15W	12W			

Notes: Maximum practical continuous power in an open frame design with sufficient drain pattern as a heat sink, at 50 $^{\circ}$ C ambient.

Absolute Maximum Ratings

Drain Voltage	0.3V ~ 650V
Supply Voltage VCC	
VCC-G	30V
FB,Sense	-0.3 ~ 7V
Max Operating Junction Temperature	150 °C
Operating Ambient Temperature	20°C ~ 85°C
Storage Temperature Range	
Lead Temperature (All Pb free packages, soldering, 10 sec)	
ESD Voltage Protection, Human Body Model	2KV
ESD Voltage Protection, Machine Model	200V



Block Diagram





Electrical Characteristics (VCC=15.0V & TA = +25 °C, unless otherwise specified.)

Parameter	Pin	Min.	Тур.	Max.	Unit		
SUPPLY VOLTAGE							
Startup Current	2		7	20	uA		
Operating Current (with 1nF load on OUT pin), Vcomp = 0V	5	400	800	1500	uA		
Operating Current, VFB = 2.5V	2		2.5		mA		
UVLO (off)	2	6.3	6.8	7.3	V		
UVLO (on)	2	10.8	11.3	11.8	V		
OVP Level on VCC Pin	2	27	28	29	V		
VOLTAGE FEEDBACK							
Short Circuit Current, Vcomp = 0V	3		0.4	0.8	mA		
Open Loop Voltage, COMP Pin Open	3		5.7		V		
CURRENT SENSING							
Maximum Input Voltage	4	0.8	0.85	0.9	V		
Leading-Edge Blanking time	4		350		nS		
Input Impedance	4	1			MΩ		
Delay to Output	4		100		nS		
OSCILLATOR		-					
Frequency	-	60	65	70	KHz		
Jitter Frequency	-		<u>+</u> 6		%		
Green Mode Frequency			22		KHz		
Temp. Stability (-40°C ~ 110°C)	-		5		%		
Voltage Stability (VCC = 11V ~ 25V)	-		3		%		
MOSFET SECTION	-						
BVdss Vgs=0	5&6		650		V		
Rds (on)	5&6		4		Ω		
OLP							
OLP Delay Time	-		60		ms		
OLP Trim Level	-		3.45		V		
PWM SECTION							
Maximum Duty Cycle	-	70	75	80	%		



Application Information Start-up Current

The typical start-up current is 8uA. Very low start-up current allows the PWM controller to increase the value of start-up resistor and then reduce the power dissipation on it.

Under-voltage Lockout (UVLO)

A hysteresis UVLO comparator is implemented in GR8937L, then the turn-on and turn-off thresholds level are fixed on 11.3V and 6.8V respectively. This hysteresis shown in Fig. 7 ensures that the start-up capacitor will be adequate to supply the chip during start-up.



Soft Start

During initial power on, GR8937L provides soft start function. It effectively suppresses the start up peak current to reduce the power MOSFET drain voltage especially at high line.

Oscillator

The frequency of the oscillator is fixed internally at

about 65kHz. The maximum duty-cycle of internal oscillator is limited about 75% to avoid the transformer saturation.

Green Mode Operation

When the load decreases to an extent, the frequency of the controller will decrease so as to reduce the system power consumption. The

minimum frequency is about 22KHz, which is outside the audio range.

Leading-edge Blanking (LEB)

Each time the power MOSFET is switched on, a turn-on spike will inevitably occur at the sense resistor. To avoid fault trigger, a 350ns leading-edge blanking time is built in. Conventional RC filtering can therefore be omitted. During this blanking period, the current-limit comparator is disabled and can not switch off the gate driver.

Internal Slope Compensation

Built-in slope compensation circuit adds voltage ramp in the current sense input voltage for PWM generation. This greatly improves the close loop stability at CCM and prevents the sub-harmonic oscillation and thus reduces the output ripple voltage.

Over-load Protection (OLP)

The controller has over load protection function. An internal circuit detects the load level, when the load is larger than a threshold and the condition lasts more than 60ms, the gate output will keep low level. Then VCC decreases below UVLO off level, the controller resets again.

Over-voltage Protection (OVP) on VCC

To prevent power MOSFET from being damaged, GR8937L is implemented an OVP function on VCC. When the VCC voltage is higher than the OVP threshold voltage, the output gate driver circuit will be shut down immediately to stop the switching of internal HV power MOSFET.

The VCC OVP function is an Auto-recovery type



protection. If OVP happens, the pulses will be stopped and recover at the next UVLO on. GR8937L is working in a hiccup mode.

Gate Driver

Driving capability can be adjusted by a resistor between VCC and VCC-G for EMI improvement. Typical Application Circuit The resistor can decrease rising time of internal gate driver. But be attention that too large resistance could decrease system efficiency (especially at CCM condition).



GR8937L



BOM List



1	C1	E-CAP 400V/10uF					
2	C2	E-CAP 400V/10uF				I	
3	C3	332/1KV		No.	Part.No	Description	Note
4	C4	E-CAP 50V/10uF		25	R1	SMD1206 / 105	
5	C5	SMD0805,104		26	R2	SMD1206 / 514	
6	C6	10 nF		27	R3	SMD0805/ 511	
7	C7	Y1 Cap 1000pF		28	R4	SMD1206 / 104	
8	C8	SMD0805,222		29	R5	SMD1206 / 683	
9	C9	E-CAP 10V/1000uF		30	R6	SMD1206 / 2.7R	
10	C10	E-CAP 10V/1000uF		31	R7	SMD1206 / 2.2R	
11	C11	E-CAP 16V/330uF		32	R8	SMD0805 / 0R	
12	C12	SMD0805,104		33	R9	SMD1206 / 33R	
13	C13	SMD0805,104		34	R10		N.C
14	CR1	1N4007	•	35	R11	SMD0805/ 221	
15	CR2	1N4007		36	R12		N.C
16	CR3	1N4007		37	R13	SMD0805 / 202	
17	CR4	1N4007		38	R14	SMD0805 / 472	1%
18	CR5	FR104		39	R15	SMD0805 / 472	1%
19	CR6	FR107		40	R16	SMD0805/ 47R	
20	CR7	SR540		41	J1	Jumper	
21	F1	250V/1A		42	J2	SMD1206 / 0R	
22	L1		30mH	43	T1	EE19	1.77mH
23	L2	R3*5	1.0uH	44	TVR1		N.C
24	NTC1	5Ω		45	U1	GR8937L	DIP8
			<u> </u>	46	U2	EL817	
				47	U3	GL431	
				48	ZD1	6.5V	

Package Information





		DI	P-8		
SYMBOL	MILLIM	IETERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
А		5.33		0.210	
A1	0.38		0.015		
A2	2.92	4.95	0.115	0.195	
b	0.36	0.56	0.014	0.022	
b2	1.14	1.78	0.045	0.070	
с	0.20	0.35	0.008	0.014	
D	9.01	10.16	0.355	0.400	
D1	0.13		0.005		
E	7.62	8.26	0.300	0.325	
E1	6.10	7.11	0.240	0.280	
е	2.54 BSC		0.100) BSC	
eA	7.62 BSC		0.300) BSC	
eB		10.92		0.430	
L	2.92	3.81	0.115	0.150	

Note: 1. Followed from JEDEC MS-001 BA.

2. Dimension D, D1 and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 10 mil.

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Revision History