

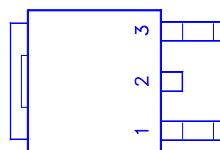
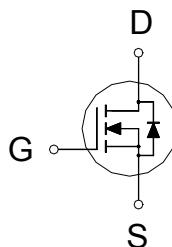
**NIKO-SEM**
**N-Channel Enhancement Mode  
Field Effect Transistor**
**P1025HDB**

TO-252

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

| $V_{(BR)DSS}$ | $R_{DS(ON)}$ | $I_D$ |
|---------------|--------------|-------|
| 250V          | 460mΩ        | 10A   |


 1: GATE  
 2: DRAIN  
 3: SOURCE
**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$  Unless Otherwise Noted)**

| PARAMETERS/TEST CONDITIONS            |                     | SYMBOL         | LIMITS     | UNITS |
|---------------------------------------|---------------------|----------------|------------|-------|
| Drain-Source Voltage                  |                     | $V_{DS}$       | 250        | V     |
| Gate-Source Voltage                   |                     | $V_{GS}$       | $\pm 20$   | V     |
| Continuous Drain Current <sup>2</sup> | $T_C = 25^\circ C$  | $I_D$          | 10         | A     |
|                                       | $T_C = 100^\circ C$ |                | 6.3        |       |
| Pulsed Drain Current <sup>1</sup>     |                     | $I_{DM}$       | 26         |       |
| Avalanche Current                     |                     | $I_{AS}$       | 5.3        |       |
| Avalanche Energy                      | $L = 1mH$           | $E_{AS}$       | 14         | mJ    |
| Power Dissipation                     | $T_C = 25^\circ C$  | $P_D$          | 52         | W     |
|                                       | $T_C = 100^\circ C$ |                | 20         |       |
| Junction & Storage Temperature Range  |                     | $T_J, T_{stg}$ | -55 to 150 | °C    |

**THERMAL RESISTANCE RATINGS**

| THERMAL RESISTANCE  | SYMBOL          | TYPICAL | MAXIMUM | UNITS  |
|---------------------|-----------------|---------|---------|--------|
| Junction-to-Ambient | $R_{\theta JA}$ |         | 62.5    | °C / W |
| Junction-to-Case    | $R_{\theta JC}$ |         | 2.4     |        |

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>This characteristics assumes the die are assembled in TO-220 packages.**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ C$ , Unless Otherwise Noted)**

| PARAMETER                                     | SYMBOL        | TEST CONDITIONS                                 | LIMITS |     |           | UNIT      |
|---|---------------|---|--------|-----|-----------|-----------|
|   |               |   | MIN    | TYP | MAX       |           |
| <b>STATIC</b>                                 |               |   |        |     |           |           |
| Drain-Source Breakdown Voltage                | $V_{(BR)DSS}$ | $V_{GS} = 0V, I_D = 250\mu A$                   | 250    |     |           | V         |
| Gate Threshold Voltage                        | $V_{GS(th)}$  | $V_{DS} = V_{GS}, I_D = 250\mu A$               | 1      | 2   | 3         |           |
| Gate-Body Leakage                             | $I_{GSS}$     | $V_{DS} = 0V, V_{GS} = \pm 20V$                 |        |     | $\pm 100$ | nA        |
| Zero Gate Voltage Drain Current               | $I_{DSS}$     | $V_{DS} = 250V, V_{GS} = 0V$                    |        |     | 1         | $\mu A$   |
|   |               | $V_{DS} = 200V, V_{GS} = 0V, T_J = 125^\circ C$ |        |     | 10        |           |
| Drain-Source On-State Resistance <sup>1</sup> | $R_{DS(ON)}$  | $V_{GS} = 4.5V, I_D = 5A$                       |        | 491 | 590       | $m\Omega$ |
|   |               | $V_{GS} = 10V, I_D = 5A$                        |        | 381 | 460       |           |

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|   |              |   |  |      |    |    |
|---|--------------|---|--|------|----|----|
| Forward Transconductance <sup>1</sup>   | $g_{fs}$     | $V_{DS} = 10V, I_D = 5A$  |  | 7    |    | S  |
| <b>DYNAMIC</b>  |              |   |  |      |    |    |
| Input Capacitance   | $C_{iss}$    | $V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$                                   |  | 372  |    | pF |
| Output Capacitance  | $C_{oss}$    |   |  | 64   |    |    |
| Reverse Transfer Capacitance  | $C_{rss}$    |   |  | 10.9 |    |    |
| Total Gate Charge <sup>2</sup>  | $Q_g$        |   |  | 13   |    |    |
| Gate-Source Charge <sup>2</sup>   | $Q_{gs}$     | $V_{GS} = 10V, V_{DS} = 200V$<br>$I_D = 10A$                            |  | 1.7  |    | nC |
| Gate-Drain Charge <sup>2</sup>  | $Q_{gd}$     |   |  | 6.1  |    |    |
| Turn-On Delay Time <sup>2</sup>   | $t_{d(on)}$  |   |  | 10   |    |    |
| Rise Time <sup>2</sup>  | $t_r$        |   |  | 18   |    |    |
| Turn-Off Delay Time <sup>2</sup>  | $t_{d(off)}$ | $V_{DS} = 125V$ ,<br>$I_D \approx 10A, V_{GS} = 10V, R_{GEN} = 6\Omega$ |  | 29   |    | nS |
| Fall Time <sup>2</sup>  | $t_f$        |   |  | 22   |    |    |
| <b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b> |              |   |  |      |    |    |
| Continuous Current  | $I_S$        |   |  |      | 10 | A  |
| Forward Voltage <sup>1</sup>  | $V_{SD}$     | $I_F = 10A, V_{GS} = 0V$  |  |      | 1  | V  |
| Diode Reverse Recovery Time   | $t_{rr}$     | $I_F = 10A, dI/dt = 100A/\mu s$   |  | 134  |    | nS |
| Diode Reverse Recovery Charge   | $Q_{rr}$     |   |  | 511  |    | nC |

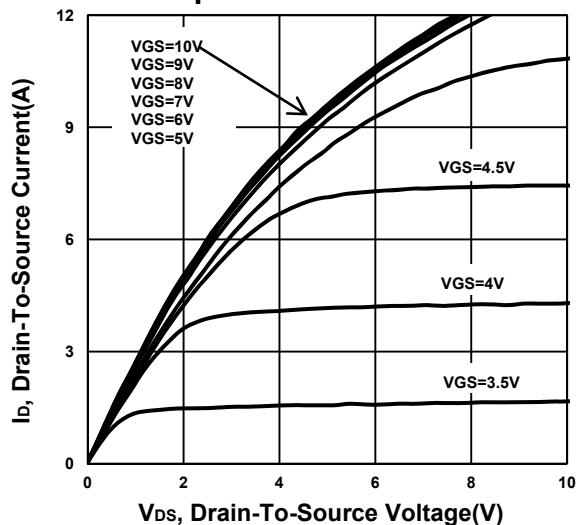
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.

**NIKO-SEM**

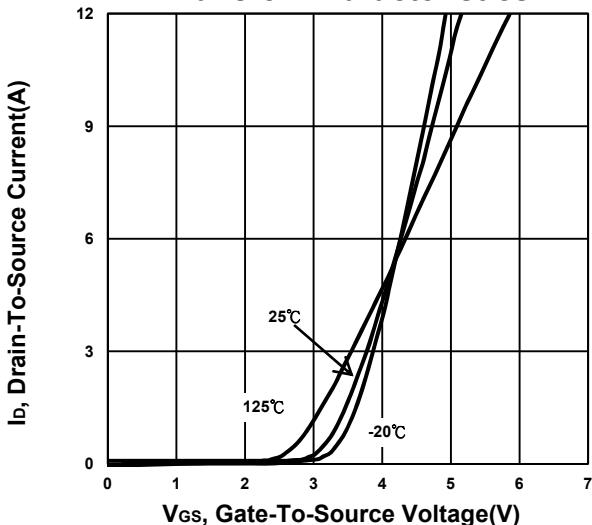
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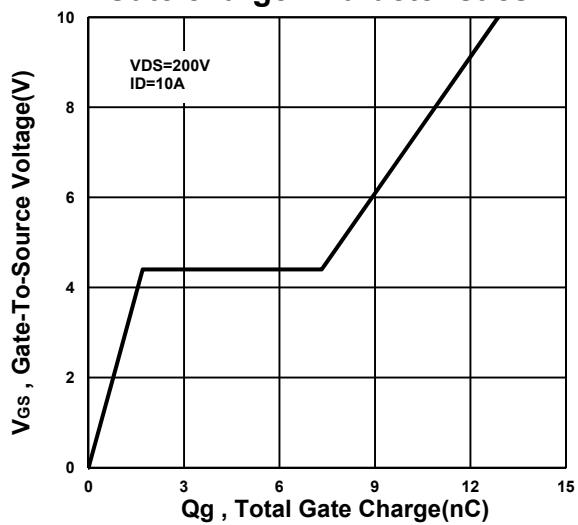
**Output Characteristics**



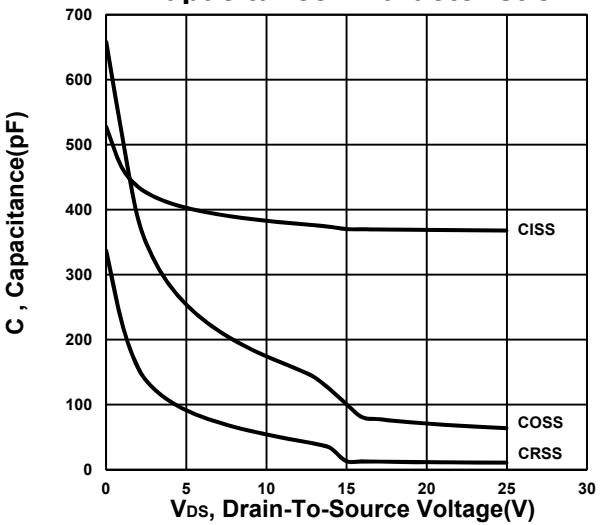
**Transfer Characteristics**



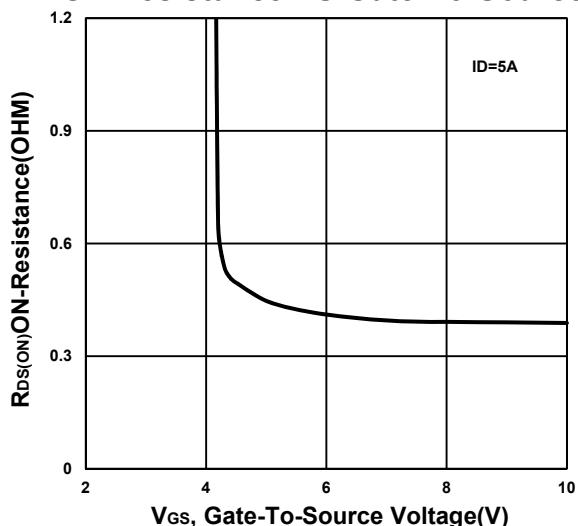
**Gate charge Characteristics**



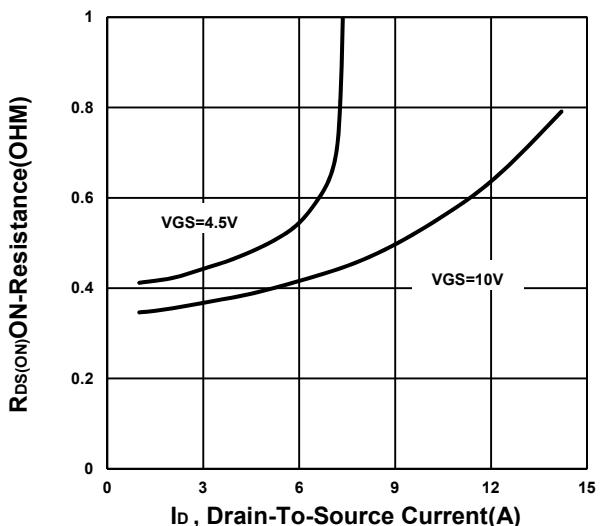
**Capacitance Characteristic**



**On-Resistance VS Gate-To-Source**



**On-Resistance VS Drain Current**



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