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SMD Power Inductor

TMPC1004H-R19YG-D

	ECN HISTORY LIST										
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN						
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SMD Power Inductor

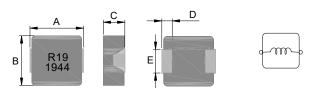
1. Features

- 1. Carbonyl Powder.
- 2. Compact design.
- 3. High current , low DCR , high efficiency.
- 4. Very low acoustic noise and very low leakage flux noise.
- 5. High reliability.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. Operating temperature -40~+125°C (Including self temperature rise)

2. Applications

Note PC power system , incl. IMVP-6 DC/DC converter .

3. Dimensions



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	
TMPC1004H	11.0±0.5	10.0±0.3	3.8±0.2	2.3±0.3	3.0±0.3	

4. Part Numbering

TMPC A	<mark>1004</mark> В	H C	-	<mark>R19</mark> D	YG - E	D F				
A: Series										
B: Dimen	BxC									
C: Type	Carbonyl Powder.									
D: Induct	D: Inductance				R19=0.19uH					
E: Induct	Y=±30%.									

F: Code

 $Y_{1}=\pm 30\%$. Marking: Black.R19 and 1944(19 YY,44 WW,follow production date).

5. Specification

Part Number	Part Number Inductance L0 (uH)		I sat(A) Typ	DCR(mΩ) Typ.@25℃	DCR(mΩ) Max.@25℃
TMPC1004H-R19YG-D	0.19±30%	36.0	70.0	0.6	0.9

Note:

1. Test frequency : Ls : 100KHz /1.0V.

2. All test data referenced to $25^\circ\!\mathrm{C}$ $\,$ ambient.

3. Testing Instrument(or equ) : L: HP4284A,CH11025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.

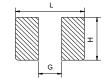
4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\ {\rm \Delta T}$ of 40 $^\circ\!{\rm C}$

5. Saturation Current (Isat) will cause L0 to drop approximately 30%.

6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

7. Special inquiries besides the above common used types can be met on your requirement.

Recommend PC Board Pattern



L(mm)	G(mm)	H(mm)					
13.6	5.4	3.5					
Note: 1. The above PCB layout reference only.							

 Recommend solder paste thickness at 0.15mm and above. P2



TMPC1004H-R19YG-D

6. Material List



NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Solder	100% Pb free solder
4	Ink	Halogen-free ketone
5	paint	Epoxy resin

7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	110~+40℃,50~60%RH (Product witht taping) 240~+125℃ (on board)	
Electrical Performance	Test	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502, Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately <u></u> L30%.	Saturation DC Current (Isat) will cause L0 to drop \triangle L(%)
Heat Rated Current (Irms)	Approximately	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃) without core loss. 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test	-	
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature: 125±2°C(Inductor) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity: 85±2 % R.H, Temperature: 85℃±2℃ Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs
Moisture Resistance	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs. 3. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs,keep at $25°C$ for 2 hrs then keep at $-10°C$ for 3 hrs 4. Keep at $25°C$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1-2 hrs.
Thermal shock		$eq:preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1: -40\pm2^\circ C 30±5min Step2: 25\pm2^\circ C \leq 0.5min Step3: 125\pm2^\circ C 30±5min Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs$
Vibration		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 10g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).

TAI-TECH

Item	Performance			Test	Con	dition	
Bending	Appearance : No damage. Impedance : within±15% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.					
Shock	Inductance:within±10% of initial value Q:Shall not exceed the specification value. RDC:within ±15% of initial value and shall not	11 1	Peak value (g's)	Norn duratio (ms	n (D)	Wave form	Velocity change (Vi)ft/sec
SHOCK	exceed the specification value	SMD Lead	50 50	11		Half-sine Half-sine	11.3 11.3
Solder ability	More than 95% of the terminal electrode should be covered with solder。	Preheat: 150°C,60sec.₀ Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C ∘ Flux for lead free: Rosin. 9.5% ∘ Dip time: 4±1sec ∘ Depth: completely cover the termination					
Resistance to Soldering Heat		Depth: con Tempera 260 (solder	ature(°C)		Ten ramp and er	ation nperature /immersion mersion rate n/s ±6 mm/s	Number of heat cycles
Terminal Strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value e	Preconditi J-STD-022 With the of tested, ap device be seconds. apply a sh	ioning: R 0DClassi compone oply a for eing test Also the	fication R nt mount ce(>0805 ed. This e force s e comport	Reflow F ted on 5:1kg , force thall be nent be	Profiles a PCB with <=0805:0.5k shall be a	mes.(IPC/JEDEC the device to be glo the side of a oplied for 60 +1 adually as not to wide thick

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Reflow Soldering

tp(245¢& / 20~40s.)

TEMPERATURE(¢⊠)

2

217

PRE-HEATING

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150 $^\circ\!\mathrm{C}$ · Never contact the ceramic with the iron tip

• 355℃ tip temperature (max) • 1.0mm tip diameter (max)

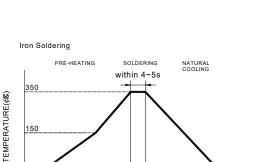
SOLDERING

TP(260°C / 10s max.)

60 ·150s NATURAL

150

Over 60s



TIME(sec.)

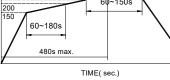
Iron Soldering times: 1 times max.

Fig.2

Gradual cooling

· Limit soldering time to 4~5sec.

Use a 20 watt soldering iron with tip diameter of 1.0mm

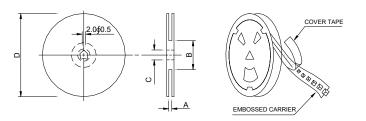


Reflow times: 3 times max.

Fig.1

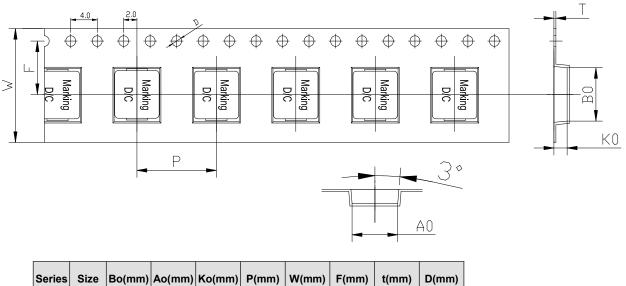
9. Packaging Information

(1) Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13+0.5/-0.2	330

(2) Tape Dimension



oeneo	0120	20(1111)	Λ ο(IIIII)	1.00(11111)	. ()	•••()	. ()	(,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	D(IIIII)
тмрс	1004	11.6±0.1	10.4±0.1	4.5±0.1	16.0±0.1	24±0.3	11.5±0.1	0.35±0.05	1.5±0.1

(3) Packaging Quantity

ТМРС	1004	
Chip / Reel	500	
Inner box	1000	
Carton	4000	

(4) Tearing Off Force

165¢%/0180¢X Top cover tape Base tape

The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Room Temp.	Room Humidity	Room atm	Tearing Speed
(°C)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

- · Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^\circ\!\!C$ and 60% RH.
- Recommended products should be used within 12 months form the time of delivery.
 The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

10. Typical Performance Curves

