



#### 100V N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> MAX T <sub>C</sub> = +25°C	
100V	9.5mΩ @ V <sub>GS</sub> = 10V	29.5A	

### **Description and Applications**

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize  $R_{DS(ON)}$  and yet maintain superior switching performance. This device is ideal for use in Notebook battery power management and Load switch.

- Backlighting
- Power Management Functions
- DC-DC Converters

#### **Features and Benefits**

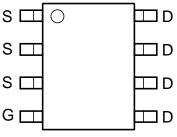
- 100% Unclamped Inductive Switch (UIS) Test in Production
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

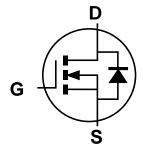
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 <sup>(3)</sup>
- Weight: 0.074 grams (Approximate)



Top View



Top View Internal Schematic



Equivalent circuit

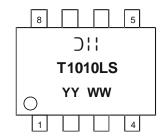
### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMT10H010LSS-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



);; = Manufacturer's Marking T1010LS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 18 = 2018) WW = Week (01 to 53)



### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	100	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Dusin Compat (Note C) 1/ 401/	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	11.5 9.2	А
Continuous Drain Current (Note 6), V <sub>GS</sub> = 10V	Steady State	T <sub>C</sub> = +25°C T <sub>C</sub> = +100°C	I <sub>D</sub>	29.5 18.6	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	75	Α
Maximum Continuous Body Diode Forward Current (Note 6)			Is	3	Α
Avalanche Current (Note 8), L=0.3mH			I <sub>AS</sub>	10	Α
Avalanche Energy (Note 8), L=0.3mH			E <sub>AS</sub>	15	mJ

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

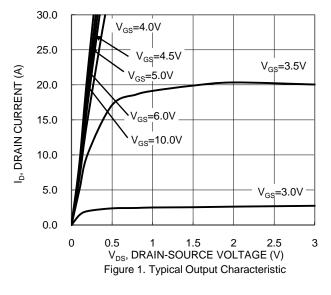
Characteristic	Symbol	Value	Unit	
Total Power (Note 5)		$P_{D}$	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	<u> </u>	90	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	48.8	
Total Power Dissipation (Note 6)		$P_{D}$	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	<u> </u>	66	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	35.8	
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	10.1	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

# **Electrical Characteristics** (T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	1	_	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.4	1.9	2.8	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
		1	8	9.5		$V_{GS} = 10V, I_D = 13A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		9	12	mΩ	$V_{GS} = 6V, I_D = 13A$	
		_	10	14.5		$V_{GS} = 4.5V, I_D = 5A$	
Diode Forward Voltage	$V_{SD}$	_	0.8	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 13A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	l	4166	_			
Output Capacitance	Coss		764	_	pF	$V_{DS} = 50V$ , $V_{GS} = 0V$ f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	44	_		I = IIVINZ	
Gate Resistance	Rg	_	2	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	$Q_g$		58.4	_		V 50V L 42A	
Gate-Source Charge	$Q_{gs}$	_	11.4	_	nC	$V_{DD} = 50V, I_D = 13A,$ $V_{GS} = 10V$	
Gate-Drain Charge	$Q_{gd}$	_	14.2	_		GS = 10V	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	11.6	_			
Turn-On Rise Time	t <sub>R</sub>	_	14.1	_		$V_{DD} = 50V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	42.9	_	ns	$I_D = 13A$ , $R_g = 6\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	22	_		-	
Reverse Recovery Time	t <sub>RR</sub>	_	49.8	_	ns	1 12A di/dt 100A/us	
Reverse Recovery Charge	$Q_{RR}$	_	85.1	_	nC	I <sub>F</sub> = 13A, di/dt = 100A/μs	

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.





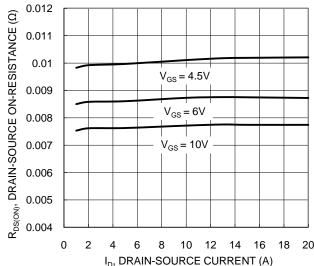


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

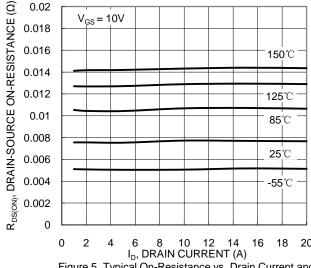
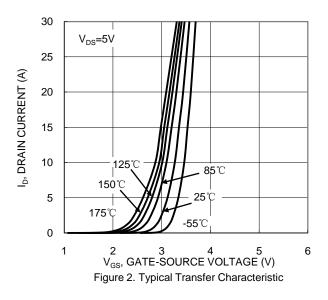
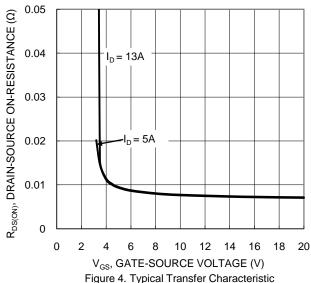


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





2.5

Wos = 10V, I<sub>D</sub> = 13A

V<sub>GS</sub> = 6V, I<sub>D</sub> = 13A

V<sub>GS</sub> = 6V, I<sub>D</sub> = 13A

V<sub>GS</sub> = 4.5V, I<sub>D</sub> = 5A

V<sub>GS</sub> = 4.5V, I<sub>D</sub> = 5A

T<sub>J</sub>, JUNCTION TEMPERATURE (°C)



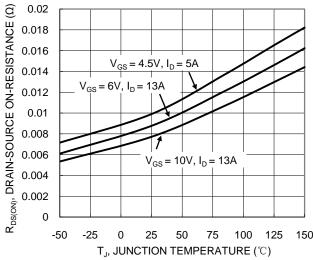
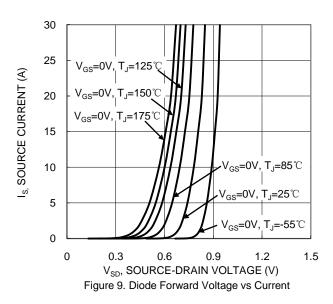


Figure 7. On-Resistance Variation with Temperature



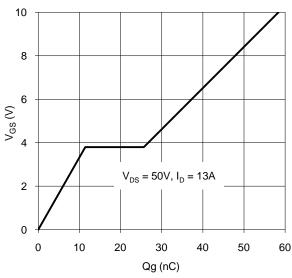


Figure 11. Gate Charge

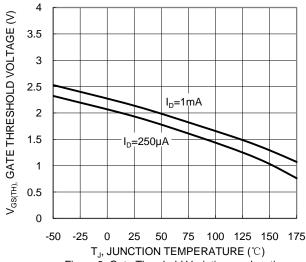
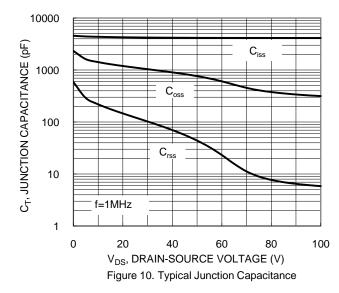


Figure 8. Gate Threshold Variation vs. Junction Temperature



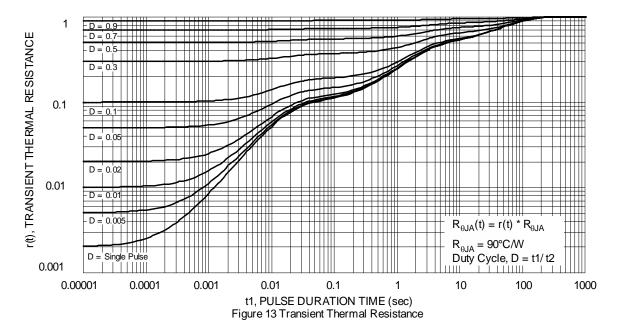
100

R<sub>DS(on)</sub>

10

10 (V) (V)

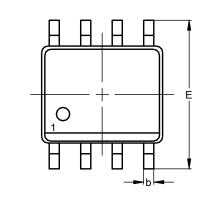


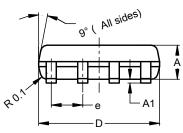


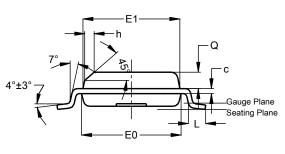


### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.





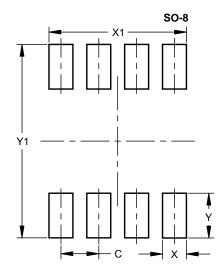


SO-8

SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е			1.27		
h	-		0.35		
L	0.62	0.82	0.72		
Q	0.60	0.70	0.65		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)			
С	1.27			
Х	0.802			
X1	4.612			
Y	1.505			
Y1	6.50			



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