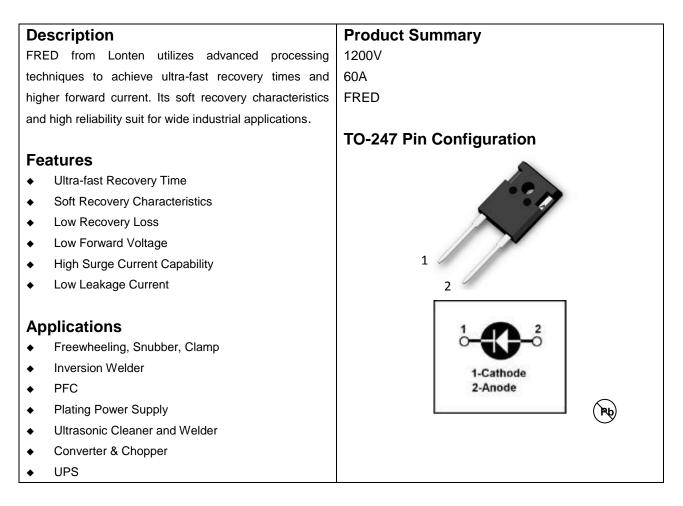


1200V 60A Ultra-Fast Recovery Diode



Absolute Maximum Ratings T_c = 25°C unless otherwise noted

Parameter	Symbol	Value	Unit	
Maximum D.C. Reverse Voltage	V _R	1200	V	
Maximum Repetitive Reverse Voltage	V _{RRM}	1200	V	
Average Forward Current(Tc = 110° C)	I _{F(AV)}	60	А	
RMS Forward Current(Tc = $110^{\circ}C$)	I _{F(RMS)}	84	А	
Non-Repetitive Surge Forward Current(TJ =	I _{FSM}	500	А	
45℃,t=10ms,50Hz,Sine)				
Power Dissipation	P _D	312	W	
Junction Temperature Range	TJ	-55 to +150	°C	
Storage Temperature Range	T _{STG}	-55 to +150	°C	
Module-to-Sink(Recommended M3)	Torque	1.1	Nm	
	Weight	6.0	g	

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	0.4	°C/W

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LDB120U60W4

Package Marking and Ordering Information

Device	Device Package	Marking	
LDB120U60W4	TO-247	LDB120U60W4	

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{RM} Reverse Leakage Current	Deserve hardware Orment	V _R =1200V			10	uA
	Reverse Leakage Current	V _R =1200V, T _J =125℃			1000	uA
V _F Forward Voltage		I _F =60A		2.8	3.3	V
	I _F =60A, T _J =125℃		2.3		V	
t _{rr} Reverse Recovery Time	I _F =1A, V _R =30V, di _F /dt=-200A/us		27		ns	
	$I_F=0.5A, I_R=1A$ $I_{RR}=0.25A,$		65	80	ns	
t _{rr}	Reverse Recovery Time	V _R =600V, I _F =60A		75		ns
I _{RRM}	Max. Reverse Recovery Current	di _⊧ /dt=-200A/us, Tյ=25℃		9		А
t _{rr}	Reverse Recovery Time	V _R =600V, I _F =30A		180		ns
I _{RRM}	Max. Reverse Recovery Current	di _F /dt=-200A/us, TJ=125℃		17		А

Electrical Characteristics T_J = 25°C unless otherwise noted

Electrical Characteristics Diagrams

Figure 1. Forward Voltage Drop vs Forward Current

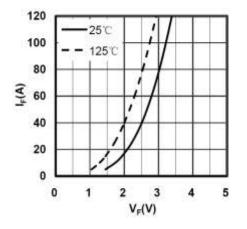
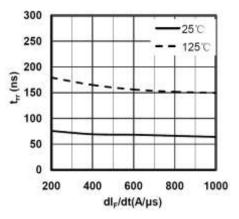


Figure 2. Reverse Recovery Time vs diF/dt





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Figure 3. Reverse Recovery Current vs diF/dt

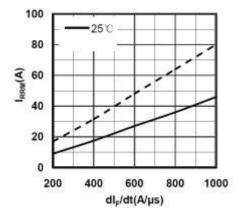


Figure 5. Forward current vs Case temperature

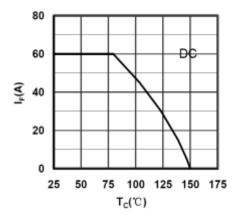


Figure 4. Reverse Recovery Charge vs diF/dt

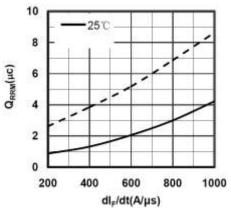


Figure 6. Transient Thermal Impedance

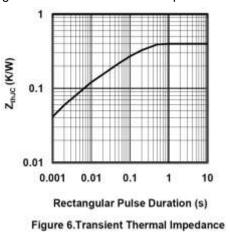
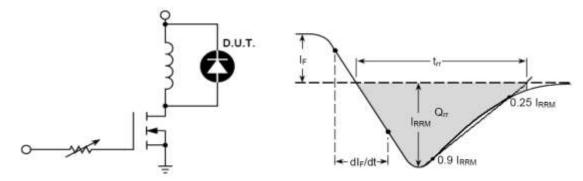


Figure 7. Diode Reverse Recovery Test Circuit and Waveform

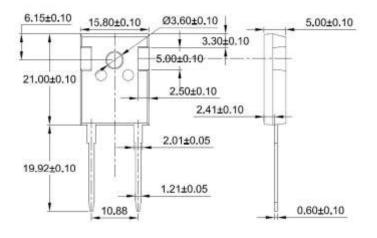


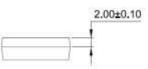


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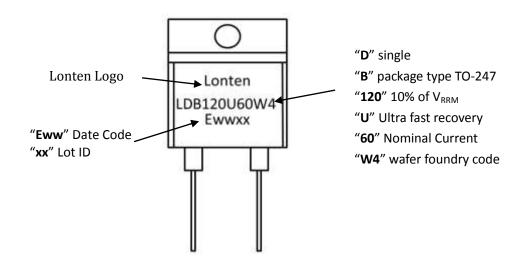
Figure 8. Package Outline

Dimensions in Millimeters





Marking Information





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