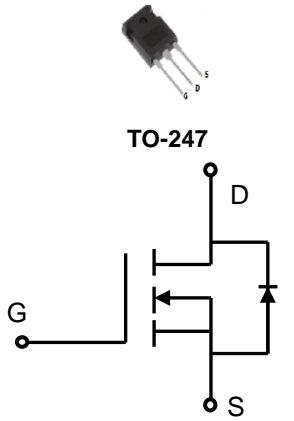


Lonten N-channel 600V, 54A, 0.066Ω LonFET™ Power MOSFET

| | |
|--|--|
| <p>Description LonFET™ Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.</p> <p>Features</p> <ul style="list-style-type: none"> ◆ Ultra low $R_{DS(on)}$ ◆ Ultra low gate charge (typ. $Q_g = 87\text{nC}$) ◆ 100% UIS tested ◆ RoHS compliant <p>Applications</p> <ul style="list-style-type: none"> ◆ Power factor correction (PFC). ◆ Switched mode power supplies (SMPS). ◆ Uninterruptible power supply (UPS). | <p>Product Summary</p> <p>$V_{DS} @ T_{j,max}$ 650V</p> <p>$R_{DS(on),max}$ 0.066Ω</p> <p>I_{DM} 135A</p> <p>$Q_{g,typ}$ 87nC</p> <div style="text-align: center;">  <p>TO-247</p> <p>N-Channel MOSFET</p> </div> |
|--|--|

Absolute Maximum Ratings

| Parameter | Symbol | Value | Unit |
|---|----------------|-------------|------|
| Drain-Source Voltage | V_{DSS} | 600 | V |
| Continuous drain current ($T_C = 25^\circ\text{C}$) | I_D | 54 | A |
| ($T_C = 100^\circ\text{C}$) | | 30 | A |
| Pulsed drain current ¹⁾ | I_{DM} | 135 | A |
| Gate-Source voltage | V_{GSS} | ± 30 | V |
| Avalanche energy, single pulse ²⁾ | E_{AS} | 1200 | mJ |
| Power Dissipation TO-247 ($T_C = 25^\circ\text{C}$) | P_D | 290 | W |
| - Derate above 25°C | | 2.32 | W/°C |
| Operating and Storage Temperature Range | T_J, T_{STG} | -55 to +150 | °C |
| Continuous diode forward current | I_S | 54 | A |
| Diode pulse current | $I_{S,pulse}$ | 135 | A |

Thermal Characteristics TO-247

| Parameter | Symbol | Value | Unit |
|---|-----------------|-------|------|
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 0.43 | °C/W |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 60 | °C/W |
| Soldering temperature, wavesoldering only allowed at leads. (1.6mm from case for 10s) | T_{sold} | 260 | °C |

Package Marking and Ordering Information

| Device | Device Package | Marking | Units/Tube | Units/Real |
|-------------|----------------|-------------|------------|------------|
| LSB60R066GF | TO-247 | LSB60R066GF | 30 | |

Electrical Characteristics
 $T_c = 25^\circ\text{C}$ unless otherwise noted

| Parameter | Symbol | Test Condition | Min. | Typ. | Max. | Unit |
|--------------------------------------|---------------|--|------|-------|--------|---------------|
| Static characteristics | | | | | | |
| Drain-source breakdown voltage | BV_{DSS} | $V_{GS}=0\text{ V}, I_D=0.25\text{ mA}$ | 600 | - | - | V |
| Gate threshold voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=0.25\text{ mA}$ | 2 | 3 | 4 | V |
| Drain cut-off current | I_{DSS} | $V_{DS}=600\text{ V}, V_{GS}=0\text{ V},$ $T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$ | - | - | 1 | μA |
| Gate leakage current, Forward | I_{GSSF} | $V_{GS}=30\text{ V}, V_{DS}=0\text{ V}$ | - | - | 50 | nA |
| Gate leakage current, Reverse | I_{GSSR} | $V_{GS}=-30\text{ V}, V_{DS}=0\text{ V}$ | - | - | -50 | nA |
| Drain-source on-state resistance | $R_{DS(on)}$ | $V_{GS}=10\text{ V}, I_D=27\text{ A}$ $T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$ | - | 0.060 | 0.0660 | Ω |
| | | | - | 0.13 | - | |
| Dynamic characteristics | | | | | | |
| Input capacitance | C_{iss} | $V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$ | - | 4677 | - | pF |
| Output capacitance | C_{oss} | | - | 2556 | - | |
| Reverse transfer capacitance | C_{rss} | | - | 30 | - | |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD} = 400\text{ V}, I_D = 27\text{ A}$ $R_G = 10\Omega, V_{GS}=10\text{ V}$ | - | 29.0 | - | ns |
| Rise time | t_r | | - | 12.8 | - | |
| Turn-off delay time | $t_{d(off)}$ | | - | 191.6 | - | |
| Fall time | t_f | | - | 13.6 | - | |
| Gate charge characteristics | | | | | | |
| Gate to source charge | Q_{gs} | $V_{DD}=480\text{ V}, I_D=27\text{ A},$ $V_{GS}=0\text{ to }10\text{ V}$ | - | 24 | - | nC |
| Gate to drain charge | Q_{gd} | | - | 31.24 | - | |
| Gate charge total | Q_g | | - | 87 | - | |
| Gate plateau voltage | $V_{plateau}$ | | - | 5.5 | - | V |
| Reverse diode characteristics | | | | | | |
| Diode forward voltage | V_{SD} | $V_{GS}=0\text{ V}, I_F=27\text{ A}$ | - | 1.0 | - | V |
| Reverse recovery time | t_{rr} | $V_R=50\text{ V}, I_F=47\text{ A},$ $dI_F/dt=100\text{ A}/\mu\text{s}$ | - | 234 | - | ns |
| Reverse recovery charge | Q_{rr} | | - | 1.65 | - | μC |
| Peak reverse recovery current | I_{rrm} | | - | 12.9 | - | A |

Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- $I_{AS} = 8\text{ A}, V_{DD} = 60\text{ V},$ Starting $T_j = 25^\circ\text{C}.$

Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics

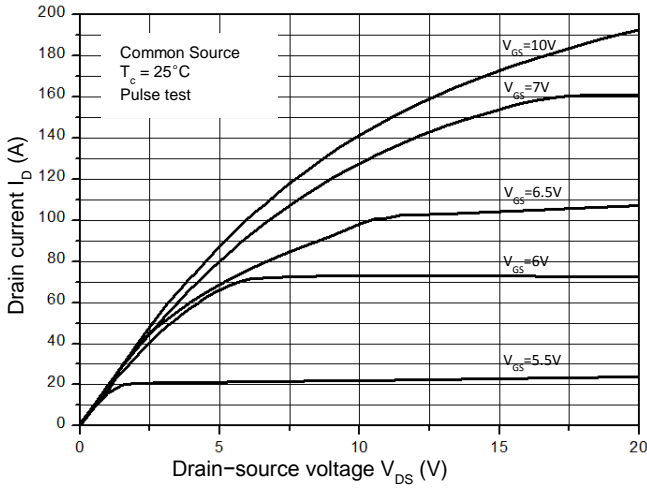


Figure 2. Transfer Characteristics

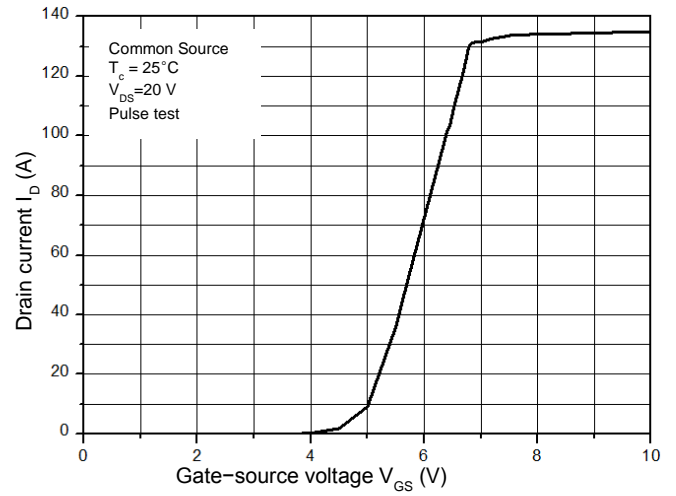


Figure 3. On-Resistance Variation vs. Drain Current

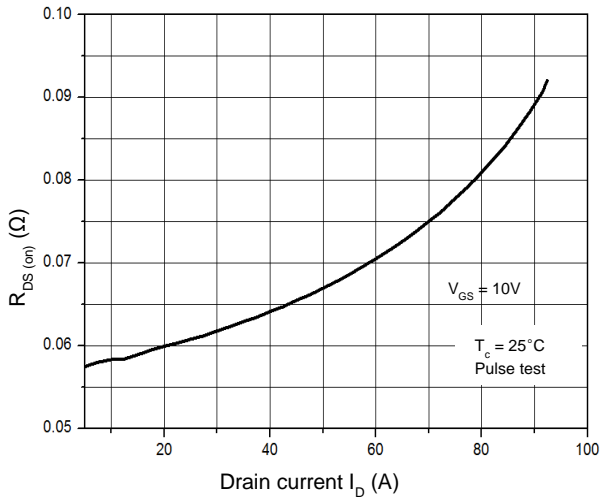


Figure 4. Threshold Voltage vs. Temperature

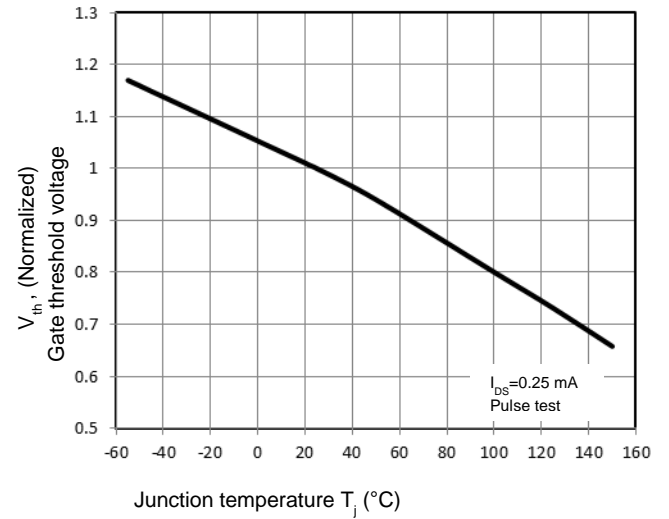


Figure 5. Breakdown Voltage vs. Temperature

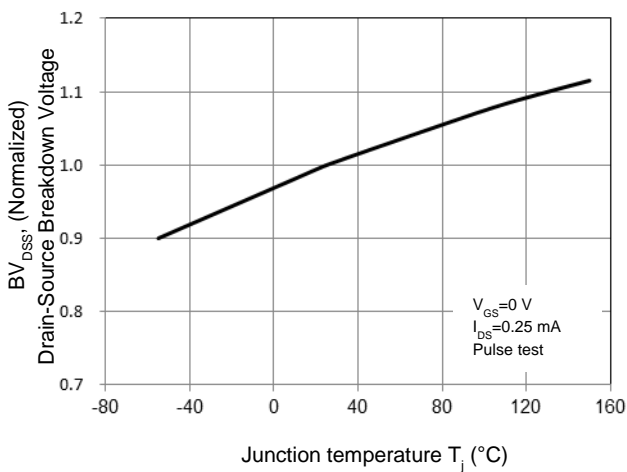


Figure 6. On-Resistance vs. Temperature

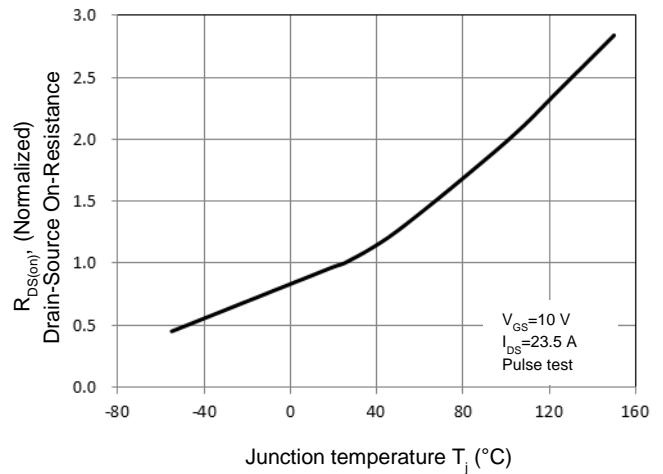


Figure 7. Capacitance Characteristics

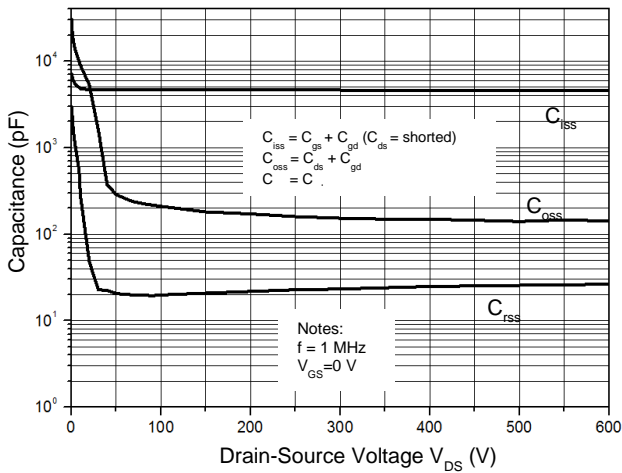


Figure 8. Gate Charge Characterist

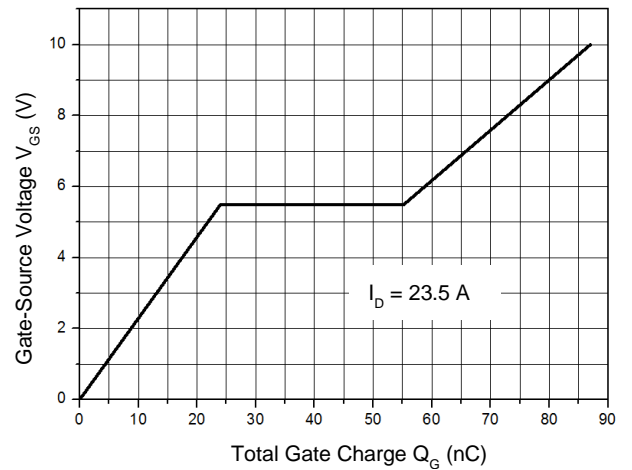


Figure 9. Maximum Safe Operating Area

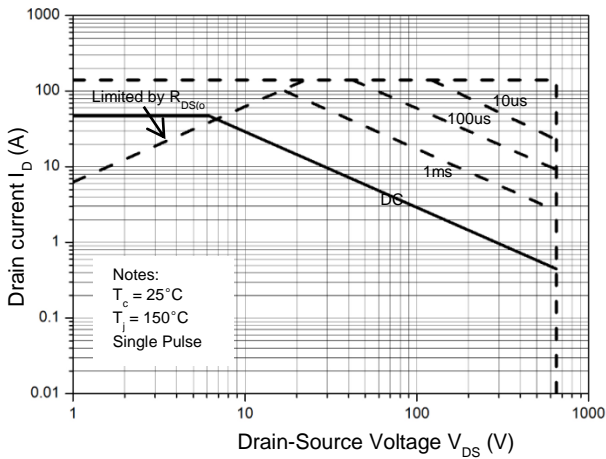


Figure 10. Power Dissipation vs. Temperature

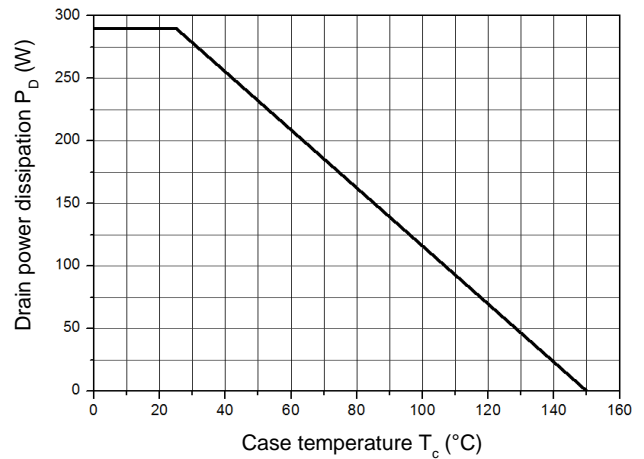
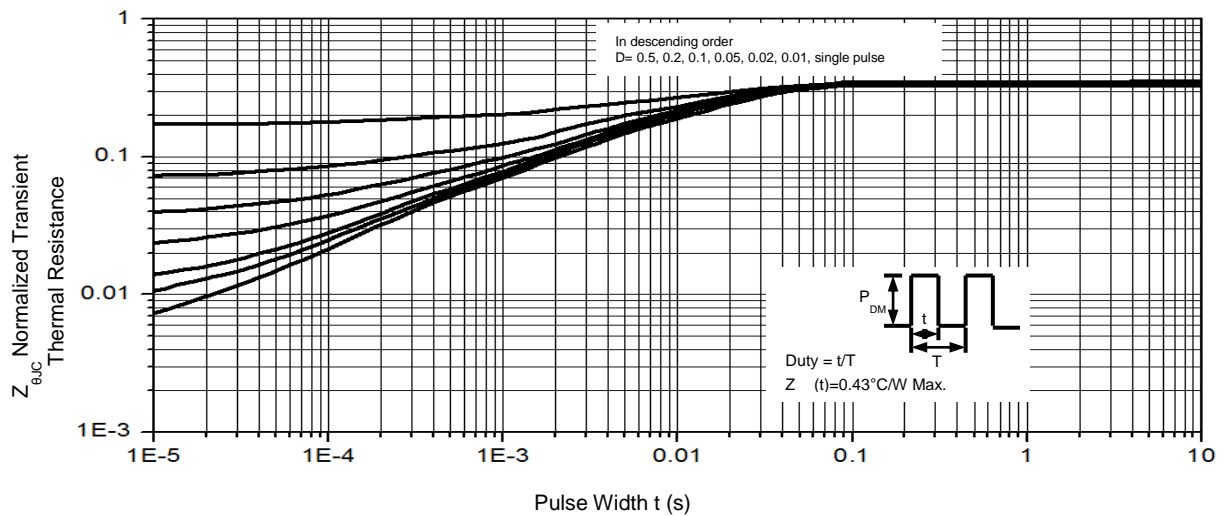
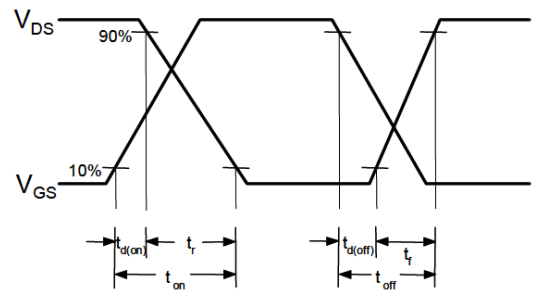
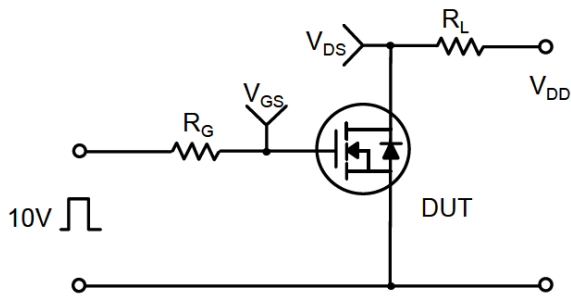
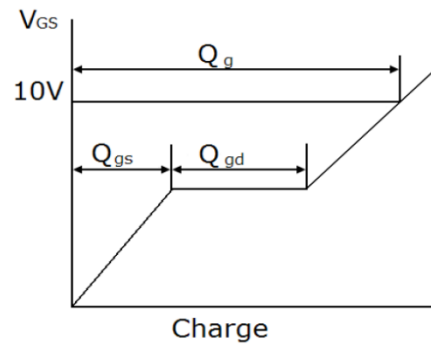
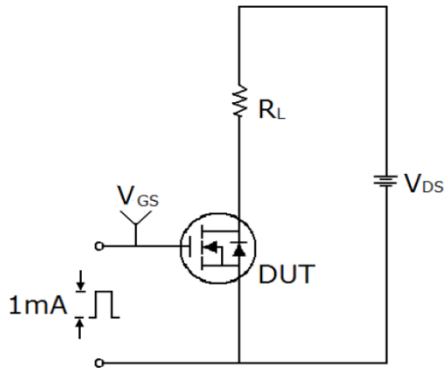


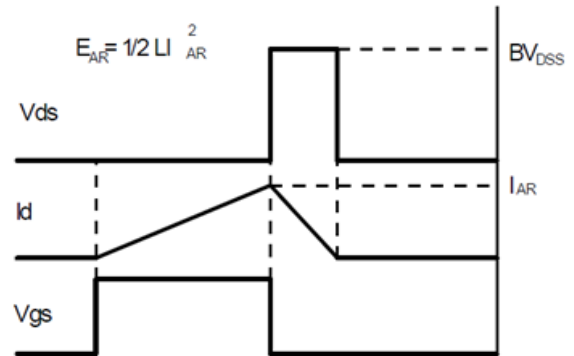
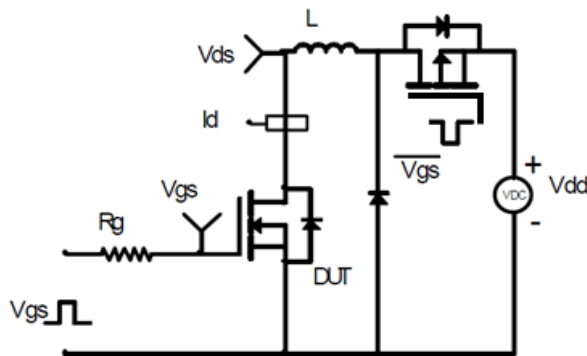
Figure 11. Transient Thermal Response Curve



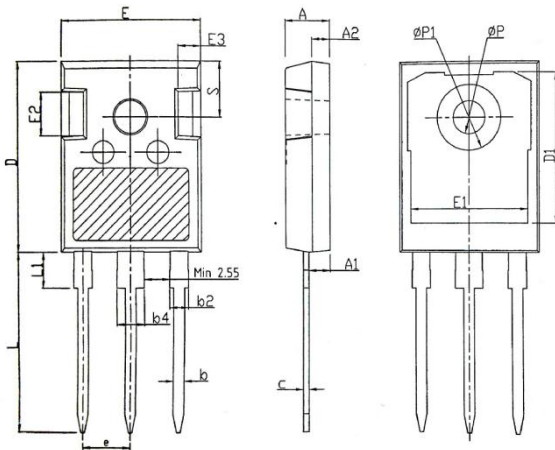
Gate Charge Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveforms

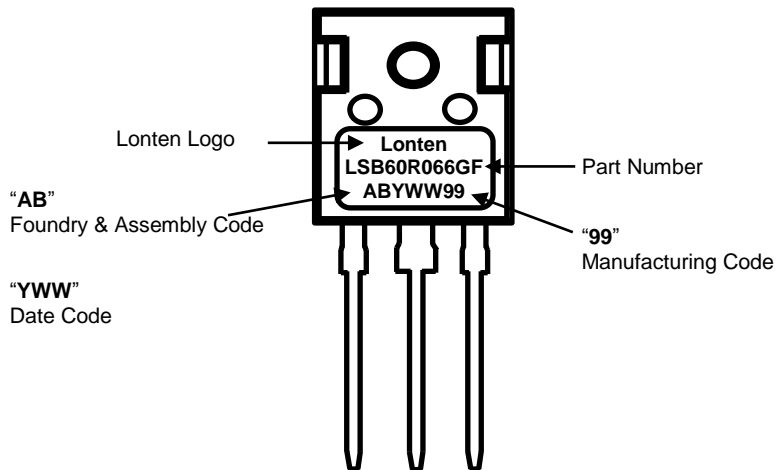


Mechanical Dimensions for TO-247



| SYMBOL | mm | | |
|--------|---------|-------|-------|
| | MIN | NOM | MAX |
| A | 4.80 | 5.00 | 5.20 |
| A1 | 2.21 | 2.41 | 2.59 |
| A2 | 1.85 | 2.00 | 2.15 |
| b | 1.11 | 1.21 | 1.36 |
| b2 | 1.91 | 2.01 | 2.21 |
| b4 | 2.91 | 3.01 | 3.21 |
| c | 0.51 | 0.61 | 0.75 |
| D | 20.80 | 21.00 | 21.30 |
| D1 | 16.25 | 16.55 | 16.85 |
| E | 15.50 | 15.80 | 16.10 |
| E1 | 13.00 | 13.30 | 13.60 |
| E2 | 4.80 | 5.00 | 5.20 |
| E3 | 2.30 | 2.50 | 2.70 |
| e | 5.44BSC | | |
| L | 19.82 | 19.92 | 20.22 |
| L1 | — | — | 4.30 |
| ØP | 3.40 | 3.60 | 3.80 |
| ØP1 | — | — | 7.30 |
| S | 6.15BSC | | |

TO-247 Part Marking Information



Disclaimer

The content specified herein is for the purpose of introducing LONTEN's products (hereinafter "Products"). The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. Examples of application circuits, circuit constants and any other information contained herein illustrate the standard usage and operations of the Products. The peripheral conditions must be taken into account when designing circuits for mass production.

LONTEN does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of the Products or technical information described in this document.

The Products are not designed or manufactured to be used with any equipment, device or system which requires an extremely high level of reliability the failure or malfunction of which may result in a direct threat to human life or create a risk of human injury (such as a medical instrument, transportation equipment, aerospace machinery, nuclear-reactor controller, fuel-controller or other safety device). LONTEN shall bear no responsibility in any way for use of any of the Products for the above special purposes.

Although LONTEN endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a LONTEN product.

The content specified herein is subject to change for improvement without notice. When using a LONTEN product, be sure to obtain the latest specifications.

Apr. 2018 Revision 2.0