

\* H Q H U D O ' H V F U L S W L R Q

3 U R G X F W 6 X P P H U \

7 K H W S F 1 5 N 1 0  
N S F

% 9 ' 6 6	5 ' 6 2 1	, '
100V	m	1 A

S N

W S F 1 5 N 1 0

100 S S

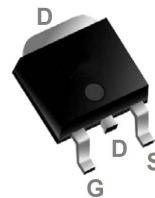
\$ S S O L F D W L R Q V

- z High Frequency Point-of-Load Synchronous Buck Converter
- z Networking DC-DC Power System
- z Load Switch

) H D W X U H V

7 2 3 I & Q I L J X U D W L R Q

- z Advanced high cell density Trench technology
- z Super Low Gate Charge
- z Excellent Cdv/dt effect decline
- z Green Device Available



\$ E V R O X W H 0 D [ L P X P 5 D W L Q J V

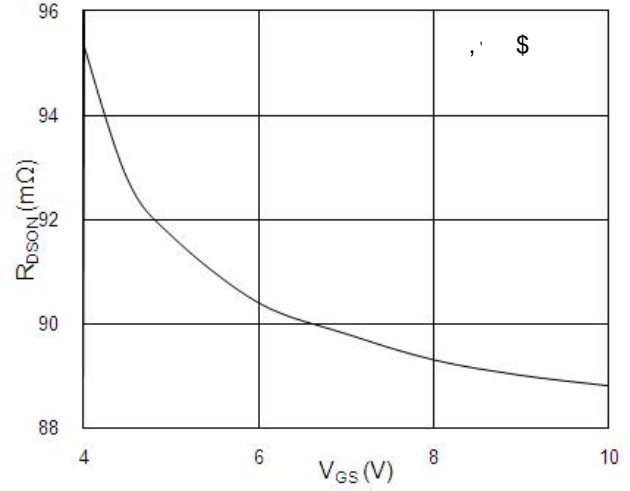
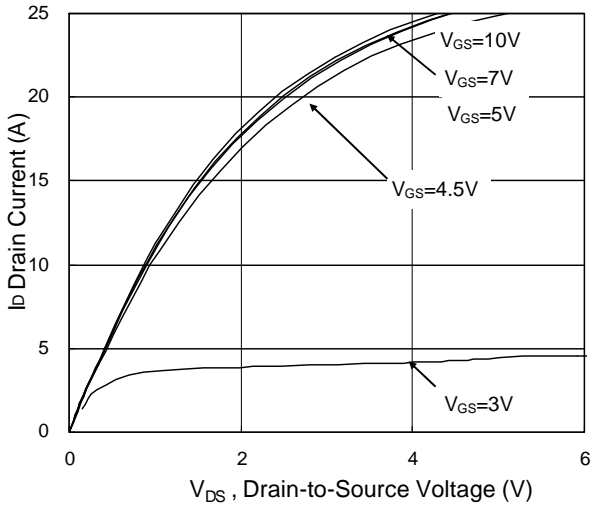
6 \ P E R O	3 D U D P H W H U	5 D W L Q J	8 Q L W V
V <sub>DS</sub>	Drain-Source Voltage	100	V
V <sub>GS</sub>	Gate-Source Voltage	f20	V
I <sub>D</sub> @T <sub>C</sub> =25	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>		A
I <sub>D</sub> @T <sub>C</sub> =100	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>		A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>		A
EAS	Single Pulse Avalanche Energy <sup>3</sup>		mJ
I <sub>AS</sub>	Avalanche Current		A
P <sub>D</sub> @T <sub>C</sub> =25	Total Power Dissipation <sup>3</sup>		W
P <sub>D</sub> @T <sub>⊕</sub>	Total Power Dissipation <sup>3</sup>		W
T <sub>STG</sub>	Storage Temperature Range	-55 to 1 0	
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 1 0	

7 K H U P D O ' D W D

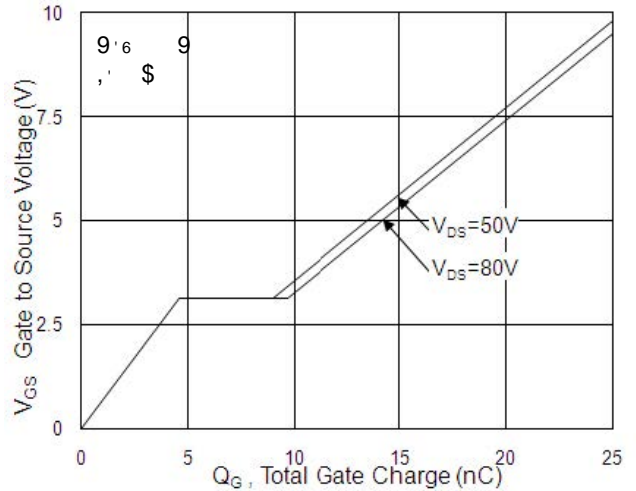
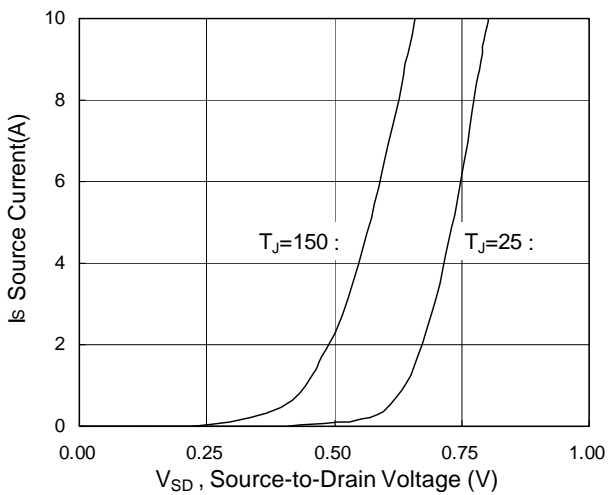
6 \ P E R O	3 D U D P H W H U	7 \ S	0 D [	8 Q L W V
R <sub>JA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	---		/W
R <sub>JC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---		/W



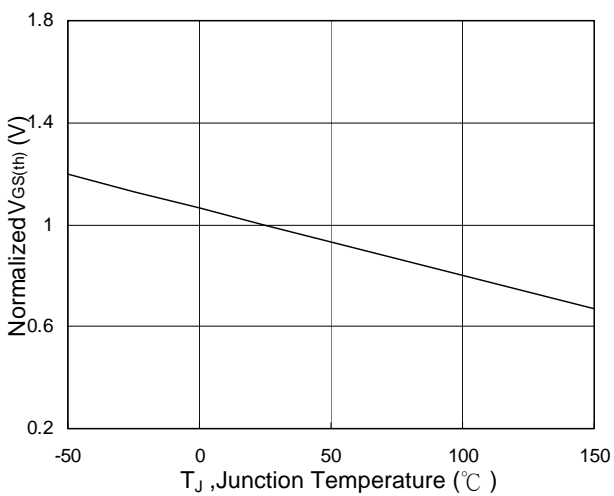
7 \ S L F D O & K D U D F W H U L V W L F V



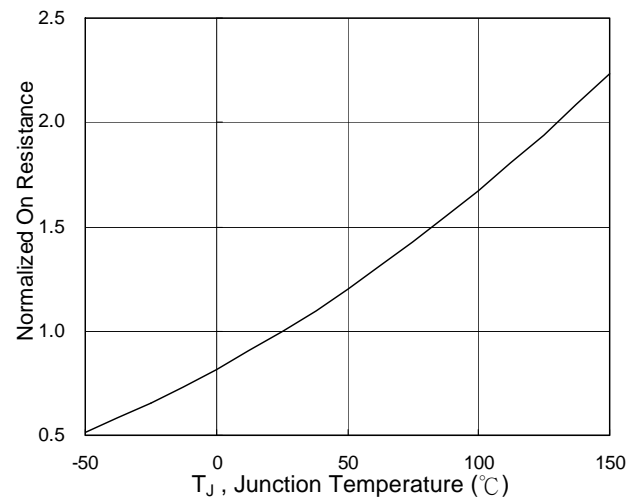
) L J \ S L F D O 2 X W S X W & K D U D F W H U L V W L F V ) L J 2 Q 5 H V L V W D W H 6 V X U F H



) L J ) R U 2 K D U D F W H U L V W L F V H

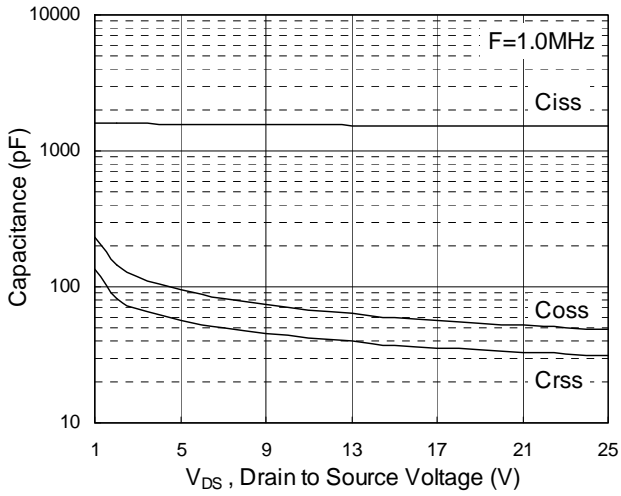


) L J \* D W H & K D U D F W H U L V W L F V

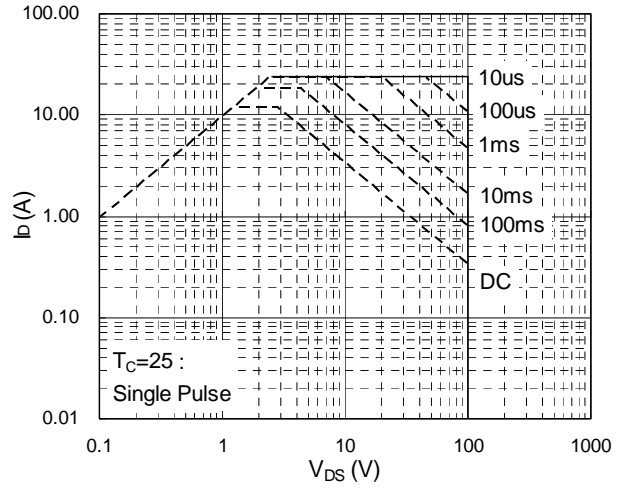


) L J 1 R U P D Q L J H G . 9 7

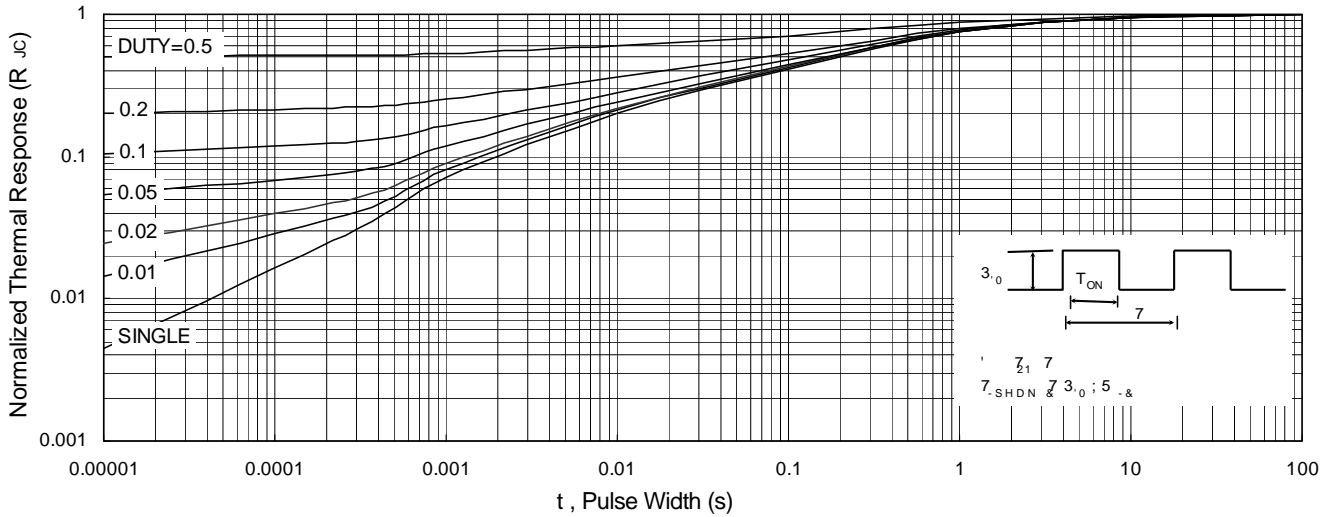
) L J 1 R U P D Q L J H G . 5 7



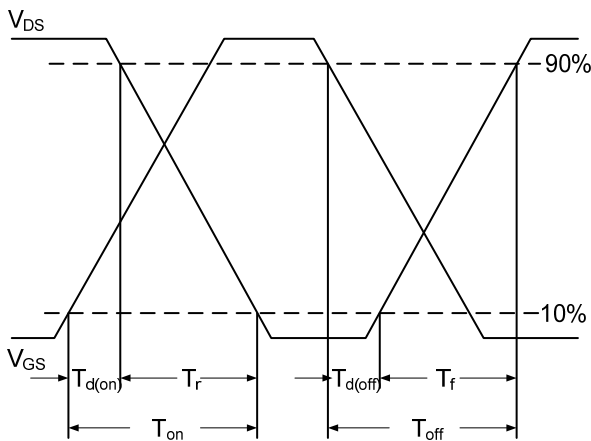
) LJ & DSDFLWDQFH



) LJ 6DIH 2SHUDWLQJ \$UHD

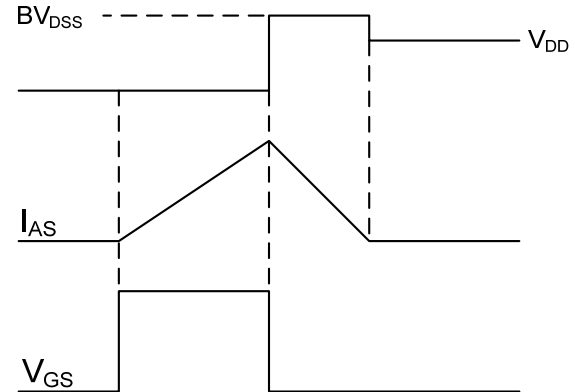


) LJ 1RUPDOLJHG 0D[LPXP 7UDQVLHQW 7KHUPDO ,PSHGDO



) LJ 6ZLWFKLQJ 7LPH :DYHIRU) LJ 8QFODPSHG ,QGXFWLYH 6ZLWF

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$



1, Any and all Winsok power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Winsok power representative nearest you before using any Winsok power products described or contained herein in such applications.

2, Winsok power assumes responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Winsok power products described or contained herein.

3, Specifications of any and all Winsok power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should evaluate and test devices mounted in the customer's products or equipment.

4, Winsok power Semiconductor CO., LTD. strives to supply high quality products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to property. When designing equipment, adopt safety measures so that these kinds of events do not occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

5, In the event that any or all Winsok power products (including technical data, services) described or contained herein are controlled under any of applicable export control laws and regulations, products must not be exported without obtaining the export license from authorities concerned in accordance with the above law.

6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, any information storage or retrieval system, or otherwise, without the prior written permission of Winsok power Semiconductor CO., LTD.

7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Winsok power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

8, Any and all information described or contained herein is subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Winsok power product that you intend to use.

9, this catalog provides information as of Sep.2014. Specifications and information herein are subject to change without notice.