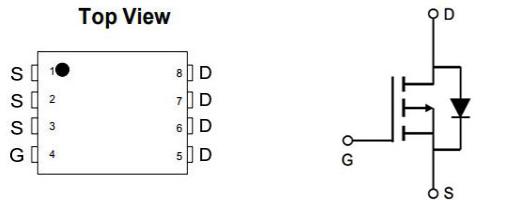
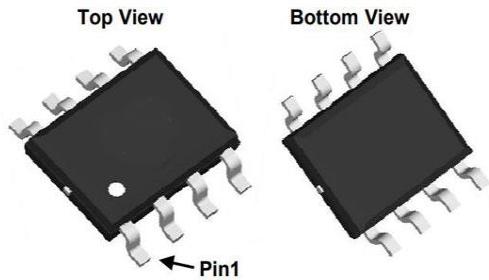


-30V /-6A Single P Power MOSFET
General Description

-30V /-6A Single P Power MOSFET
 Very low on-resistance RDS(on) @ VGS=4.5 V
 Pb-free lead plating; RoHS compliant

V_{DS}	-30	V
R_{DS(on),TYP}@VGS=10V	59.5	mΩ
R_{DS(on),TYP}@VGS=4.5	93.5	mΩ
I_D	-6	A


 100% UIS Tested
 100% RG Tested

Part ID	Package Type	Marking	Tape and reel infomation
SM4405PRL	SOP8	4405	3000

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	20	±V
Continuous Drain Current A	I _D	-6.0	A
		-5.1	
Pulsed Drain Current B	I _{DM}	-9.6	
Avalanche Current G	I _{AR}	-1.9	
Repetitive avalanche energy L=0.1mH G	E _{AR}	-4.4	mJ
Power Dissipation A	P _D	3.1	W
		2	
Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient A	R _{θJA}	212	318	°C/W
Maximum Junction-to-Ambient A		425	510	°C/W
Maximum Junction-to-Lead c	R _{θJL}	127	204	°C/W

STATIC PARAMETERS

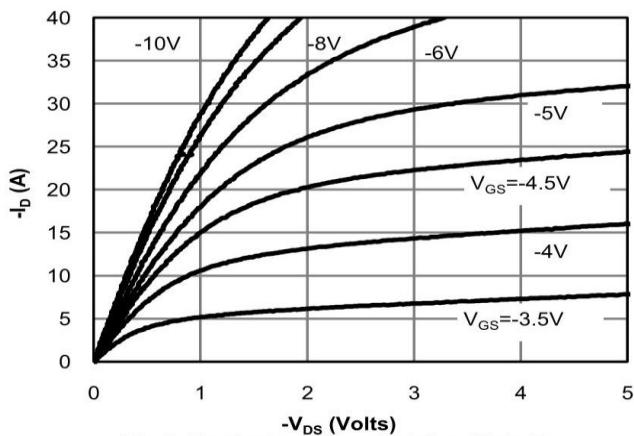
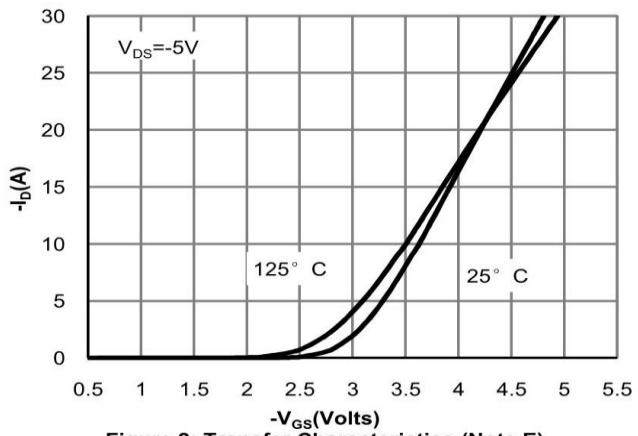
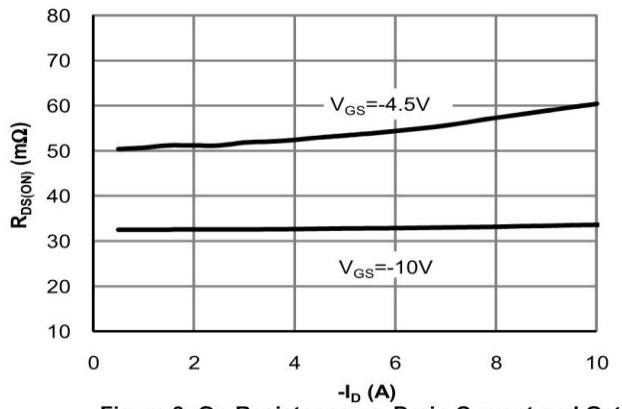
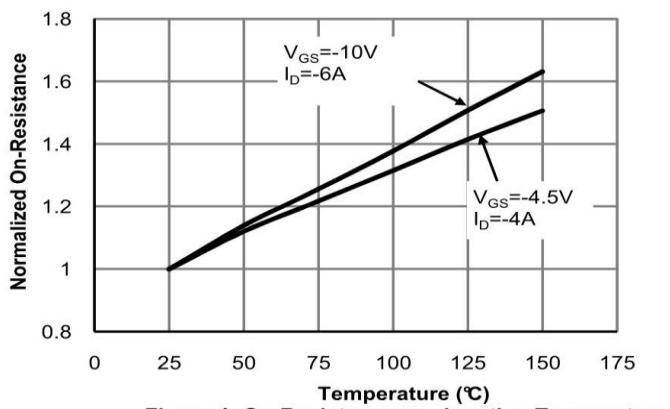
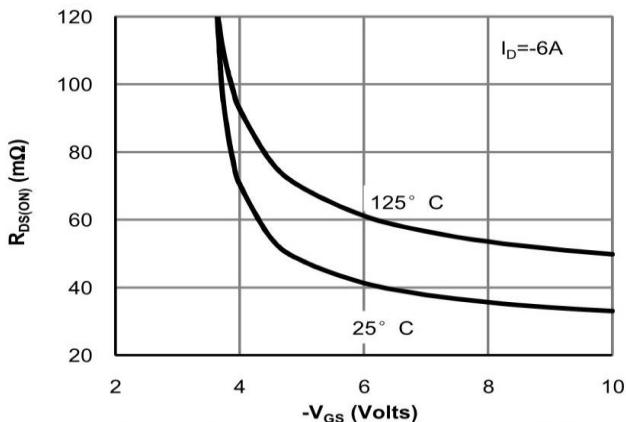
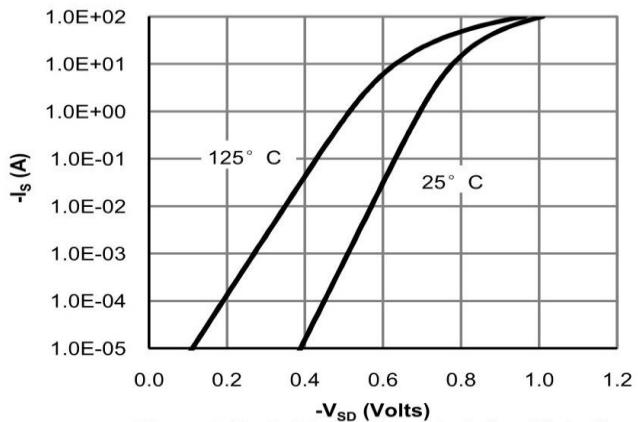
Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = -250\mu A, V_{GS} = 0V$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-30V, V_{GS}=0V$			-1	uA
					-5	
I_{GSS}	Gate-Body leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	-1.2	-1.8	-2.4	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-6A$		59.5	85.0	mΩ
		$V_{GS}=-4.5V, I_D=-6A$		93.5	121.6	
g_{FS}	Forward Transconductance	$V_{DS}=-5V, I_D=-6A$		49		S
V_{SD}	Diode Forward Voltage	$I_S=-1A, V_{GS}=0V$		-0.72	-1	V
I_S	Maximum Body-Diode Continuous Current				-6	A

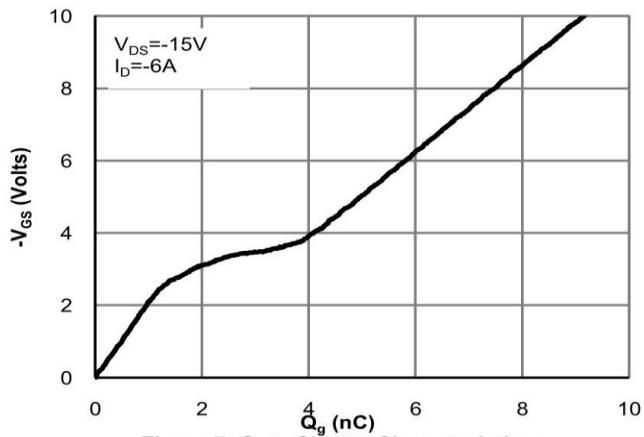
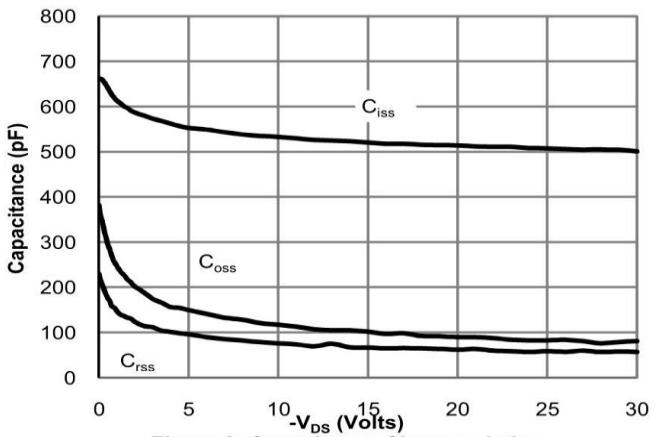
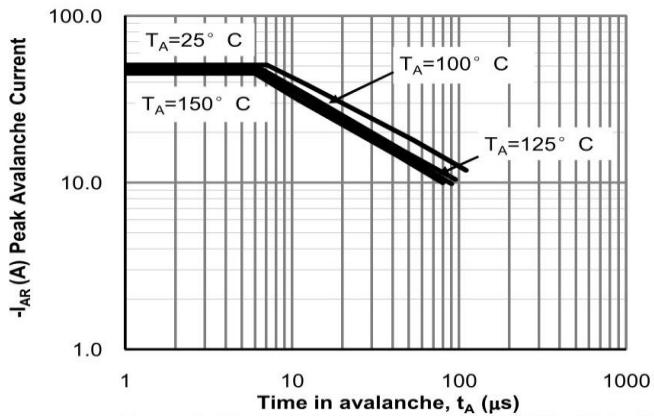
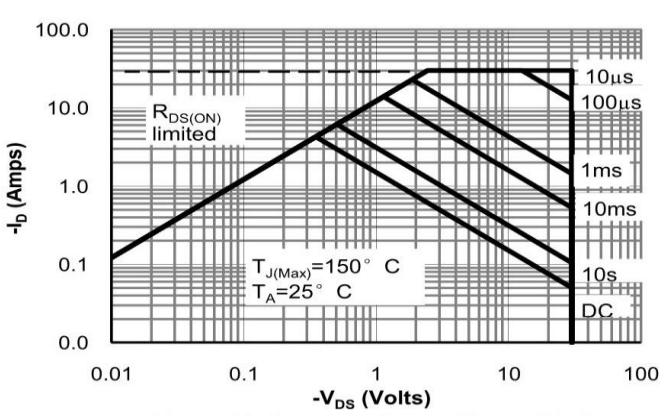
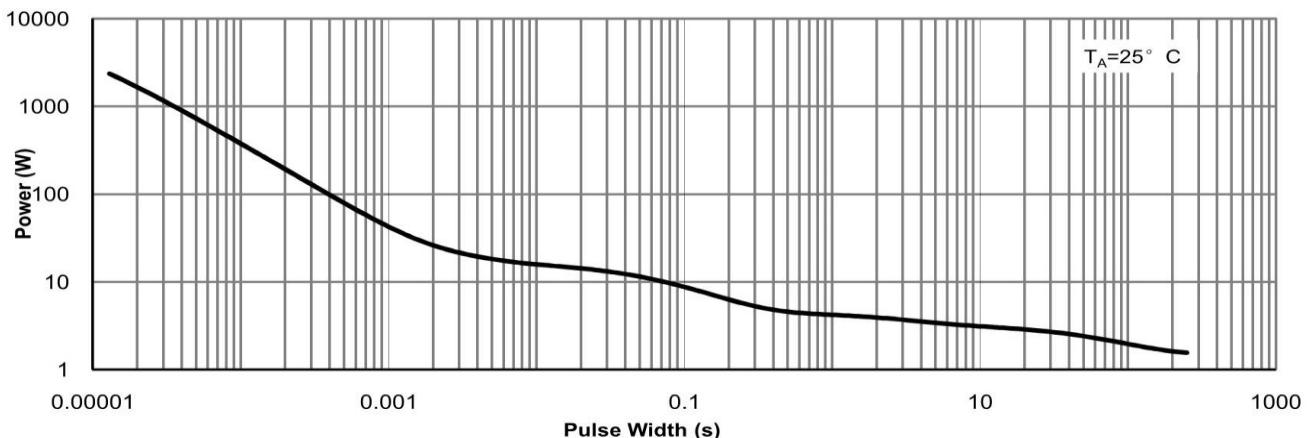
DYNAMIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=-15V, f=1MHz$		520	634	pF
C_{oss}	Output Capacitance			100	123	pF
C_{rss}	Reverse Transfer Capacitance			65	77	pF
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1MHz$			1.1	Ω

SWITCHING PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$Q_g(10V)$	Total Gate Charge	$V_{GS}=-10V, V_{DS}=-15V, I_D=-6A$		4.6		nC
$Q_g 4.5V)$	Total Gate Charge			2.3		
Q_{gs}	Gate Source Charge			1.54		
Q_{gd}	Gate Drain Charge			2.2		
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=-10V, V_{DS}=-15V, RL=0.75\Omega, R_{GEN}=3\Omega$		5.5		ns
t_r	Turn-On Rise Time			4.4		
$t_{D(off)}$	Turn-Off DelayTime			15.4		
t_f	Turn-Off Fall Time			4.95		
t_{rr}	Body Diode Reverse Recovery Time	$I_F=-8A, dI/dt=500A/\mu s$		11		ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=18A, dI/dt=500A/\mu s$		5.3		nC

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Fig 1: On-Region Characteristics (Note E)

Figure 2: Transfer Characteristics (Note E)

Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

Figure 4: On-Resistance vs. Junction Temperature (Note E)

Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characteristics

Figure 9: Single Pulse Avalanche capability (Note C)

Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)