

## Description

The HSS3414A is the high cell density trenched N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications.

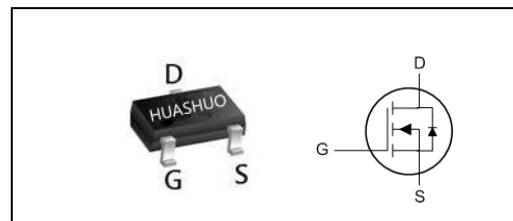
The HSS3414A meet the RoHS and Green Product requirement with full function reliability approved.

- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

## Product Summary

$V_{DS}$	20	V
$R_{DS(ON),max}$	26	$m\Omega$
$I_D$	6	A

## SOT23 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V_1$	6.0	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V_1$	5.0	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	17	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation <sup>3</sup>	1	W
$P_D @ T_A=70^\circ C$	Total Power Dissipation <sup>3</sup>	0.66	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup>	120	°C/W

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVD <sub>SS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20	---	---	V
△BVD <sub>SS</sub> /△T <sub>J</sub>	BVD <sub>SS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.018	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	---	21	26	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A	---	28	35	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =2A	---	40	50	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	0.35	---	1.0	V
△V <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-3.1	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =4A	---	30	---	S
Q <sub>g</sub>	Total Gate Charge (4.5V)	V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	---	8.6	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	1.37	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	2.3	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, R <sub>G</sub> =3.3Ω	---	5.2	---	ns
T <sub>r</sub>	Rise Time		---	34	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	23	---	
T <sub>f</sub>	Fall Time		---	9.2	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	670	---	pF
C <sub>oss</sub>	Output Capacitance		---	75	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	68	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>s</sub>	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	6	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,4</sup>		---	---	17	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>s</sub> =1A, T <sub>J</sub> =25°C	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup>FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

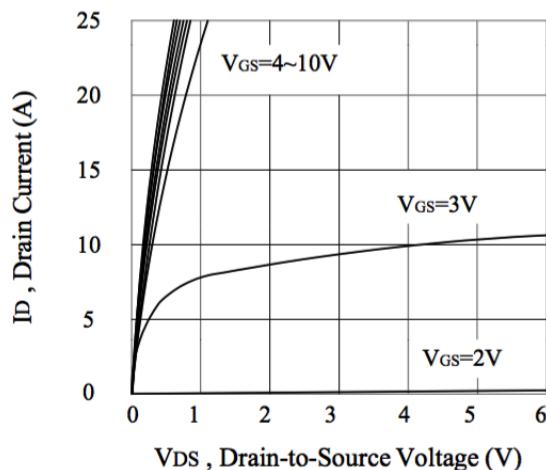


Figure 1. Output Characteristics

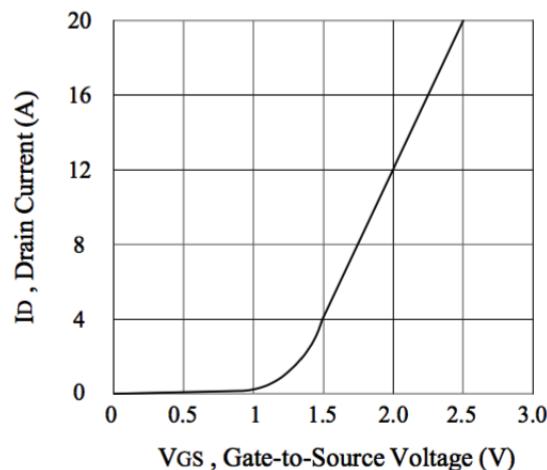


Figure 2. Transfer Characteristics

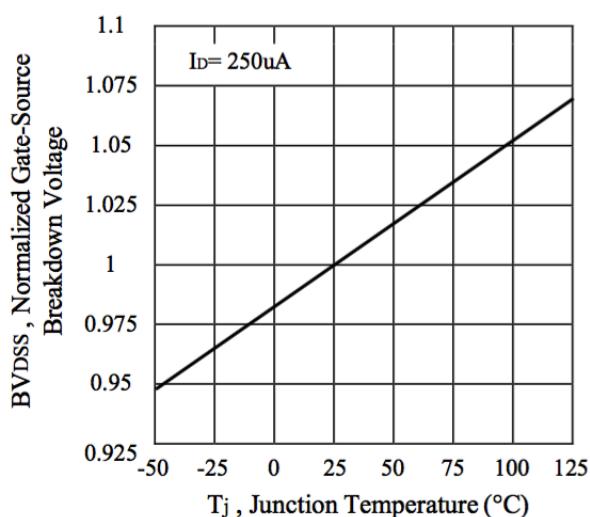


Figure 3. Breakdown Voltage Variation with Temperature

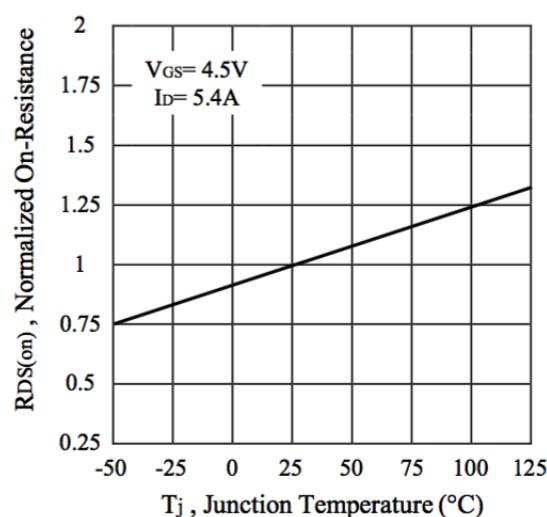


Figure 4. On-Resistance Variation with Temperature

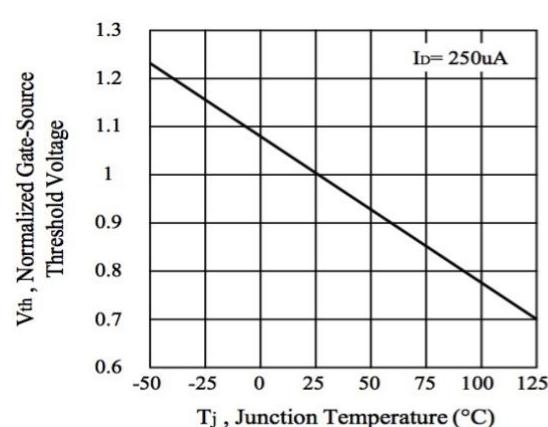


Figure 5. Gate Threshold Variation with Temperature

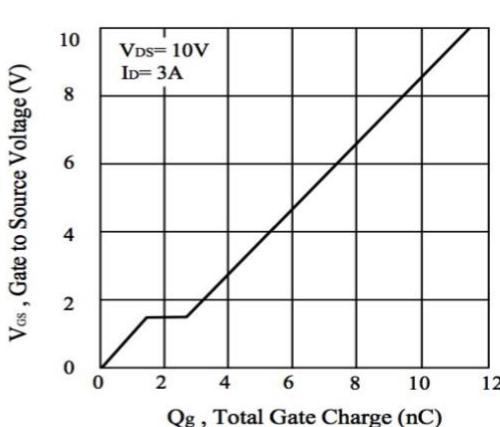
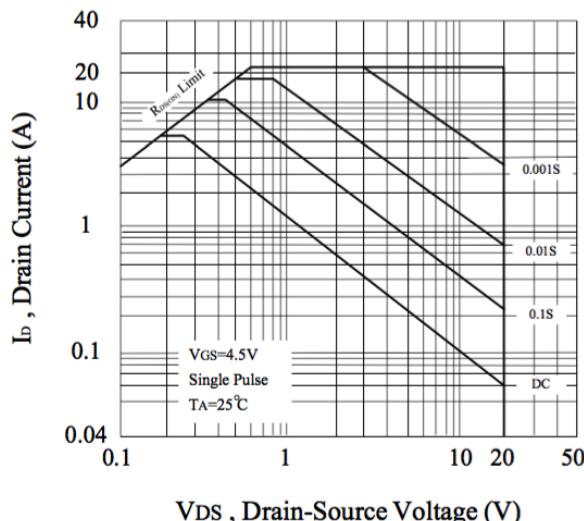
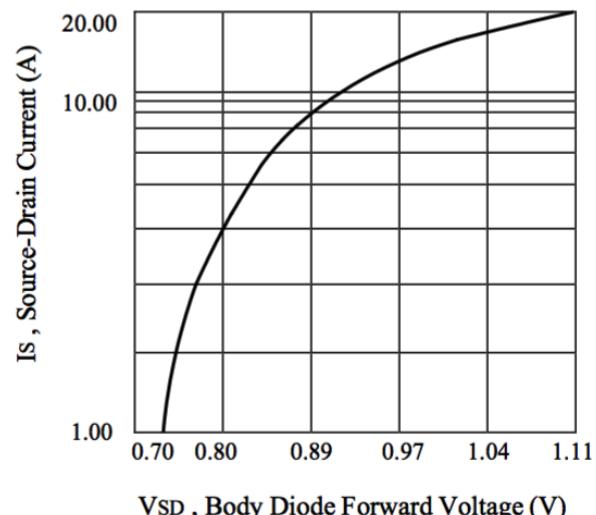


Figure 6. Gate Charge



V<sub>DS</sub> , Drain-Source Voltage (V)

Figure 7. Maximum Safe Operating Area



V<sub>SD</sub> , Body Diode Forward Voltage (V)

Figure 8. Body Diode Forward Voltage Variation with Source Current

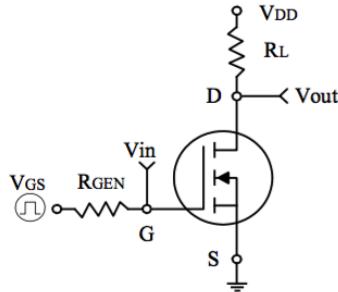


Figure 9. Switching Test Circuit and Switching Waveforms

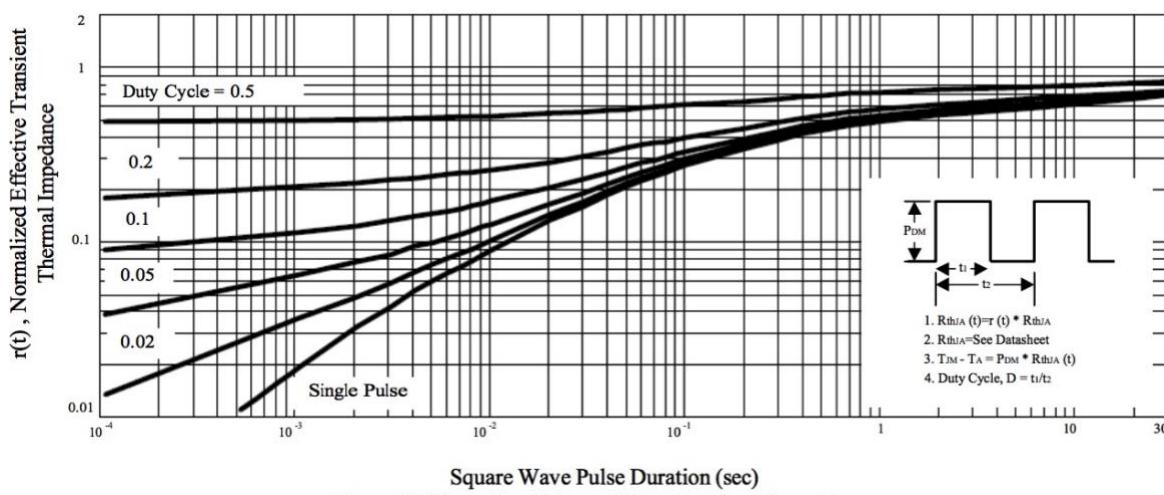
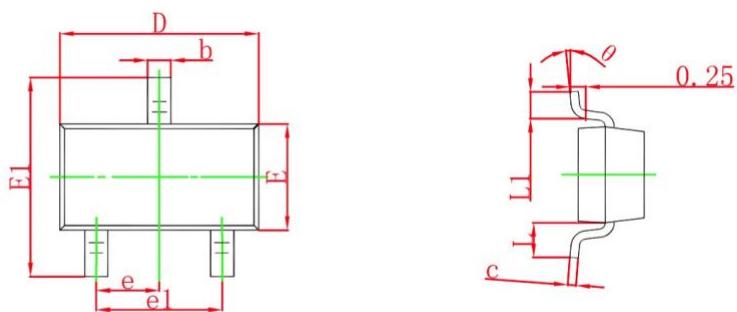


Figure 10. Normalized Thermal Transient Impedance Curve

## Ordering Information

Part Number	Package code	Packaging
HSS3414A	SOT-23	3000/Tape&Reel



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°