

convex termination with square corners resistor array

resistors



features

- Manufactured to type RK73 standards
- · Less board space than individual chips
- Isolated resistor elements
- Convex terminations with square corners (CN_K)
- Flat termination with square corners (CN_N)
- Products with lead-free terminations meet EU RoHS requirements. EU RoHS regulation is not intended for Pb-glass contained in electrode, resistor element and glass.
- AEC-Q200 Qualified: CN1E4K and CN1J4K only

dimensions and construction

CN1E2K, CN1J2K



CN1E4K, CN1J4K, CN1F8K, CN1FN8K



Size	Dimensions inches (mm)								
Code	L	W	С	d	t	а	a 2	b	Р
1E2K (0402x2)	.039±.004 (1.0±0.1)	.039±.004	$.006 \pm .004$ (0.15 ± 0.1)	.010±.004 (0.25±0.1)	.014±.004	.013±.004 (0.33±0.1)	_	.007±.002 (0.17±0.05)	.026 (0.67)
1E4K (0402x4)	.079±.004 (2.0±0.1)	(1.0±0.1)	.006±.004 (0.15±0.1)	.010±.008 (0.25±0.2)	(0.35±0.1)	.012±.006 (0.3±0.15)	.016±.006 (0.4±0.15)	.006±.004 (0.15±0.1)	.020 (0.5)
1J2K (0603x2)	.063±.006 (1.6±0.15)	.063±.006	.012±.008	.010±.004 (0.25±0.1)	.020±.004 (0.5±0.1)	.024±.006 (0.6±0.15)	—	.012±.004 (0.3±0.1)	0.031 (0.8)
1J4K (0603x4)	.126±.006 (3.2±0.15)	(1.6±0.15)	(0.3±0.2)			.020±.006 (0.5±0.15)	.026±.006 (0.65±0.15)		
1F8K 1FN8K (0602x8)	.149±.004 (3.8±0.1)	.063±.004 (1.6±0.1)	.012±.004 (0.3±0.1)	.012±.004 (0.3±0.1)	.018±.004 (0.45±0.1)	.012±.004 (0.30±0.1)		. 006 (0.15)	.020 (0.5)

ordering information

CN	1J	4	К			Т		TD	101	J
Туре	Size	Elements	Termi Conv		Termination Material			Packaging	Nominal Resistance	Tolerance
	1E	2			T: Sn		TD		2 significant	F: ±1%
	1J			are termina ners be ava Flat type contac n square options		J: Other ation styles may ailable, please at factory for s)	7" paper tape TDD: 10" paper tape		figures + 1 multiplier for ±5% 3 significant figures + 1 multiplier for ±1%	J: ±5%
CN	1F	N	8	К		Т		TD	101	J
Туре	Size	Marking E	lements	Termii Conve		Terminatio Material	on	Packaging	Nominal Resistance	Tolerance
For further info	r to Apper	K: Conv type wit square corners	th	T: Sn (Other terminati styles may be available, please contact factory for options)	-	TD: 7" paper tape	2 significant figures + 1 multiplier for ±5% 3 significant figures + 1 multiplier for ±1%	F: ±1% J: ±5%		
Specif	ications given herein	may be changed at	any time wit	thout prior r	notice.	Please confirm tec	hnical	specifications before	you order and/or use.	11/14/17







For resistors operated at a terminal part

temperature of described for each size or

above, a power rating shall be derated in accordance with the derating curve.

Please refer to "Introduction of the derating

curve based on the terminal part temperature" in the beginning of our catalog before use.

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applications and ratings

Part	Power Rating @ 70°C (Per Element)	Ambient	Rated Terminal Temp.	Resistano E-24, E-96 (F±1%)	ce Range E-24 (J±5%)	T.C.R. (pp (F±1%)	m/°C) Max. (J±5%)	Absolute Maximum Working Voltage	Maximum Overload Voltage (5 Secs. Max.)	Operating Temp Range
				(. = 1 / 3)	(010/0)	(1 ± 1 /0)	(010/0)	vonage	(J Jecs. IVIA.)	
CN1E2K						±200:R≥10Ω			501/	
CN1E4K	1/16W				10Ω - 1ΜΩ Ω	±200.n21032	±200:R>10Ω ±400:R<10Ω	25V	50V	-55°C to +155°C
CN1J2K	(.063W)	7000	+125°C	10Ω - 100kΩ		100-D>100		JUV	100V	
CN1J4K		+70°C	/0°C		1Ω - 1MΩ	±100:R≥10Ω				
CN1F8K	1/16W			100 10000	100 110	000 D 100		051/	50)/	-55°C to
CN1FN8K	(.063W) 0.25W per package			10Ω - 100kΩ	10Ω - 1MΩ	±200:R≥10Ω		25V	50V	+125°C

Note that network resistors generate higher heat rather than single flat chip resistor under rated power output

100

80

60

40

20

0

-55

-40 -20 0 20 40

Rated Powe

%

If any questions should arise whether to use the "Rated Ambient Temperature" or the "Rated Terminal Part Temperature," please give priority to the "Rated Terminal Part Temperature." Prior to use and for more details refer to "Introduction of the derating curves on the terminal part temperature" in the beginning of the catalog.

i

Terminal Part Temperature (°C)

environmental applications

Derating Curve



For resistors operated at an ambient temperature of 70°C or above, a power rating shall be derated in accordance with the above derating curve.

circuit schematic

CN1E2K, CN1J2K	CN1E4K, CN1J4K	CN1F8K, CN1FN8K
99	$\varphi \varphi \varphi \varphi$	9 9 9 9 9 9 9 9
L ≩R1≩R2	LLLL ≩R1≩R2≩R3≩R4	<pre></pre>
R1 = R2	R1 = R2 = R3 = R4	R1 = R2 = R3 = R4 = R5 = R6 = R7

0 '≷R8 6 7 = R8

Circuit Board Application

CN1E2K CN1E4K

CN1J2K

CN1J4K

60⁴80 100 120⁴ 140⁴160 180 70 125 155



IC IC

12/01/17

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Performance Characteristics

Requirement Δ R		Δ R ±(%+0.1Ω)					
Parameter	Limit	Typical	Test Method				
Resistance	Within regulated tolerance	_	25°C				
T.C.R.	Within specified T.C.R.	_	+25°C/-55°C, +25°C/+125°C				
Overload (Short time)	±2.0%	±0.25%	Rated voltage x 2.5 for 5 seconds				
Resistance to Solder Heat	±1.0%	±0.75%	$260^{\circ}C \pm 5^{\circ}C$, 10 seconds \pm 1 second				
Rapid Change of Temperature	±1.0%	±0.5%	-55°C (30 minutes), +125°C (30 minutes), 5 cycles				
Moisture Resistance	±5.0%	±1.0%	40°C ± 2°C, 90 - 95% RH, 1000 hours, 1.5 hr ON, 0.5 hr OFF cycle				
Endurance at 70°C	±5.0%	±0.5%	70°C ± 2°C, 1000 hours, 1.5 hr ON, 0.5 hr OFF cycle				
High Temperature Exposure	±1.0%	±0.15%: CN1F8K +0.25: All others	+125°C, 1000 hours: CN1F8K +155°C, 1000 hours: CN1E2K, CN1E4K, CN1J2K, CN1J4K				