

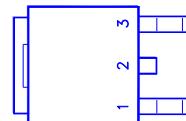
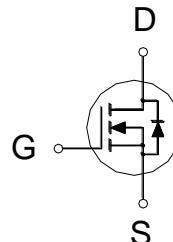
NIKO-SEM
**N-Channel Enhancement Mode
Field Effect Transistor**
PA410BD

TO-252

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	140mΩ	10A


 1. GATE
 2. DRAIN
 3. SOURCE
ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	10	A
	$T_C = 100^\circ\text{C}$		7	
Pulsed Drain Current ¹		I_{DM}	30	
Avalanche Current		I_{AS}	10	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	5	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	35	W
	$T_C = 100^\circ\text{C}$		14	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		3.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.
ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.9	2.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$			1	μA
		$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{V}, I_D = 5\text{A}$		103	170	$\text{m}\Omega$
		$V_{GS} = 10\text{V}, I_D = 5\text{A}$		93	140	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10\text{V}, I_D = 5\text{A}$		13		S

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DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$	330			pF
Output Capacitance	C_{oss}		50			
Reverse Transfer Capacitance	C_{rss}		22			
Total Gate Charge ²	Q_g	$V_{GS} = 10V, V_{DS} = 50V, I_D = 5A$	8.6			nC
Gate-Source Charge ²	Q_{gs}		1.2			
Gate-Drain Charge ²	Q_{gd}		3.5			
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DS} = 50V$ $I_D \geq 5A, V_{GS} = 10V, R_{GEN} = 6\Omega$	22			nS
Rise Time ²	t_r		60			
Turn-Off Delay Time ²	$t_{d(off)}$		30			
Fall Time ²	t_f		40			
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^{\circ}\text{C}$)						
Continuous Current	I_S				10	A
Forward Voltage ¹	V_{SD}	$I_F = 5A, V_{GS} = 0V$			1.1	V
Reverse Recovery Time	t_{rr}	$I_F = 5A, dI_F/dt = 100A/\mu\text{s}$	25			nS
Reverse Recovery Charge	Q_{rr}		25			nC

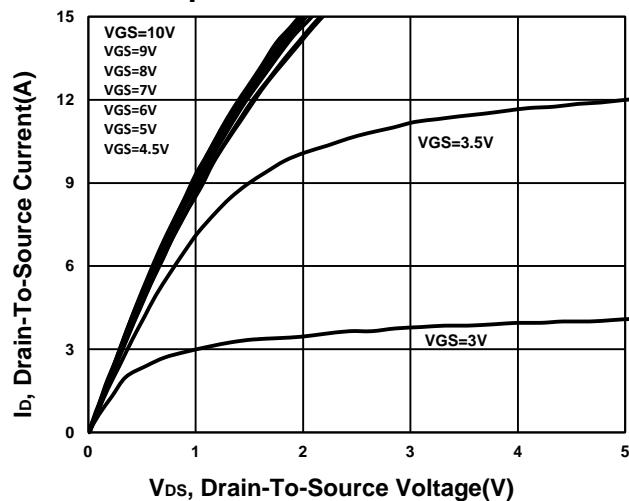
¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

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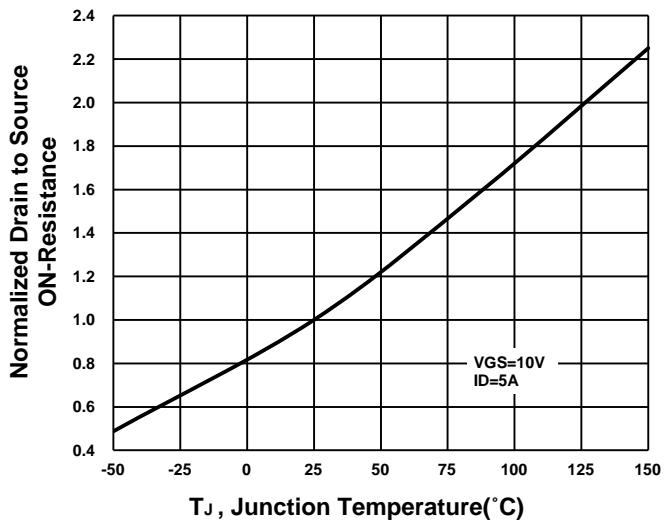
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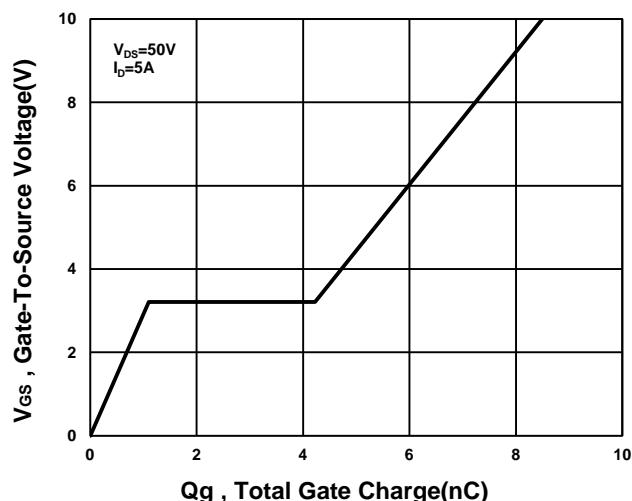
Output Characteristics



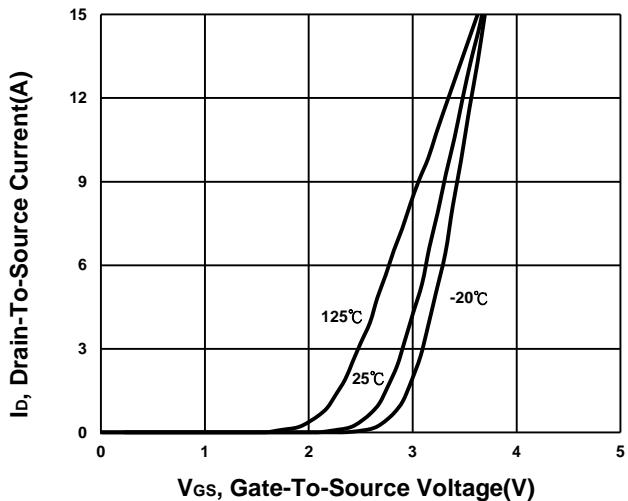
On-Resistance VS Temperature



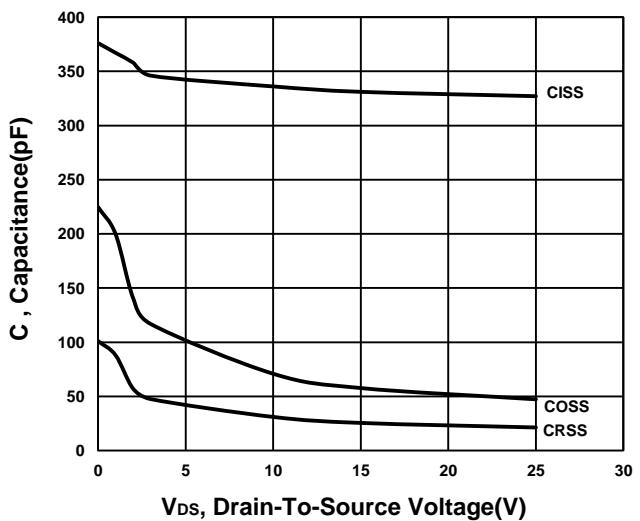
Gate charge Characteristics



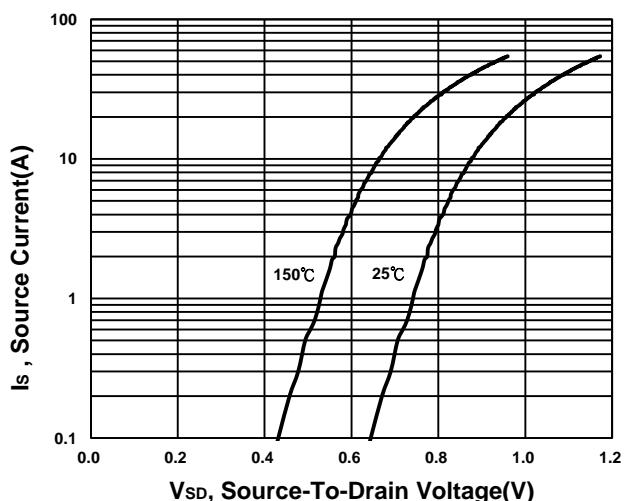
Transfer Characteristics



Capacitance Characteristic



Source-Drain Diode Forward Voltage



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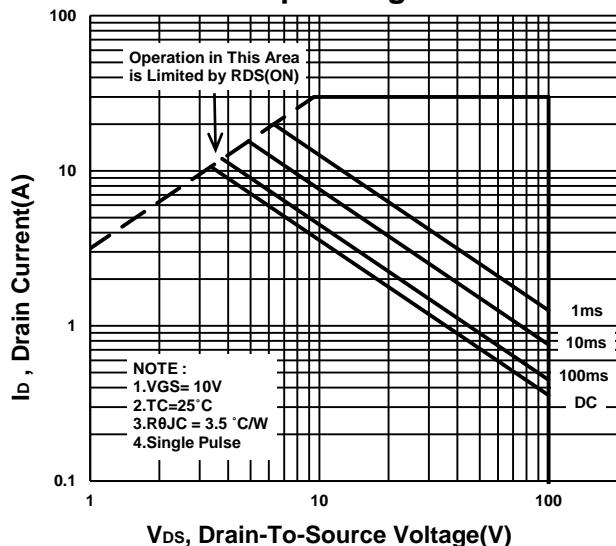
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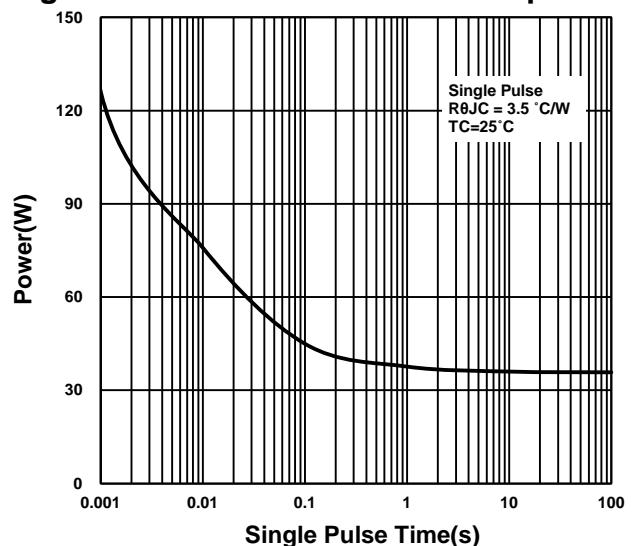
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Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

