



# Specification for Approval

Date: 2019/07/05

WCM3225F2SF-102T04

Customer: 友仁達

TAI-TECH P/N:

	CUSTOMER P/N:			
	DESCRIPTION:			
	QUANTITY:	pcs	<u>;                                    </u>	
REM	IARK:			
	Cı	stomer Approval Feedba	nck	

#### ■ 西北臺慶科技股份有限公司

TAI-TECH Advanced Electronics Co., Ltd Headquarter:

NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN HSIEN, TAIWAN, R.O.C.

TEL: +886-3-4641148 FAX: +886-3-4643565

http://www.tai-tech.com.tw

E-mail: sales@tai-tech.com.tw

□ <u>Office:</u> 深圳辦公室

11BC,Building B Fortune Plaza,NO.7002, Shennan Avenue, Futian

District Shenzhen

TEL: +86- 755-23972371 FAX: +86-755-23972340

□ 臺慶精密電子(昆山)有限公司

TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA

TEL: +86-512-57619396 FAX: +86-512-57619688

E-mail: sales@tai-tech.cn

□ 慶邦電子元器件(泗洪)有限公司

TAIPAQ ELECTRONICS (SIHONG) CO., LTD Sihong development zone Suqian City, Jiangsu , CHINA. TEL: +86-527-88601191 FAX: +86-527-88601190

E-mail: sales@taipaq.cn

## Sales Dep.

APPROVED	CHECKED
詹字鈞	鍾明君
Elven Chan	Mandy Chung

### **R&D** Center

APPROVED	CHECKED	DRAWN
楊祥忠	林志鴻	何玉蓮
Mike Yang	Zhi-Hong Lin	Anna Ho

# Wire Wound Type Common Mode Filter

WCM3225F2SF-102T04

	ECN HISTORY LIST										
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN						
1.0	19/07/05	新發行	楊祥忠	林志鴻	何玉蓮						
備											
註											

# Wire Wound Type Common Mode Filter

WCM3225F2SF-102T04

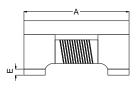
### 1. Features

- 1. High common mode impedance at high frequency cause excellent noise suppression performance.
- 2. WCM3225F2SF series realizes small size and low profile. 3.2x2.5x2.2 mm.
- 3. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 4. Operating temperature-40~+125 $^{\circ}$ C (Including self temperature rise)

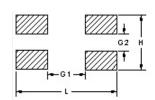




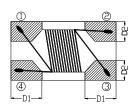
### 2. Dimension







**Recommended PC Board Pattern** 



Series	A(mm)	B(mm)	C(mm)	D1(mm)	D2(mm)	E(mm)	G1(mm)	G2(mm)	H(mm)	L(mm)
3225F2SF	3.2±0.2	2.5±0.2	2.2±0.2	0.8±0.1	0.9±0.1	0.15±0.1	1.6	0.6	3.5	4.4

Units: mm

## 3. Part Numbering



A: Series B: Dimension

C: Material Ferrite Core
D: Number of Lines 2=2 lines

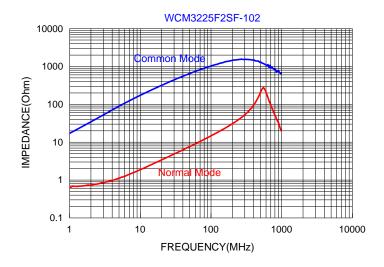
E: Type S=Shielded , N=Unshielded

F: Lead free type

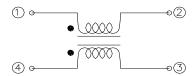
G: Impedance  $102=1000\,\Omega$  H: Packaging T=Taping and Reel I: Rated Current  $04=400\,\text{mA}$ 

# 4. Specification

TAI-TECH Part Number	Common mode Impedance $(\Omega)$	Test Frequency (MHz)	DC Resistance $(\Omega)$ max.	Rated Current (mA)max.	Rated Volt. (Vdc)max.	Withstand Volt. (Vdc) max.	IR $(\Omega)$ min.
WCM3225F2SF-102T04	1000±25%	100	0.30	400	50	125	10M

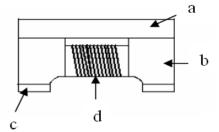


# 5. Schematic Diagram



## 6. Materials

No.	Description	Specification
a.	Upper plate	Ferrite
b.	Core	Ferrite Core
С	Termination	Ag/Ni/Sn
d	Wire	Enameled Copper Wire



# 7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	-40~+125℃ (on board)	
Electrical Performance Tes	st	
Z(common mode)		Agilent-4291A+ Agilent -16197A
DCR	Refer to standard electrical characteristics list.	Agilent-4338B
I.R.		Agilent4339
Temperature Rise Test	Rated Current < 1A ΔT 20°C Max Rated Current ≧ 1A ΔT 40°C Max	1.Applied the allowed DC current.     2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature: 125±2°C Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity: 85±% R.H., Temperature: 85℃±2℃ Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles)  1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs.  2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs.  3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs, keep at 25°C for 2 hrs then keep at -10°C for 3 hrs  4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1–2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 25±2°C ≤0.5min Step3: 125±2°C 30±5min Number of cycles: 500 Measured at room fempraturc after placing for 24±2 hrs.
Vibration		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment: Vibration checker Total Amplitude:10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) ∘ Test the quantity: 15pcs

Item	Performance	Test Condition					
Bending	Appearance : No damage. Impedance : within±15% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.					
	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not	Type Peak Normal Wave value (g's) (ms) Wave form (Vi)ft/sec					
Shock	exceed the specification value	SMD 50 11 Half-sine 11.3					
		Lead 50 11 Half-sine 11.3					
Solder ability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C ∘ Flux for lead free: Rosin. 9.5% ∘ Dip time: 4±1sec ∘ Depth: completely cover the termination					
Resistance to Soldering Heat		Depth: completely cover the termination  Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate heat cycles (solder temp) 10 ±1 25mm/s ±6 mm/s 1					
Terminal	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg, <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.					
Strength		substrate press tool shear force					

# 8. Soldering and Mounting

### 8-1. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

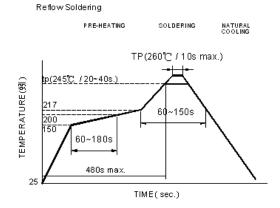
#### 8-1.1 Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

### 8-1.2 Soldering Iron(Figure 2):

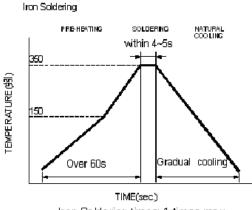
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150°C Never contact the ceramic with the iron tip Use a 20 watt soldering iron with tip diameter of 1.0mm
- 350°C tip temperature (max) 1.0mm tip diameter (max) Limit soldering time to 4~5 sec.



Reflow times: 3 times max.

Fig.1

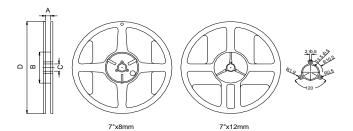


Iron Soldering times: 1 times max.

Fig.2

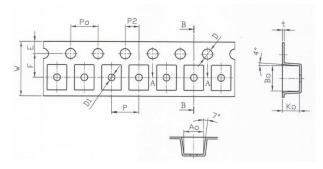
### 9. Packaging Information

### 9-1. Reel Dimension



Туре	A(mm)	A(mm) B(mm)		D(mm)	
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2	

### 9-2. Tape Dimension / 8mm

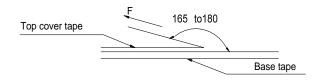


Series	W(mm)	P(mm)	E(mm)	F(mm)	P2(mm)	D(mm)	D1(mm)	P0(mm)	A0(mm)	B0(mm)	K0(mm)	t(mm)
WCM3225F2S	8.00±0.10	4.00±0.10	1.75±0.10	3.50±0.05	2.00±0.05	1.50+0.10/-0.00	1.00±0.10	4.00±0.10	2.88±0.10	3.72±0.10	2.50±0.10	0.26±0.05

### 9-3. Packaging Quantity

Chip size	Chip/Reel	Inner Box	Middle Box	Carton
WCM3225F2S	2000	10000	50000	100000

### 9-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

### **Application Notice**

Storage Conditions(component level)

To maintain the solderability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.