

Features

- 120 Watts Peak Pulse Power per Line ($t_p=8/20\mu s$)
- Protects two or four I/O lines
- Low capacitance: 0.35 pF typical (I/O to I/O)
- Low operating voltage: 5V
- RoHS Compliant
- IEC61000-4-2 (ESD) $\pm 25kV$ (air), $\pm 20kV$ (contact)
- IEC61000-4-4 (EFT) 40A (5/50 μs)
- IEC61000-4-5 (Lightning) 4A (8/20 μs)

Mechanical Characteristics

- Package: DFN1616-6
- Ultra low leakage: nA level
- Case Material: "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020

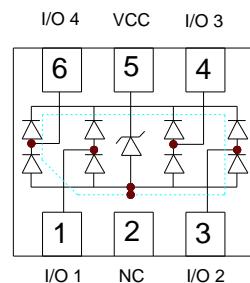
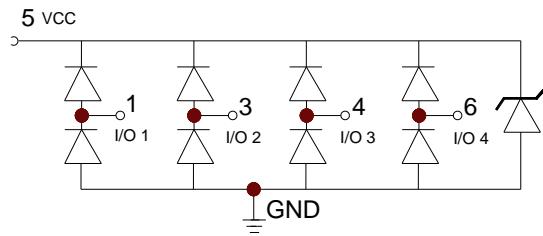
Applications

- High Definition Multimedia Interface (HDMI)
- Digital Visual Interface (DVI)
- Unified Display Interface (UDI)
- MDDI Ports
- PCI Express
- Serial ATA

Ordering Information

Part Number	Qty per Reel	Reel Size
TPESD0514PA	3000	7"

Dimensions and Pin Configuration



Absolute Maximum Ratings (Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	120	W
Peak Pulse Current (8/20μs)	IPP	4	A
ESD per IEC 61000-4-2 (Air)	VESD	± 25	kV
ESD per IEC 61000-4-2 (Contact)		± 20	
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	Any I/O pin to ground
Breakdown Voltage	VBR	6		9	V	IT = 1mA, any I/O pin to ground
Reverse Leakage Current	IR			0.2	µA	VRWM = 5V, any I/O pin to ground
Clamping Voltage	VC			9	V	IPP = 1A (8 x 20μs pulse), any I/O pin to ground
Clamping Voltage	VC			25	V	IPP = 4A (8 x 20μs pulse), any I/O pin to ground
Junction Capacitance	CJ		0.35		pF	VR = 0V, f = 1MHz, between I/O pins
Junction Capacitance	CJ		0.6	0.8	pF	VR = 0V, f = 1MHz, any I/O pin to ground

Characteristic Curves

Fig1. 8/20μs Pulse Waveform

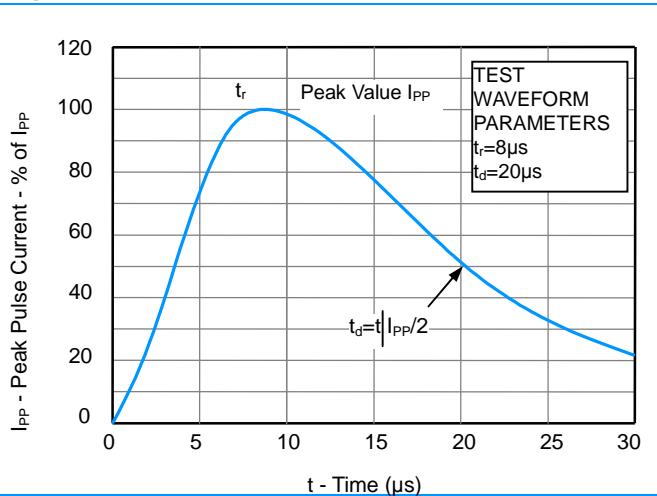


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

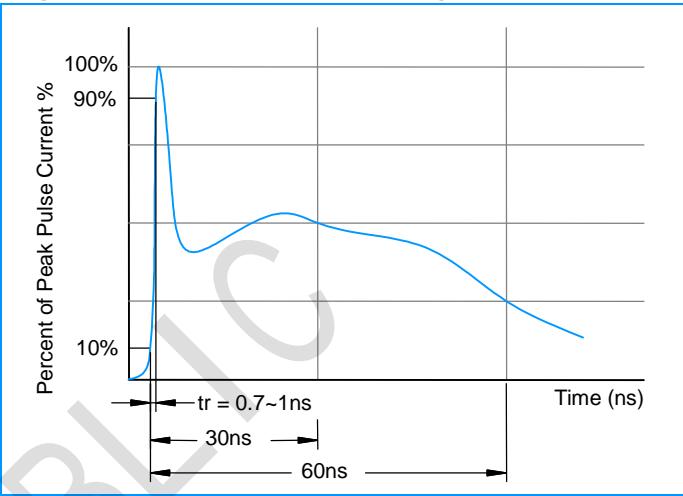


Fig3. Non - Repetitive Peak Pulse Power vs. Pulse Time

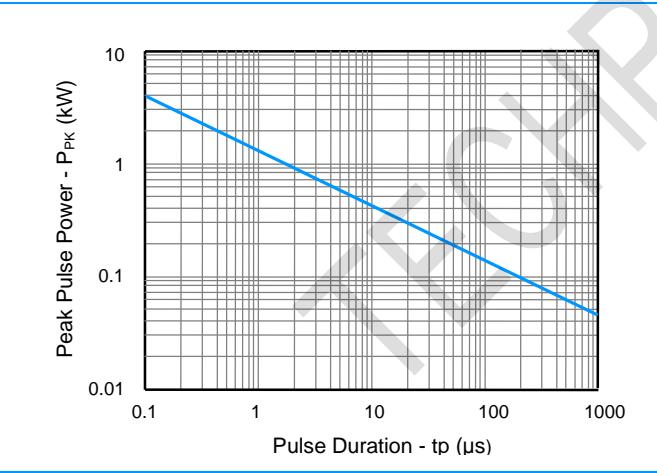
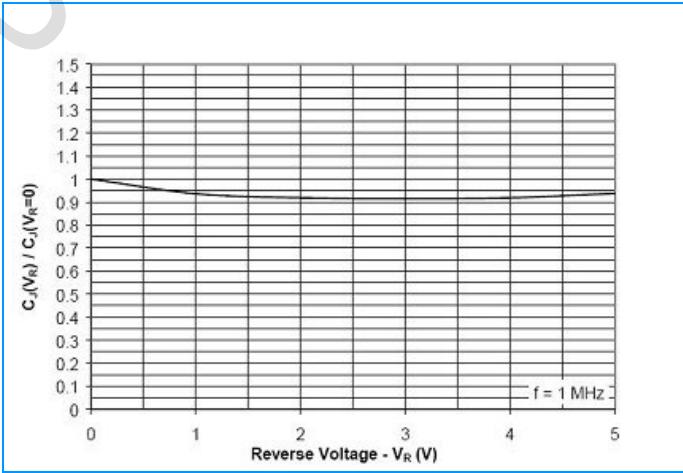
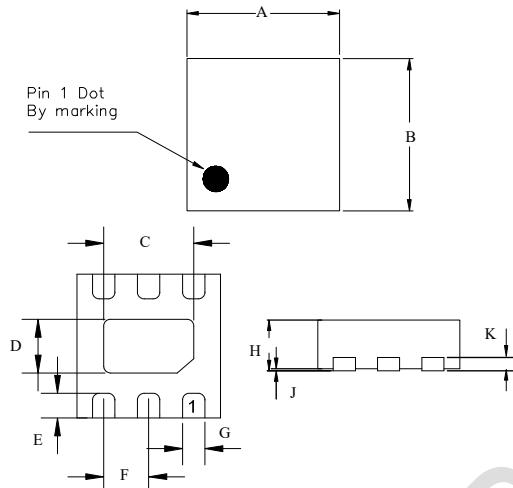


Fig4. Normalized Capacitance vs. Reverse Voltage



Outline Drawing - DFN1616-6

DFN1616-6



DIM	DIMENSIONS				NOTE	
	INCHES		MM			
	MIN	MAX	MIN	MAX		
A	0.061	0.065	1.55	1.65		
B	0.061	0.065	1.55	1.65		
C	0.035	0.041	0.90	1.05		
D	0.020	0.026	0.50	0.65		
E	0.008	0.012	0.20	0.30		
F	0.020 REF.		0.50 REF.			
G	0.008	0.026	0.20	0.30		
H	0.020	0.024	0.50	0.60		
J	0.000	0.002	0.00	0.05		
K	0.006 REF.		0.15 REF.			

Suggested Solder Pad Layout

