
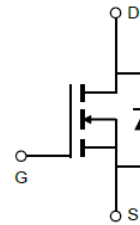
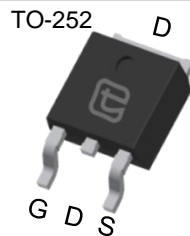


**68V N-Channel Trench MOSFET(Preliminary)**

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>● Trench Power technology</li> <li>● Low <math>R_{DS(ON)}</math></li> <li>● Low Gate Charge</li> <li>● Optimized for fast-switching applications</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>● Synchronous Rectification in DC/DC and AC/DC Converters</li> <li>● Isolated DC/DC Converters in Telecom and Industrial</li> </ul>	<p><b>Product Summary</b></p> <table> <tr> <td><math>V_{DS}</math></td> <td>68V</td> </tr> <tr> <td><math>I_D</math> (at <math>V_{GS}=10V</math>)</td> <td>135A</td> </tr> <tr> <td><math>R_{DS(ON)}</math> (at <math>V_{GS}=10V</math>)</td> <td>&lt; 5.0m<math>\Omega</math></td> </tr> </table> <p>100% UIS Tested</p> 	$V_{DS}$	68V	$I_D$ (at $V_{GS}=10V$ )	135A	$R_{DS(ON)}$ (at $V_{GS}=10V$ )	< 5.0m $\Omega$
$V_{DS}$	68V						
$I_D$ (at $V_{GS}=10V$ )	135A						
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	< 5.0m $\Omega$						



Part Number	Package Type	Form	Marking
TTD135N68A	TO-252	Tape&Reel	135N68A

**Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	68	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_C = 25^\circ\text{C}$	46
		$T_C = 100^\circ\text{C}$	46
Pulsed Drain Current <sup>A</sup>	$I_{DM}$	405	A
Avalanche Current <sup>A</sup>	$I_{AS}$	44	A
Single Pulse Avalanche Energy $L = 0.3\text{mH}$ <sup>A</sup>	$E_{AS}$	290	mJ
Power Dissipation <sup>C</sup>	$P_D$	$T_C = 25^\circ\text{C}$	160
		$T_C = 100^\circ\text{C}$	80
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 175	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Case	$R_{\theta JC}$	0.95	$^\circ\text{C/W}$
Maximum Junction-to-Ambient			
	$R_{\theta JA}$	100	



Electrical Characteristics( $T_J = 25^\circ\text{C}$ unless otherwise noted)						
Symbol	Parameter	Conditions	Value			Units
			Min	Typ	Max	
<b>STATIC PARAMETERS</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	68			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 68\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$		1	$\mu\text{A}$
			$T_J = 100^\circ\text{C}$		25	
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3	4	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 30\text{A}$		4.2	5	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 20\text{A}$		30		S
$V_{SD}$	Diode Forward Voltage	$I_S = 20\text{A}, V_{GS} = 0\text{V}$			1	V
$I_S$	Maximum Body-Diode Continuous Current <sup>B</sup>				46	A
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 30\text{V}, f = 1\text{MHz}$		6646		$\text{pF}$
$C_{oss}$	Output Capacitance			443		
$C_{rss}$	Reverse Transfer Capacitance			396		
<b>SWITCHING PARAMETERS</b>						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, I_D = 30\text{A}$		114		nC
$Q_{gs}$	Gate Source Charge			26		
$Q_{gd}$	Gate Drain Charge			34		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, I_D = 30\text{A}, R_G = 3\Omega$		17		ns
$t_r$	Turn-On Rise Time			11		
$T_{D(off)}$	Turn-Off Delay Time			55		
$t_f$	Turn-Off Fall Time			15		
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 20\text{A}, di/dt = 100\text{A}/\mu\text{s}$		33		ns
$Q_{rr}$	Body Diode Reverse Recovery Charge			51		nC

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation  $P_D$  is based on  $T_{J(MAX)} = 175^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

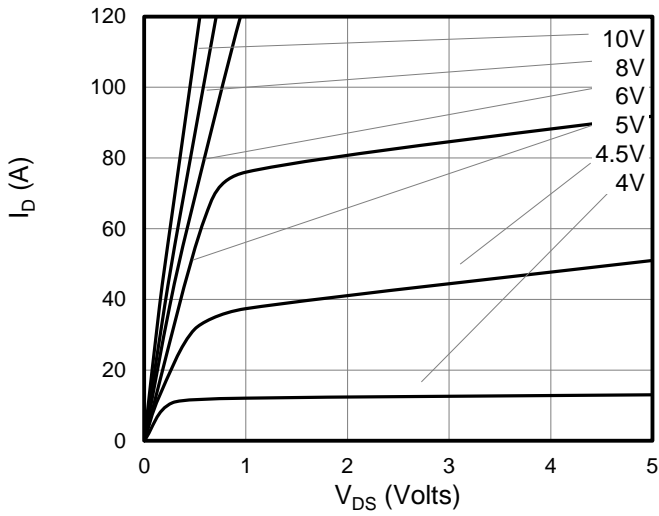


Figure 1: On-Region Characteristics

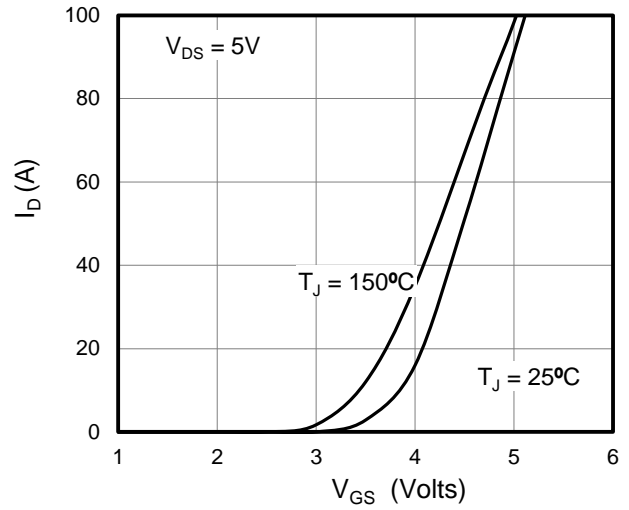


Figure 2: Transfer Characteristics

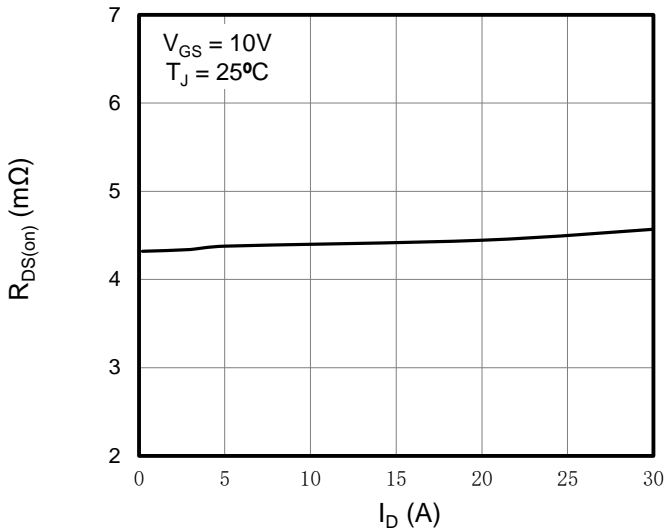


Figure 3: On-Resistance vs. Drain Current

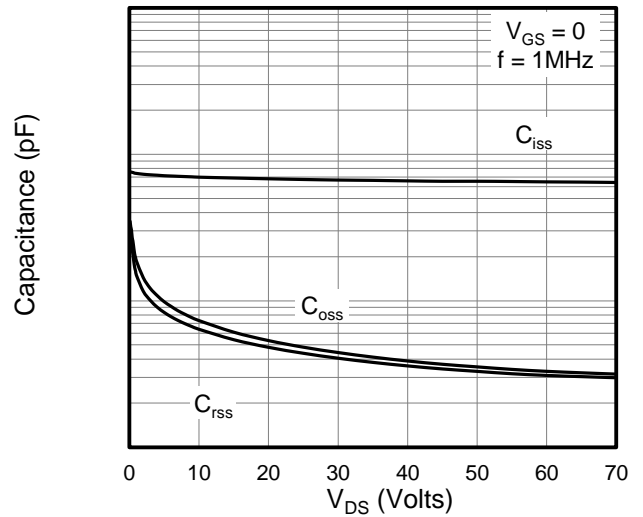


Figure 4: Capacitance Characteristics

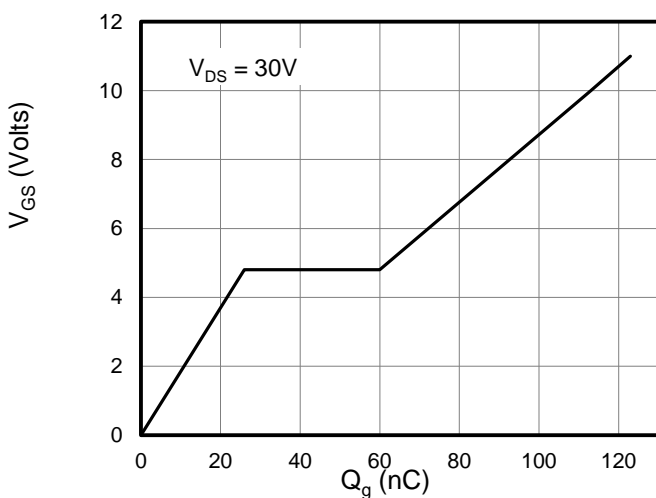


Figure 5: Gate Charge Characteristics

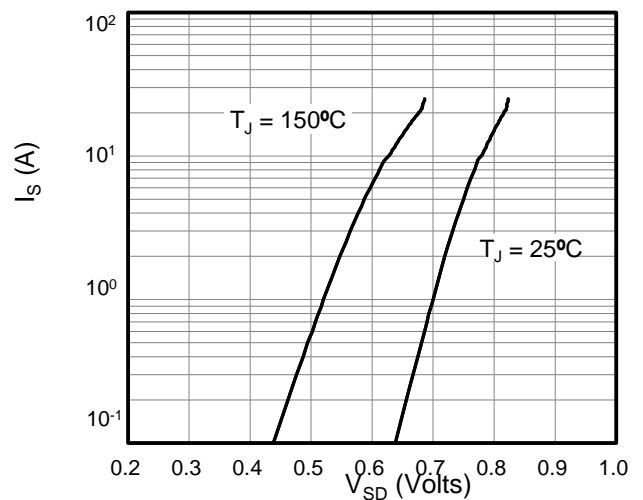


Figure 6: Body Diode Forward Voltage



### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

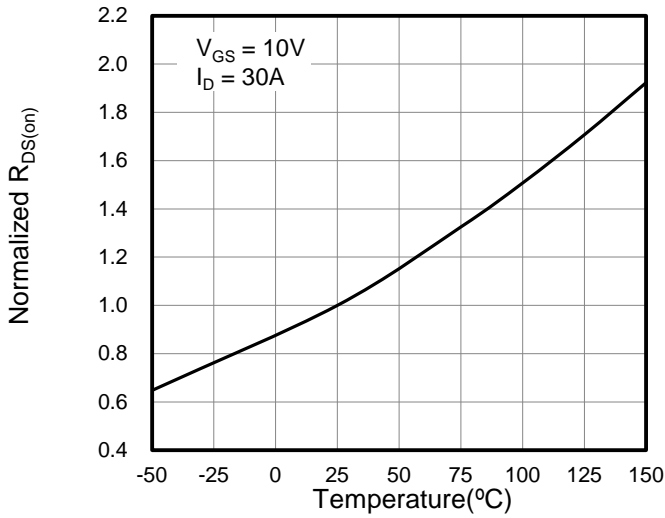


Figure 7: On-Resistance vs. Junction Temperature

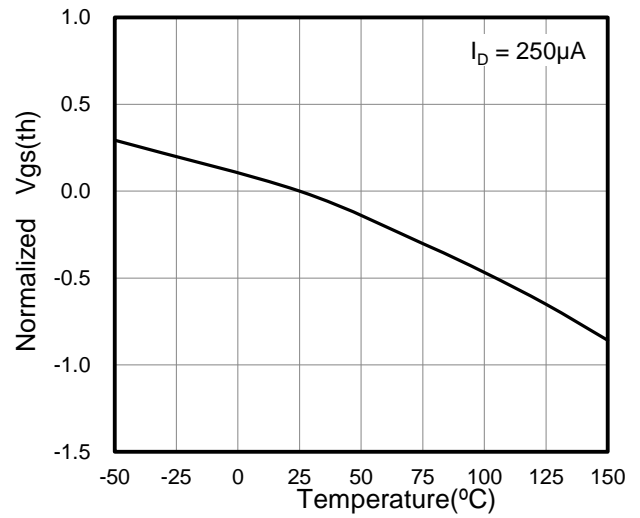


Figure 8:  $V_{GS(th)}$  vs. Junction Temperature

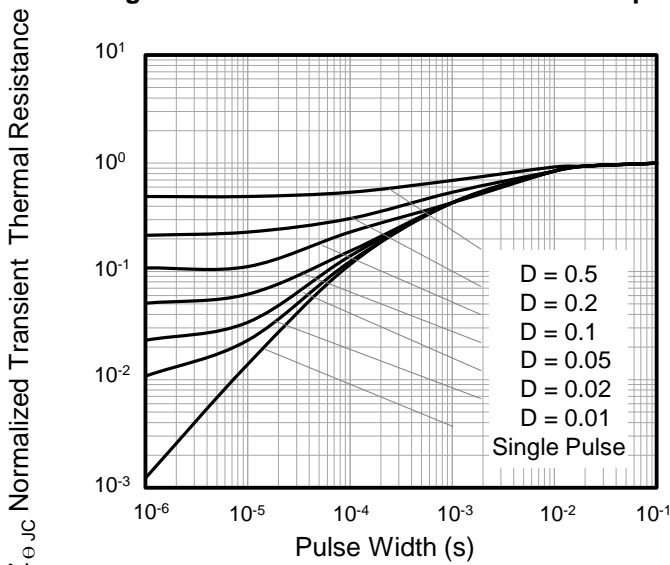


Figure 9: Normalized Transient Thermal Resistance

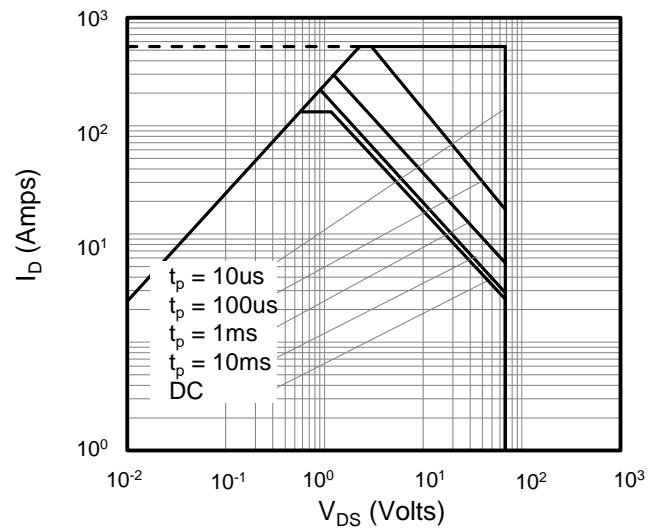


Figure 10: Safe Operating Area

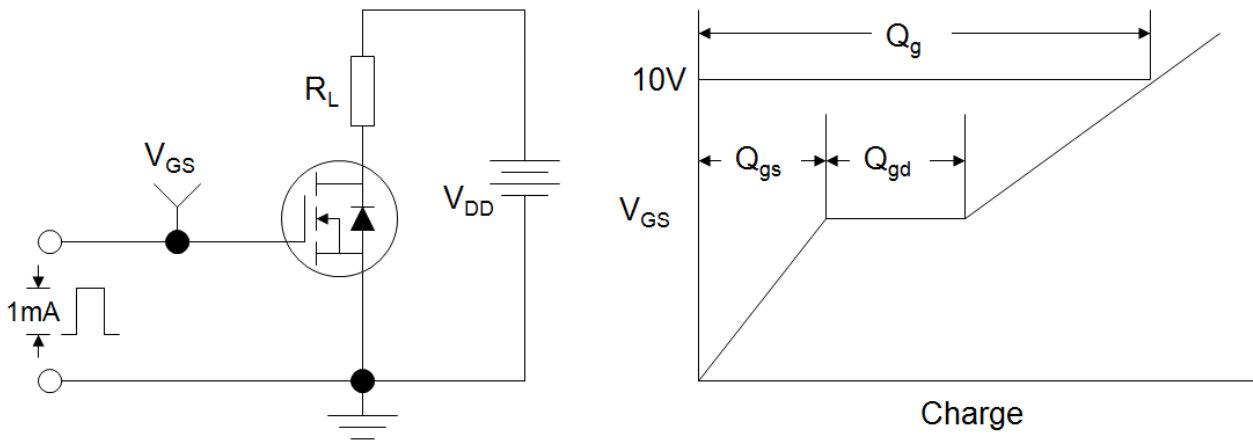


Figure A: Gate Charge Test Circuit and Waveforms

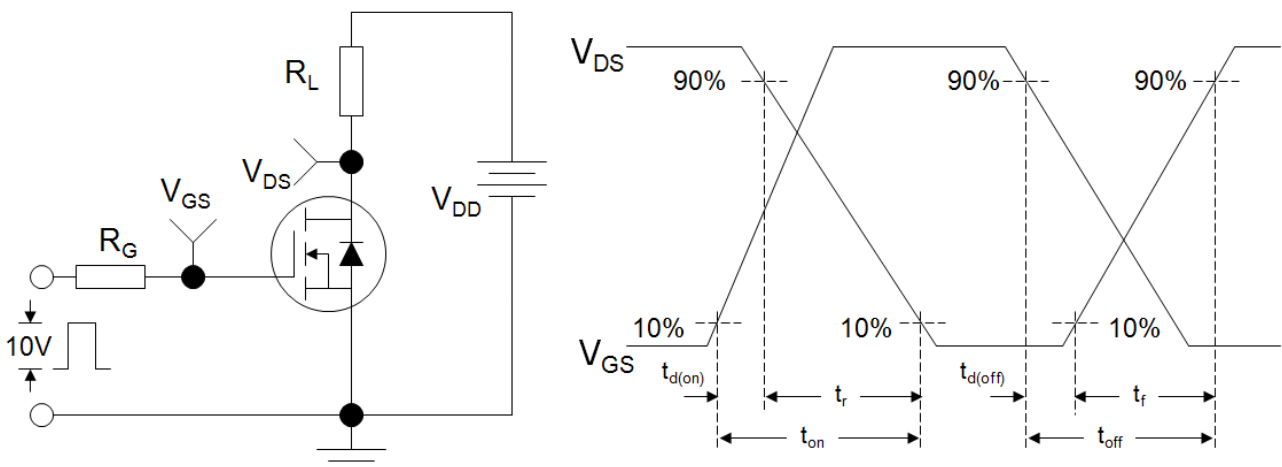


Figure B: Resistive Switching Test Circuit and Waveforms

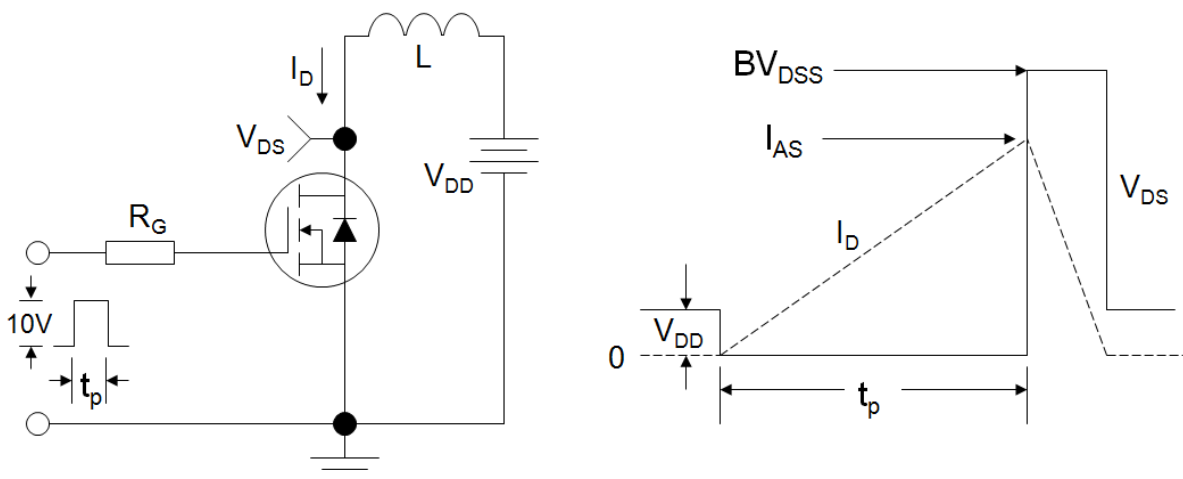
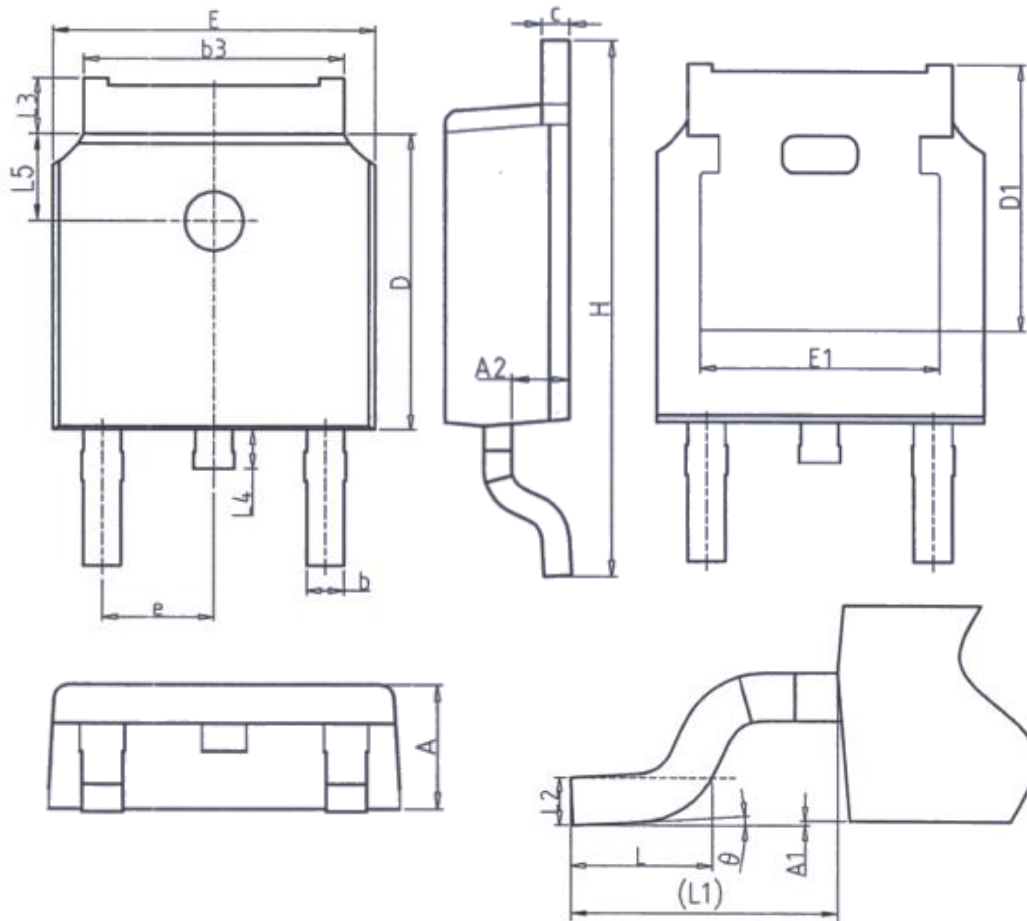


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms



### TO-252(华天)

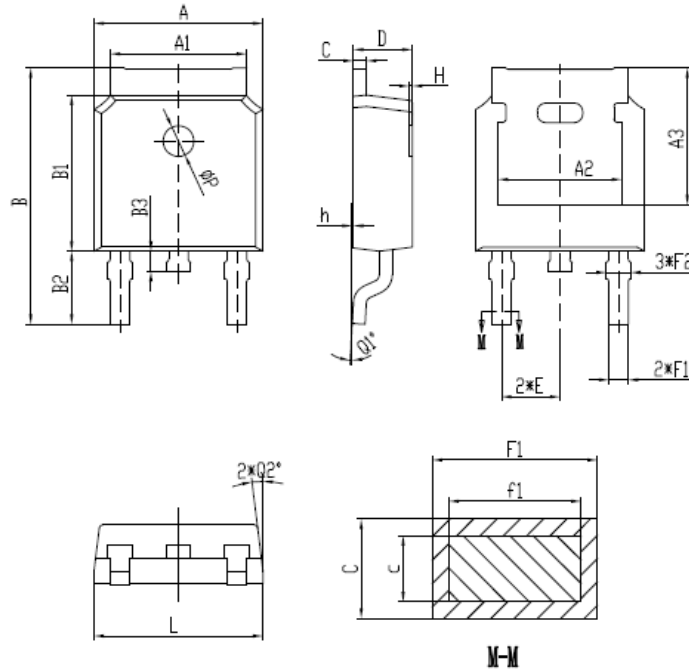


Unit: mm		
Symbol	Min.	Max.
A	2.20	2.40
A1	0.00	0.20
A2	0.97	1.17
b	0.68	0.90
b3	5.20	5.50
c	0.43	0.63
D	5.98	6.22
D1	5.30REF	
E	6.40	6.80
E1	4.63	-

Unit: mm		
Symbol	Min.	Max.
e	2.286BSC	
H	9.40	10.50
L	1.38	1.75
L1	2.90REF	
L2	0.51BSC	
L3	0.88	1.28
L4	-	1.00
L5	1.65	1.95
θ	0°	8°



### TO-252(海天)



SYMBOL	MIN	NOM	MAX
A	6.50	6.60	6.70
A1	5.16	5.31	5.46
A2	4.83 REF		
A3	5.30 REF		
B	9.77	9.97	10.17
B1	6.00	6.10	6.20
B2	2.60	2.80	3.00
B3	0.70	0.80	0.90
C	0.41	—	0.61
c	0.40	0.50	0.60
D	2.20	2.30	2.40
E	2.186	2.286	2.386
F1	0.67	—	0.87
f1	0.66	0.76	0.86
F2	0.76	0.86	0.96
H	0.00	—	0.30
h	0.00	—	0.20
L	6.50	6.60	6.70
$\phi P$	1.10	1.20	1.30
$Q1^\circ$	0°	—	8°
$Q2^\circ$	6°	7°	8°



## Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Wuxi Unigroup does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Wuxi Unigroup.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling Wuxi Unigroup products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Wuxi Unigroup for any damages arising or resulting from such use or sale.

Wuxi Unigroup disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Wuxi Unigroup's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Wuxi Unigroup Microelectronics CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Wuxi Unigroup products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only. It is not guaranteed for volume production. Wuxi Unigroup believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.