

P-Channel 100V Fast Switching MOSFET

General Description

The QM0005D is the highest performance trench P-Channel MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The QM0005D meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-13.4	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-8.5	A
$I_D@T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-2.8	A
$I_D@T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-2.3	A
I_{DM}	Pulsed Drain Current ²	-34	A
EAS	Single Pulse Avalanche Energy ³	36.4	mJ
I_{AS}	Avalanche Current	27	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	45	W
$P_D@T_A=25^\circ C$	Total Power Dissipation ⁴	2	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	2.8	$^\circ C/W$

Product Summary

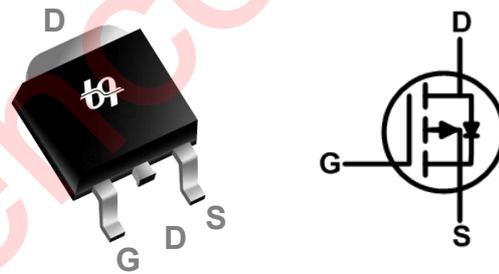


BVDSS	RDSON ($V_{GS}=-10V$)	ID ($T_C=25^\circ C$)
-100V	150m Ω	-13.4A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application

TO252 Pin Configuration



P-Channel 100V Fast Switching MOSFET
Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-100	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=-1\text{mA}$	---	-0.05	---	$V/^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=-10V, I_D=-6A$	---	120	150	m Ω
		$V_{GS}=-4.5V, I_D=-3A$	---	130	165	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	-1.6	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	4	---	$\text{mV}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-80V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-80V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	-5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=-5V, I_D=-6A$	---	12.6	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	10	20	Ω
Q_g	Total Gate Charge (-10V)	$V_{DS}=-80V, V_{GS}=-10V, I_D=-6A$	---	30	42	nC
Q_{gs}	Gate-Source Charge		---	6.4	9	
Q_{gd}	Gate-Drain Charge		---	4.5	6.3	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-50V, V_{GS}=-10V, R_G=3.3\Omega, I_D=-6A$	---	6.8	13.6	ns
T_r	Rise Time		---	9.4	17	
$T_{d(off)}$	Turn-Off Delay Time		---	62	124	
T_f	Fall Time		---	9.6	19	
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V, f=1\text{MHz}$	---	2125	2980	pF
C_{oss}	Output Capacitance		---	104	146	
C_{rss}	Reverse Transfer Capacitance		---	60	84	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	$V_{DD}=25V, L=0.1\text{mH}, I_{AS}=19A$	18	---	---	mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,6}	$V_G=V_D=0V, \text{Force Current}$	---	---	-13.4	A
I_{SM}	Pulsed Source Current ^{2,6}		---	---	-34	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=-1A, T_J=25^\circ\text{C}$	---	---	-1.2	V
t_{rr}	Reverse Recovery Time	$I_F=-6A, di/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	22.6	---	nS
Q_{rr}	Reverse Recovery Charge		---	29	---	nC

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{DD}=-25V, V_{GS}=-10V, L=0.1\text{mH}$
- The power dissipation is limited by 150°C junction temperature
- The Min. value is 100% EAS tested guarantee.
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

All information provided in this document is subjected to important notice

P-Channel 100V Fast Switching MOSFET

Typical Characteristics

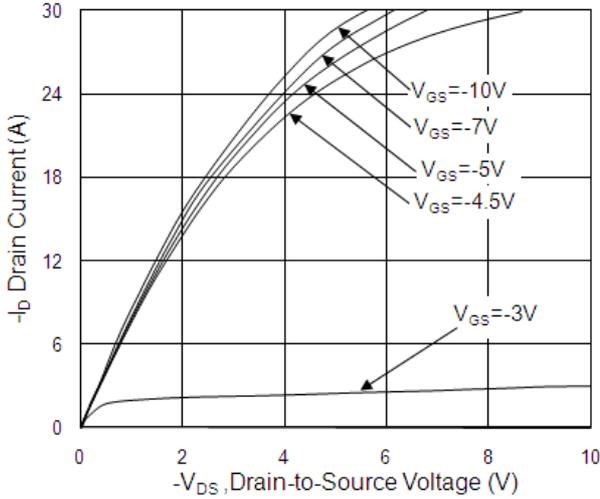


Fig.1 Typical Output Characteristics

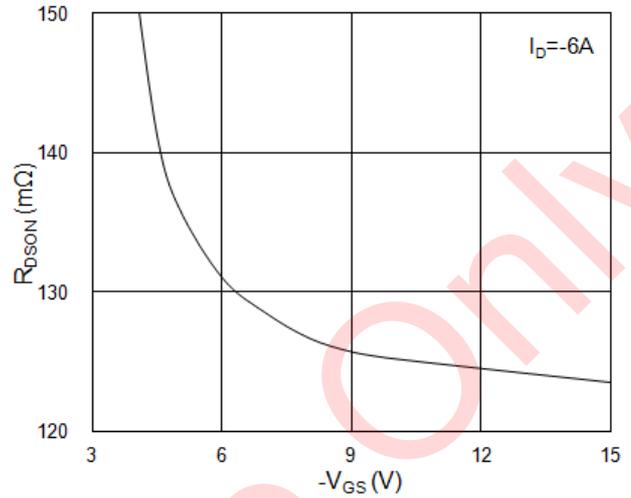


Fig.2 On-Resistance vs. Gate-Source

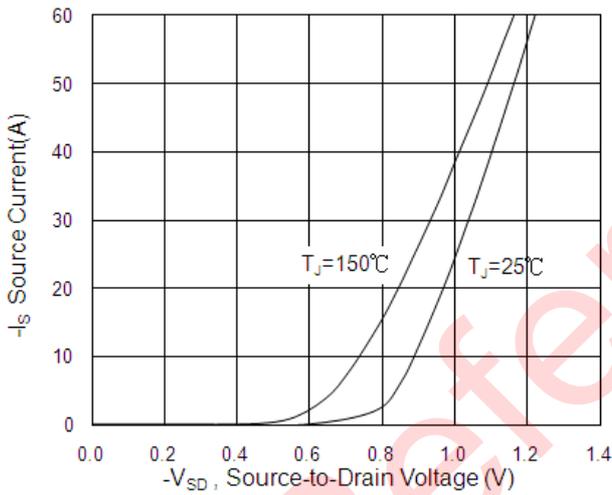


Fig.3 Forward Characteristics Of Reverse

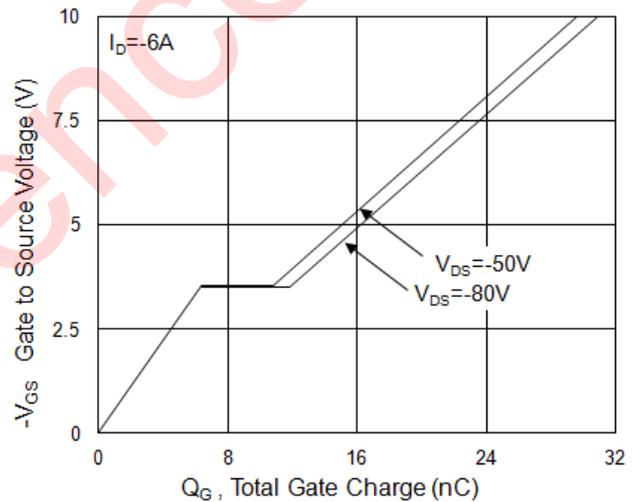


Fig.4 Gate-Charge Characteristics

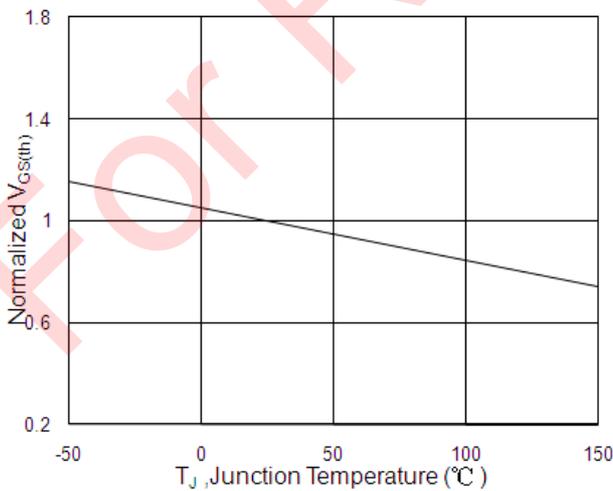


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

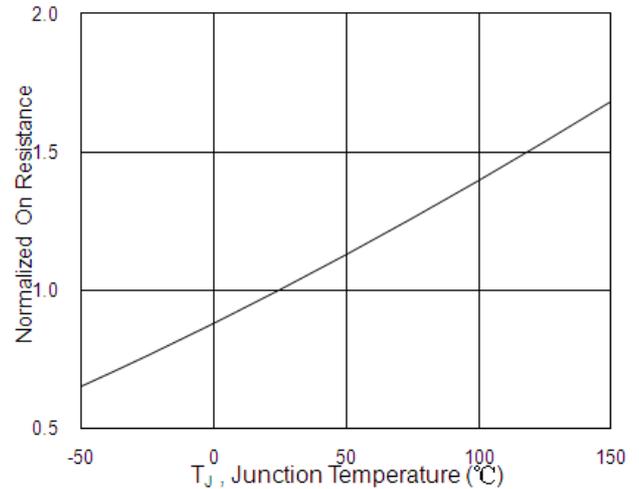


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

P-Channel 100V Fast Switching MOSFET

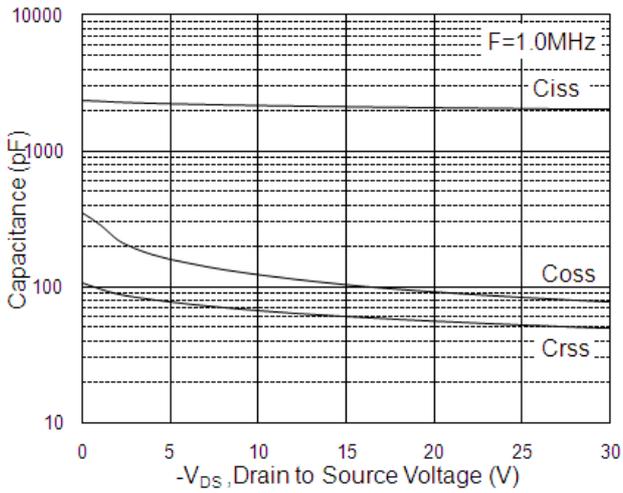


Fig.7 Capacitance

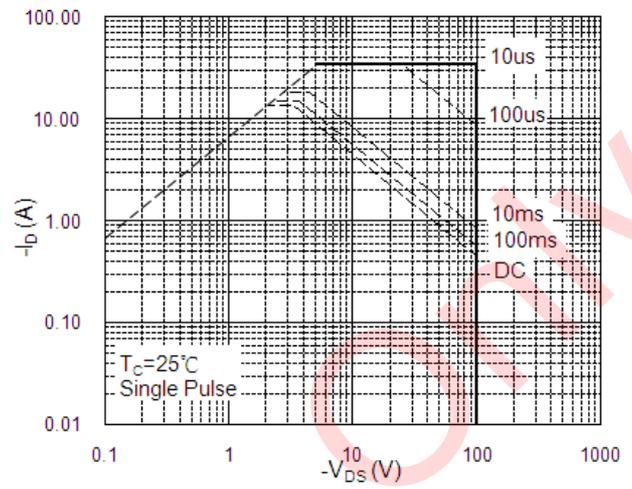


Fig.8 Safe Operating Area



Fig.9 Normalized Maximum Transient Thermal Impedance

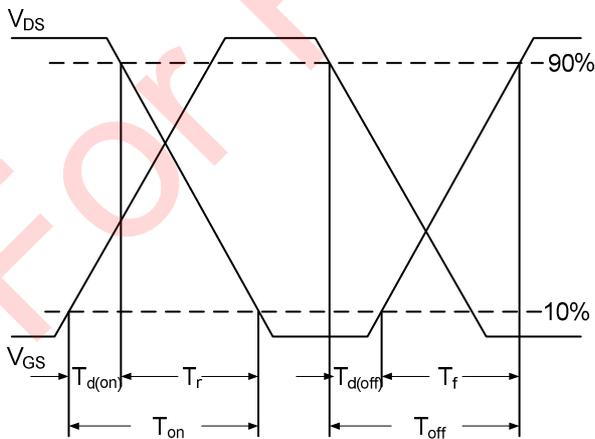


Fig.10 Switching Time Waveform

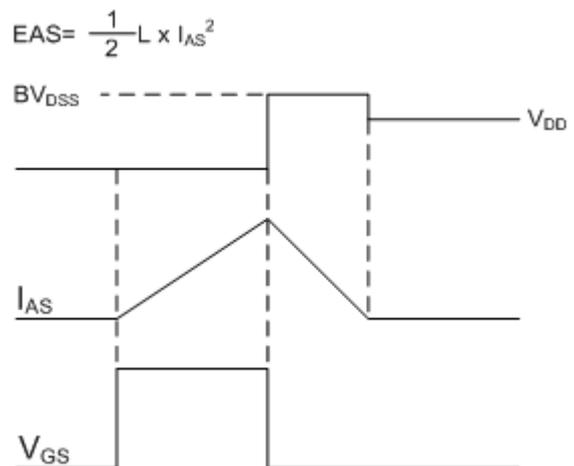
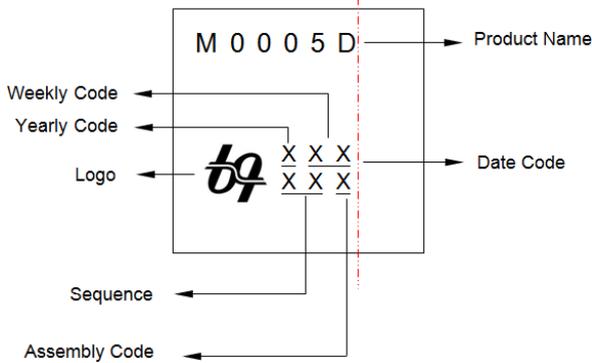


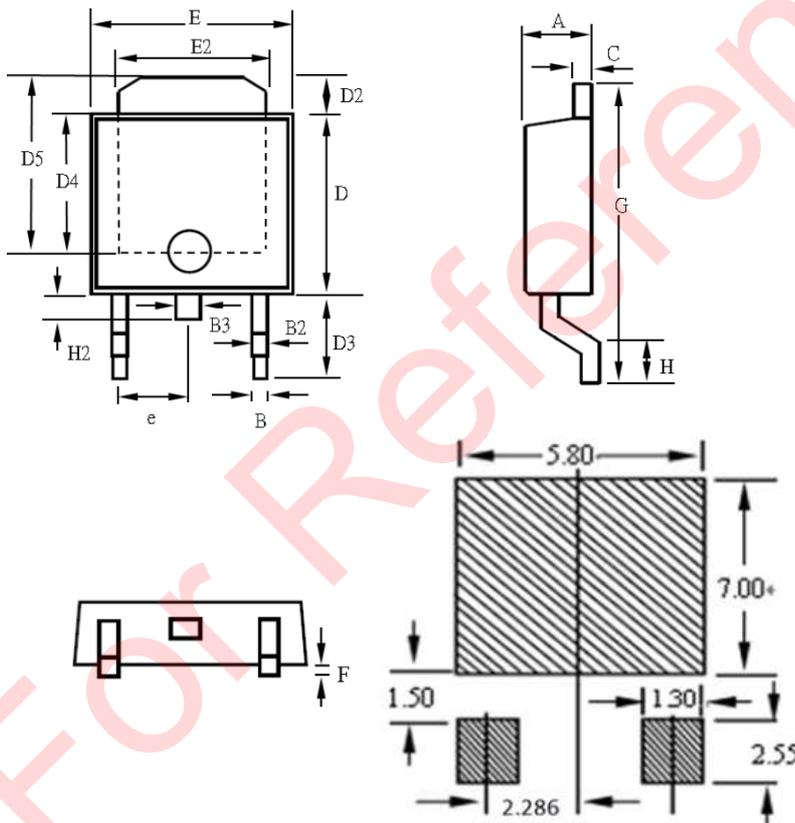
Fig.11 Unclamped Inductive Switching Waveform

All information provided in this document is subjected to important notice

Top Marking



TO252 Package Outline Drawing



LAND PATTERN RECOMMENDATION

SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	2.18	--	2.40
B	0.64	--	0.90
B2	0.76	--	1.44
B3	0.60	--	1.00
C	0.43	--	0.89
D	5.33	--	6.23
D2	0.88	--	2.03
D3	2.66	--	2.90
D4	3.04	--	--
D5	4.57	--	5.35
E	6.35	--	6.80
E2	3.81	--	5.46
F	0.00	--	0.20
G	9.39	--	10.50
H	1.38	--	1.78
H2	0.50	--	1.02
e	--	2.286	--

Note:

1. ALL DIMENSIONS LISTED ON THE DRAWING MEETING JEDEC STANDARD.
2. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
3. RECOMMENDED LAND PATTERN DESIGN IS ONLY FOR REFERENCE

All information provided in this document is subjected to important notice

Important Notice

UBIQ and its subsidiaries reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

UBIQ products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment. However, no responsibility is assumed by UBIQ or its subsidiaries for its use or application of any product or circuit; nor for any infringements of patents or other rights of third parties which may result from its use or application, including but not limited to any consequential or incidental damages. No UBIQ components are designed, intended or authorized for use in military, aerospace, automotive applications nor in systems for surgical implantation or life-sustaining. No license is granted by implication or otherwise under any patent or patent rights of UBIQ or its subsidiaries.

UBIQ Semiconductor Corp.

Headquarter

9F.,No.5, Taiyuan 1st St. Zhubei City,

Hsinchu Taiwan, R.O.C.

TEL : 886.3.560.1818 FAX : 886.3.560.1919

Sales Branch Office

12F-5, No. 408, Ruiguang Rd. Neihu District,

Taipei Taiwan, R.O.C.

TEL : 886.2.8751.2062 FAX : 886.2.8751.5064

All information provided in this document is subjected to important notice