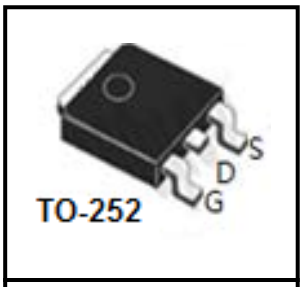
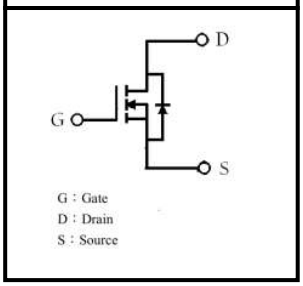
60V N-Channel Trench MOSFET

FEATURES

- Super Low Gate Charge
- 100% EAS Guaranteed
- RoHS compliant
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology





APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Hard switched and high frequency circuits

Device Marking and Package Information

Device	Package	Marking
CTD06N017	TO-252	CTD06N017

Absolute Maximum Ratings at T_j= 25ºC unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V_{GS} = 0V)	V_{DSS}	60	V
Drain Current-Continuous(Tc =25℃) (note1)	I_D	55	A
Drain Current-Continuous(Tc =100℃) (note1)		35	
Pulsed Drain Current (note2)	I_{DM}	200	A
Gate Source Voltage	V_{GSS}	±20	V
Power Dissipation T_C = 25ºC (note4)	P_D	100	W
Single Pulse Avalanche Energy (note3)	E_{AS}	64	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+175	ºC

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Case (note1)	$R_{\theta Jc}$	1.5	℃/W

Electrical Characteristics T_j= 25ºC unless otherwise specified						
Parameter	**Symbol**	**Test Conditions**	**Value**			**Unit**
			Min.	**Typ.**	**Max.**	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	60	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 60V, V_{GS} = 0V, T_J = 25ºC	--	--	1	uA
Zero Gate Voltage Drain Current		V_{DS} = 60V, V_{GS} = 0V, T_J = 55ºC	--	--	5	uA
Gate-Source Leakage	I_{GSS}	V_{GS} = ±20V	--	--	±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.0	1.6	2.5	V
Drain-Source On-Resistance (note2)	$R_{DS(on)}$	V_{GS} = 10V, I_D = 30A	--	12	17	mΩ
		V_{GS} = 4.5V, I_D = 20A	--	16	25	mΩ
Dynamic						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 15V, f = 1.0MHz	--	2890	--	pF
Output Capacitance	C_{oss}		--	140	--	
Reverse Transfer Capacitance	C_{rss}		--	124	--	
Total Gate Charge （4.5V）	Q_g	V_{DS} = 30V, I_D = 30A, V_{GS} = 10V	--	50	--	nC
Gate-Source Charge	Q_{gs}		--	6	--	
Gate-Drain Charge	Q_{gd}		--	15	--	
Turn-on Delay Time	$t_{d(on)}$	V_{DS} = 25V, I_D = 30A V_{GS} = 10V,R_G = 24Ω	--	7.4	--	ns
Turn-on Rise Time	t_r		--	5.1	--	
Turn-off Delay Time	$t_{d(off)}$		--	28.2	--	
Turn-off Fall Time	t_f		--	5.5	--	
Body Diode Characteristics						
Continuous Body Diode Current	I_S	T_C = 25 ºC	--	--	55	A
Body Diode Voltage	V_{SD}	T_J = 25ºC, I_{SD} = 20A, V_{GS} = 0V	--	--	1.2	V
Reverse Recovery Time	trr	TJ=25℃ IF= 60A, di/dt=100A/μs	--	28	--	nS
Reverse Recovery Charge	Qrr		--	40	--	NC

Notes

1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width≦300us , duty cycle≦2%
3. The EAS data shows Max. rating . The test condition is VDD =25V,VGS =10V,L=0.1mH
4. The power dissipation is limited by 175℃ junction temperature
5. The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics T_J = 25ºC, unless otherwise noted

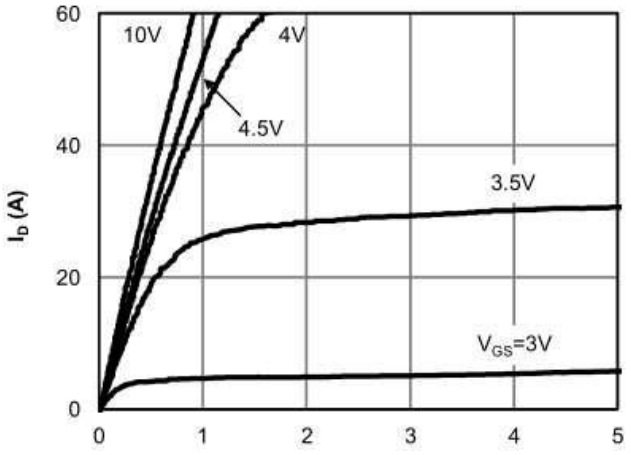


Figure 1: On-Region Characteristics (Note E)

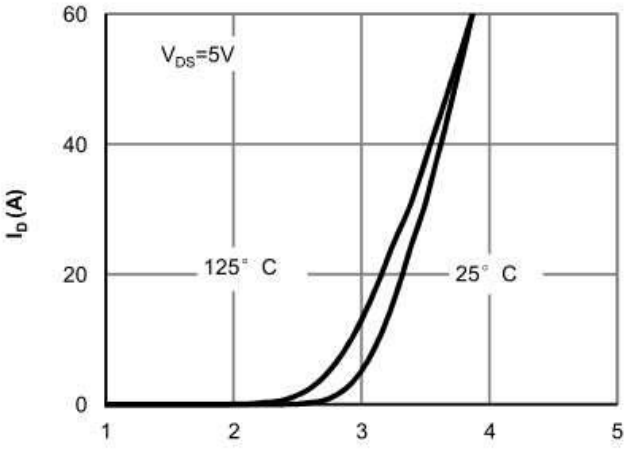


Figure 2: Transfer Characteristics (Note E)

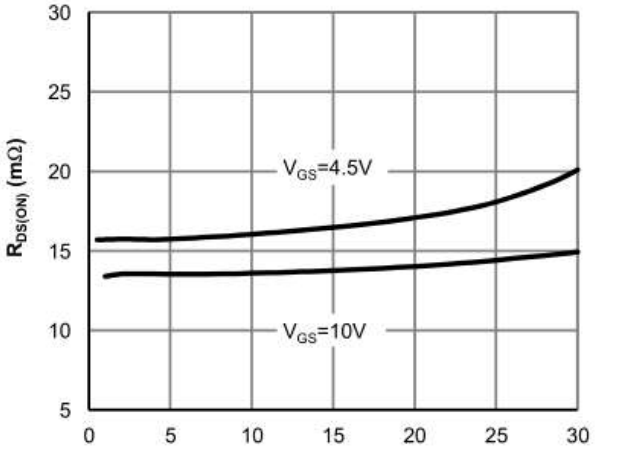


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

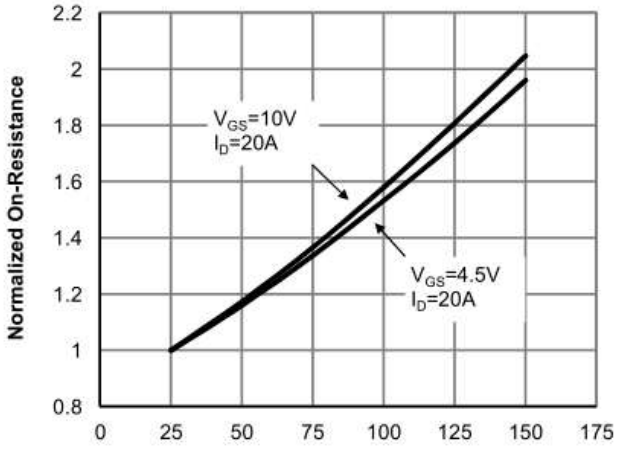


Figure 4: On-Resistance vs. Junction Temperature (Note E)

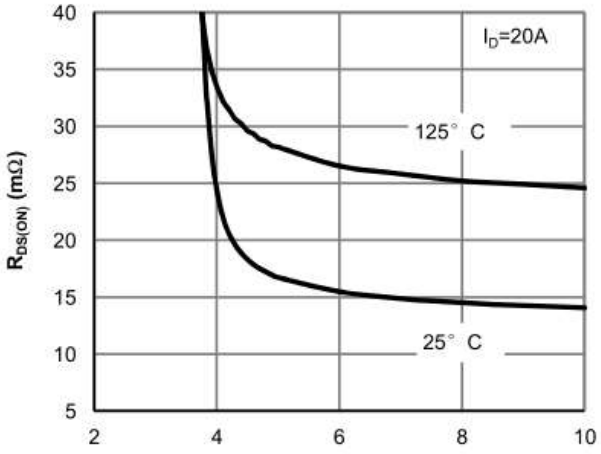


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

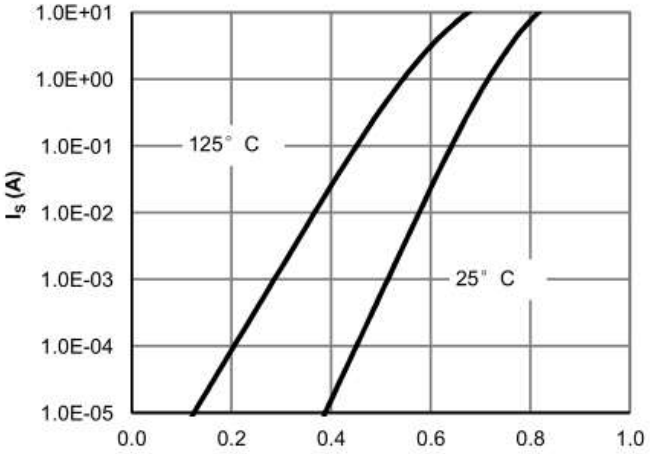


Figure 6: Body-Diode Characteristics (Note E)

Typical Characteristics T_J = 25ºC, unless otherwise noted

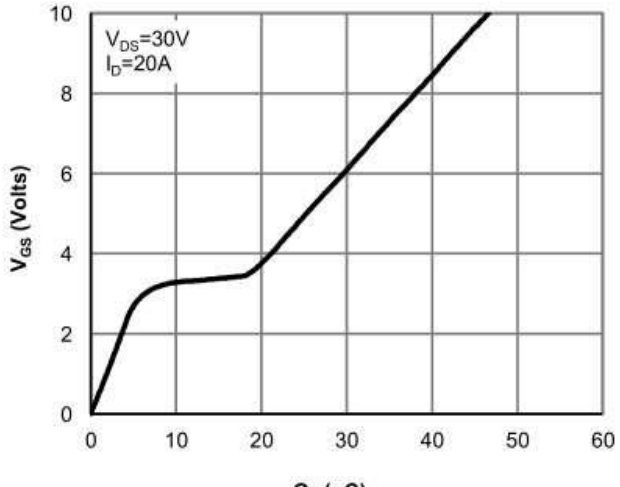


Figure 7: Gate-Charge Characteristics

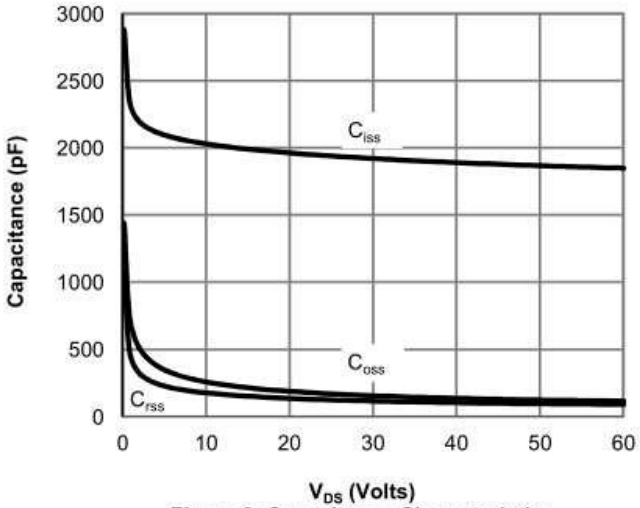
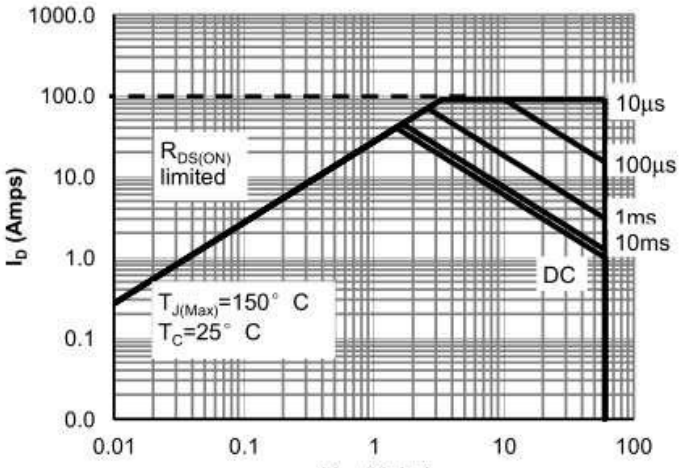


Figure 8: Capacitance Characteristics



V_{GS}> or equal to 4.5V

Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

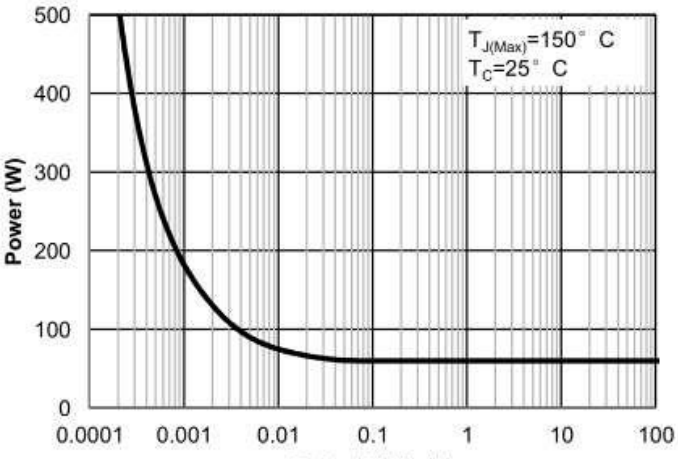


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

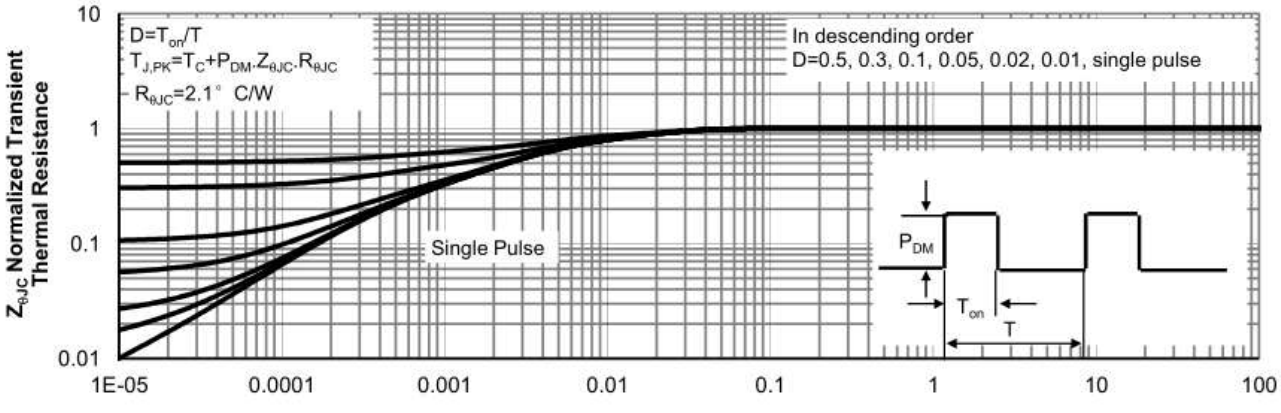


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

Figure A：Gate Charge Test Circuit and Waveform

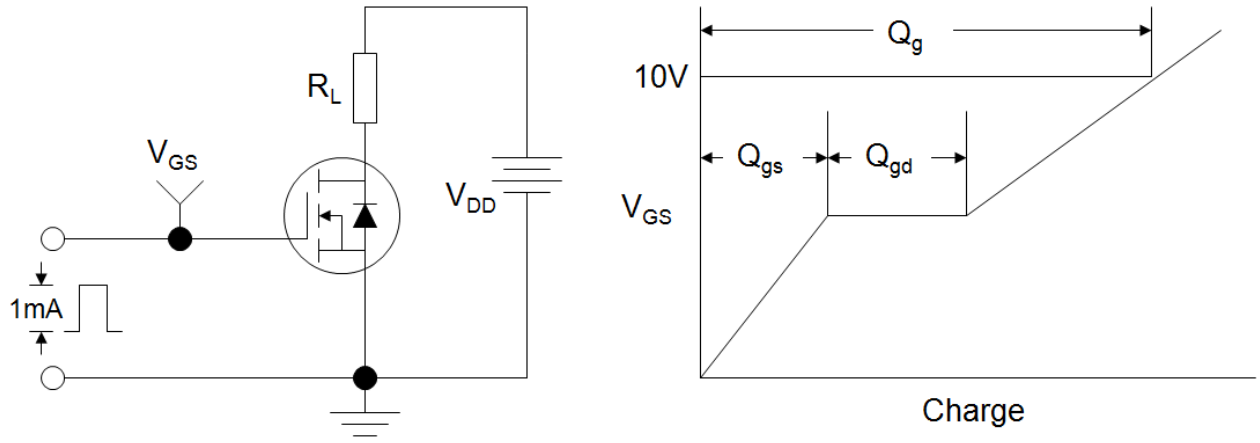


Figure B：Resistive Switching Test Circuit and Waveform

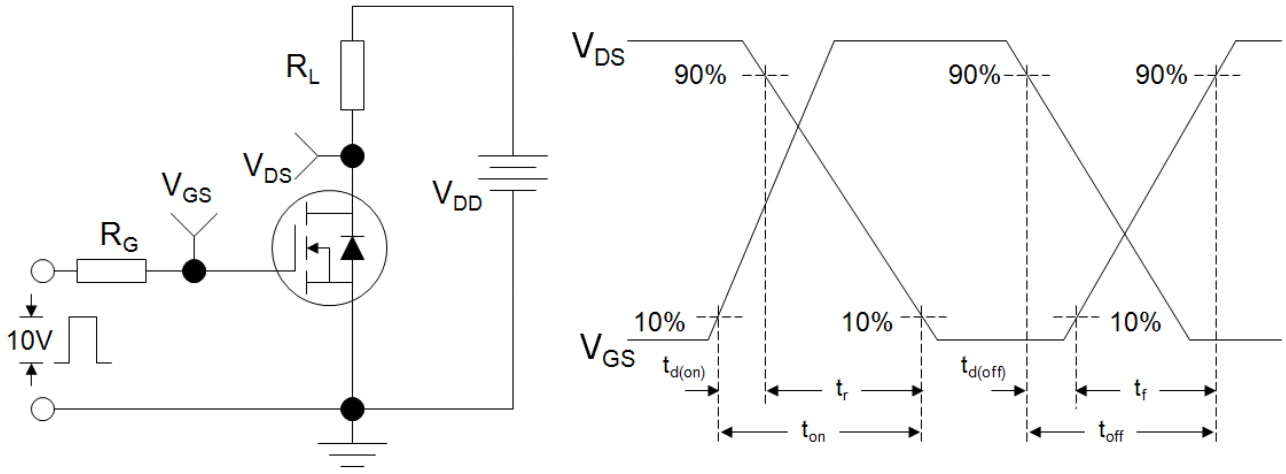
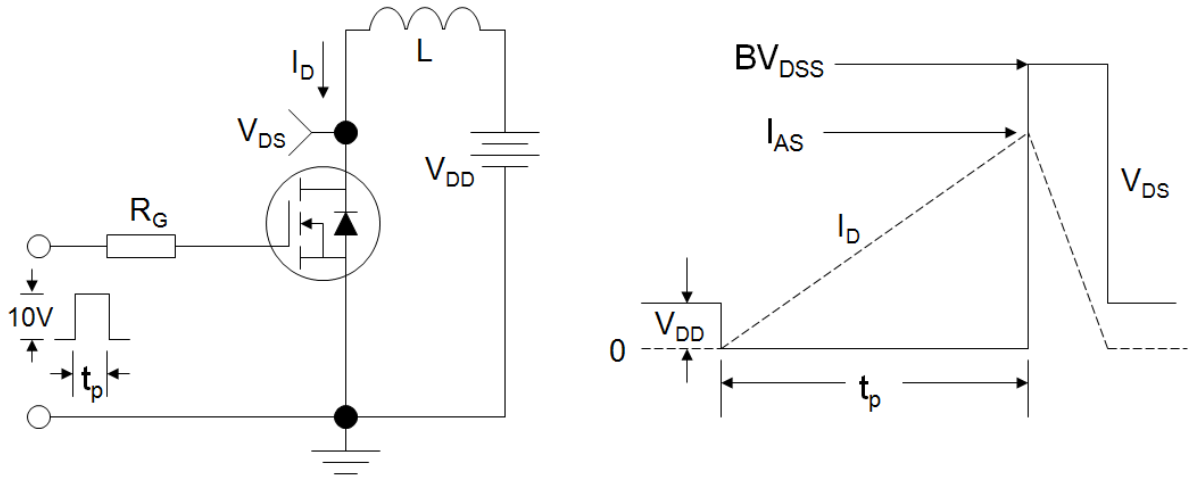
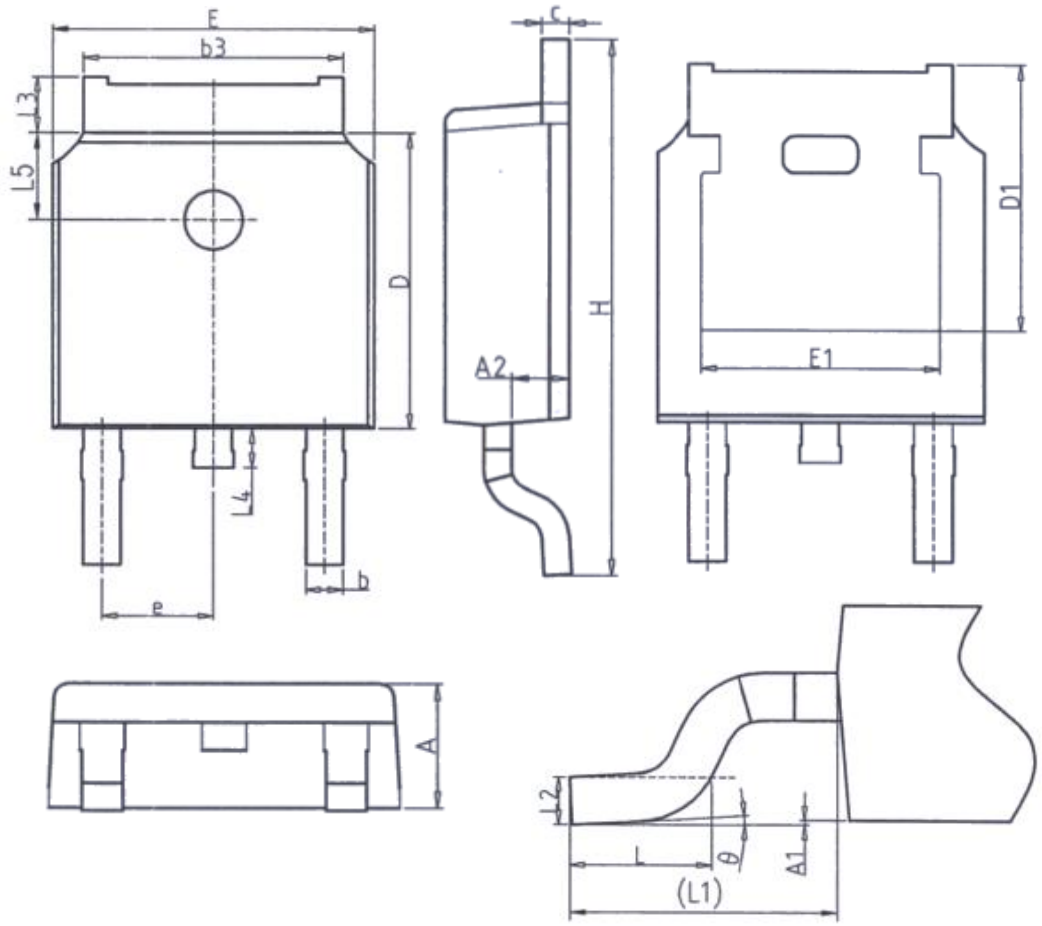


Figure C：Unclamped Inductive Switching Test Circuit and Waveform



TO-252



Unit: mm		
Symbol	Min.	Max.
A	2.20	2.40
A1	0.00	0.20
A2	0.97	1.17
b	0.68	0.90
b3	5.20	5.50
c	0.43	0.63
D	5.98	6.22
D1	5.30REF	
E	6.40	6.80
E1	4.63	–

Unit: mm		
Symbol	Min.	Max.
e	2.286BSC	
H	9.40	10.50
L	1.38	1.75
L1	2.90REF	
L2	0.51BSC	
L3	0.88	1.28
L4	–	1.00
L5	1.65	1.95
θ	0°	8°

Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Suzhou Convert does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Suzhou Convert.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless. Customers using or selling Suzhou Convert products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Suzhou Convert for any damages arising or resulting from such use or sale.

Suzhou Convert disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Suzhou Convert's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Suzhou Convert SemiConductor CO., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Suzhou Convert products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Suzhou Convert believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.