# Ferrite Chip Bead(Lead Free)

FCM1608KF-700T07

		ECN HISTOR	RY LIS	Γ	
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	13/06/06	變更可靠度條件	楊祥忠	羅培君	張嘉玲
2.0	14/01/24	變更電鍍錫層厚度 3.0um min.=>3.5um min.	楊祥忠	羅培君	張嘉玲
3.0	14/08/01	變更 Reflow 圖示	楊祥忠	羅培君	張嘉玲
3.1	14/08/01	修正包裝帶尺寸	楊祥忠	羅培君	張嘉玲
4.0	14/10/13	訂正 1608 包裝帶 Ao 尺寸	楊祥忠	羅培君	張嘉玲
5.0	16/01/26	增訂可靠度 Thermal shock: (Bead) Step3:125±2℃ 30±5min	楊祥忠	詹偉特	張嘉玲
6.0	17/02/16	修訂 Recommended PC Board Pattern	楊祥忠	詹偉特	張嘉玲
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**TAI-TECH** KBM01-170900591 P2.

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FCM1608KF-700T07

## 1.Features

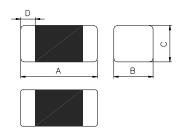
- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. S.M.T. type.
- 4. Suitable for reflow soldering.
- 5. Shapes and dimensions follow E.I.A. spec.
- 6. Available in various sizes.
- 7. Excellent solder ability and heat resistance.
- 8. High reliability.
- 9.100% Lead(Pb) & Halogen-Free and RoHS compliant.







## 2.Dimensions



Chip Size						
<b>A</b> 1.60±0.15						
В	0.80±0.15					
С	0.80±0.15					
D	0.30±0.20					

Units: mm

# 3.Part Numbering



1608 KF









A: Series

B: Dimension

LxW

C: Material

Lead Free Material 700=70  $\Omega$ 

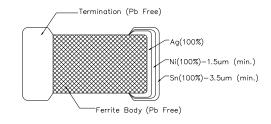
D: Impedance E: Packaging

T=Taping and Reel, B=Bulk(Bags)

**700** 

D

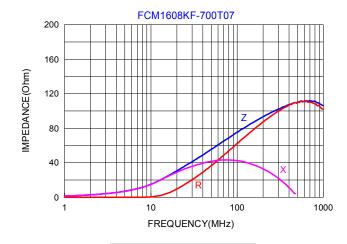
F: Rated Current 07=700m/



## 4. Specification

	Tai-Tech Impedance $(\Omega)$		Test Frequency (Hz)	DC Resistance $(\Omega)$ max.	Rated Current (mA) max.
FCM1608KF-70	0T07	70±25%	60mV/100M	0.20	700

- Rated current: based on temperature rise test
- In compliance with EIA 595
- Impedance-Frequency Characteristics



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# 5. Reliability and Test Condition

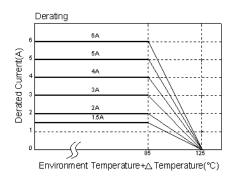
Item			Performance	9			Те	st Cond	dition	
Series No.	FCB	FCM	НСВ	GHB	FCA					
Operating Temperature		(Inclu	-40~+125℃ uding self-tempera	ture rise)						
Transportation Storage Temperature		-40~+125˚ℂ (on board)					storag		ns, please	see the
Impedance (Z)	Defeate stem	dord alcatrical	ah ara ata riati an liat			Agilent42 Agilent E Agilent42 Agilent16	287			
DC Resistance	_ Refer to stand	fer to standard electrical characteristics list								
Rated Current						DC Powe Over Rat some ris	ted Curi		ements, the	ere will be
Temperature Rise Test		1A ΔT 20℃Max ≘ 1A ΔT 40℃Ma				2. Tempe			current. by digital sı	urface
Life test	Appearance:	no damage. within±15%of in	nitial value.			times.( If Reflow F Tempera Applied of Duration Measure for 24±2	PC/JED Profiles) ture: 12 current: : 1000± ed at ro hrs.	EC J-STD 25±2°C rated cum 12hrs. om tempe	rature afte	sification
Load Humidity	Q : Shall not	•	itial value. ecification value. value and shall no	ot exceed the spe	ecification value	Preconditioning: Run through IR reflow times.(IPC/JEDEC J-STD-020D Classi Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% current. Measured at room temperature after for 24±2 hrs.			ssification % rated	
Thermal shock	Inductance: v Q : Shall not	within±15%of in within±10%of in exceed the spe		ot exceed the spe	cification value	times.( If Reflow F Condition Step1: -4 Step2: 25 Step3: + Number	PC/JED Profiles) In for 1 of 10±2°C 5±2°C 125±2°C of cycle and at ro	eycle 30±5 ≤ 0.5n 30±5m 30±5m ss: 500	nin	ssification
Vibration	Impedance : Inductance : Q : Shall not			ot exceed the spe	cification value	times.( If Reflow F Oscillation minutes Equipment Total Am	PC/JED Profiles) on Freq ent: Vi plitude: Time: 1	EC J-STD uency: 10 bration ch 1.52mm±1 2 hours(20		ssification
Bending	Appearance: No damage.  Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value  RDC: within±15% of initial value and shall not exceed the specification value  Duration of 10 sec for a min				100x1.2mm 00x0.8mm mm m					
Shock	Impedance:	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value			Test co	Peak Value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	
	Q : Shall not	exceed the spe	ecification value. value and shall no	ot exceed the spe	ecification value	SMD	50	11	Half-sine	11.3
	1.50 · WILLIIII	_10,001 IIIIII	. alue and snan III	on one ope	Januarion Value	Lead	50	11	Half-sine	11.3
Solderability	More than 95°	% of the termin	al electrode shoul	d be covered with	n solder.	Solder to Flux for I	Sn96.5% emperat ead free omplete	%-Ag3%-C ure: 245±5 e: Rosin. 9 ely cover th	S°C	on.

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Item	Performance	Test Condition
		Number of heat cycles: 1
Resistance to Soldering	Appearance : No damage. Impedance : within±15% of initial value	Temperature (°C) Time (s) Temperature ramp/immersion and emersion rate
Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value.	260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s
		Depth: completely cover the termination
Terminal strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.

#### \*\*Derating Curve

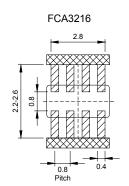
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over  $85^{\circ}\mathrm{C}$ , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



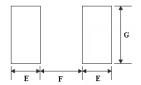
# 6. Soldering and Mounting

#### 6-1. Recommended PC Board Pattern

			Pattern ow Sold	• • • •				
Series	Туре	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)
	0603	0.6±0.03	0.30±0.03	0.30±0.03	0.15±0.05	0.35	0.30	0.40
FCB	1005	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	0.50	0.40	0.60
FCM	<mark>1608</mark>	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	<mark>0.80</mark>	<mark>0.85</mark>	0.95
нсв	0040	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30	4.05		
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.05	1.00	1.45
FCI	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	1.05	2.20	1.80
FHI	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70
FCH	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80
HCI	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40



Land
Solder Resist



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

### 6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

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#### 6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

#### 6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

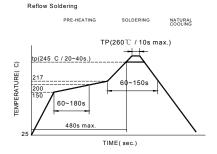
• Preheat circuit and products to 150℃

• 350 $^{\circ}$ C tip temperature (max)

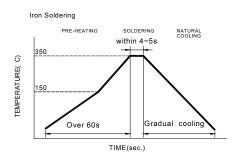
Never contact the ceramic with the iron tip

• 1.0mm tip diameter (max)

- Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5sec.



Reflow times: 3 times max Fig.1



Iron Soldering times: 1 times max

#### 6-2.3 Solder Volume:

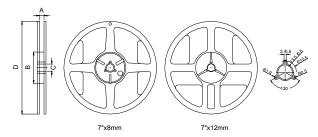
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



# 7.Packaging Information

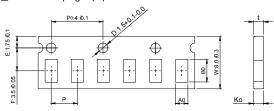
#### 7-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	<mark>60±2</mark>	<mark>13.5±0.5</mark>	<mark>178±2</mark>
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2

#### 7-2.1 Tape Dimension / 8mm

### ■Material of taping is paper



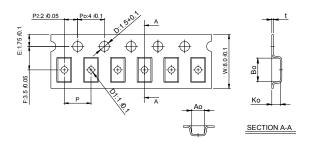
E:1.75.90.1		0.1	-  t
F:3.5 #0.1	P A	W:8.0	Ко

Size	Size Bo(mm)		Ko(mm)	P(mm)	t(mm)
060303	0.70±0.06	0.40±0.06	0.45max	2.0±0.05	0.45max
100505	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03

Size Bo(mm)		Ao(mm)	Ko(mm)	P(mm)	t(mm)	
<mark>160808</mark>	1.80±0.05 0.96+0.05/-0.0		<mark>0.95±0.05</mark>	<mark>4.0±0.10</mark>	<mark>0.95±0.05</mark>	
201209	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05	

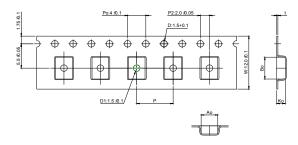
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#### ■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321611	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

#### 7-2.2 Tape Dimension / 12mm

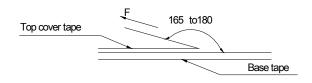


Ī	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
	451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
	453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

#### 7-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	201209	<mark>160808</mark>	100505	060303
Chip / Reel	1000	2000	2500	3000	3000	2000	4000	<mark>4000</mark>	10000	15000
Inner box	4000	8000	12500	15000	15000	10000	20000	<mark>20000</mark>	50000	75000
Middle box	20000	40000	62500	75000	75000	50000	100000	100000	250000	375000
Carton	40000	80000	125000	150000	150000	100000	200000	200000	500000	750000

#### 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed		
(℃)	(%)	(hPa)	mm/min		
5~35	45~85	860~1060	300		

### **Application Notice**

Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40  $^{\circ}$ C and 60% RH.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.