

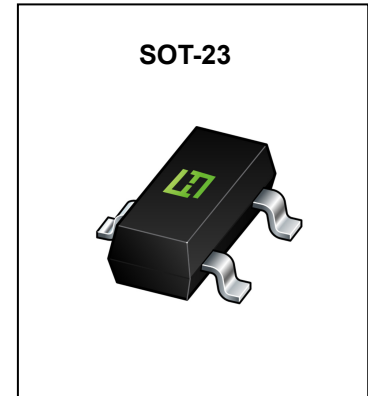
FEATURES

Complimentary to SS8550

MARKING: Y1

MAXIMUM RATINGS (T_a=25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	40	V
V _{CEO}	Collector-Emitter Voltage	25	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current	1.5	A
P _C	Collector Power Dissipation	300	mW
R _{θJA}	Thermal Resistance From Junction To Ambient	417	°C/W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature	-55~+150	°C

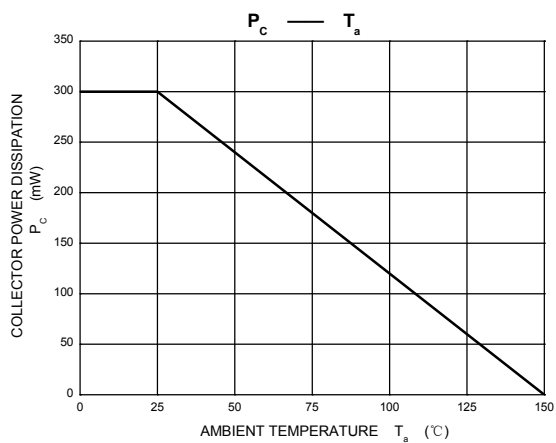
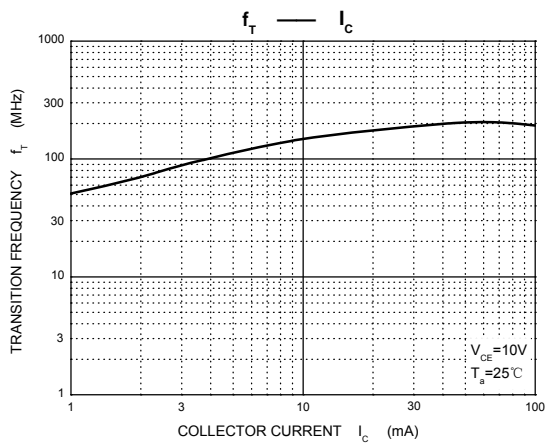
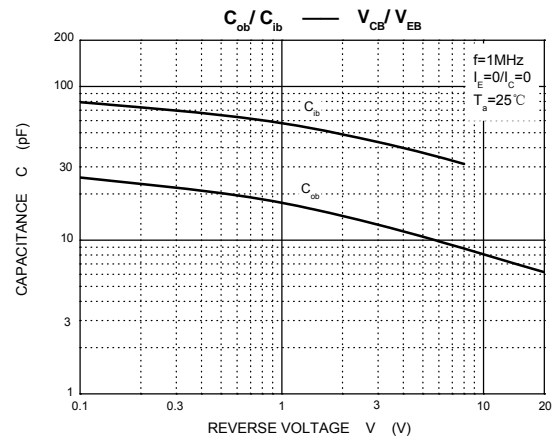
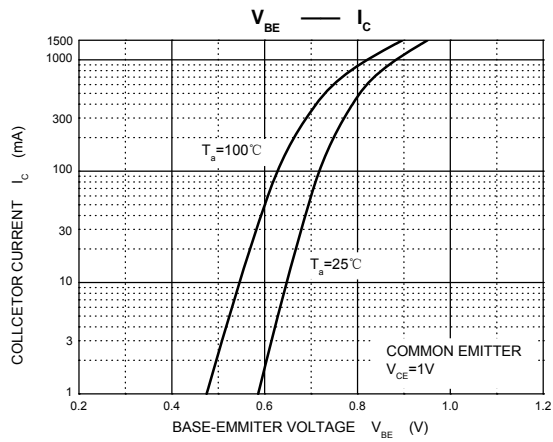
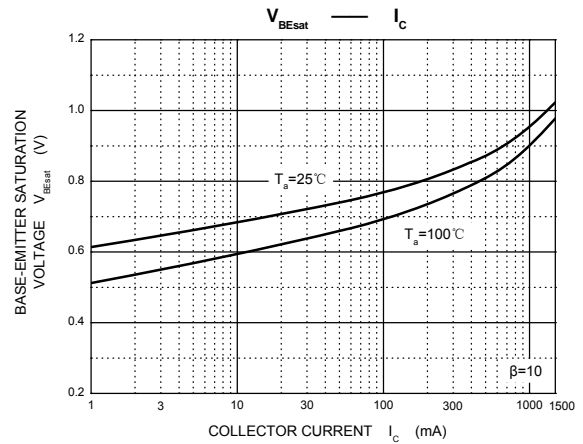
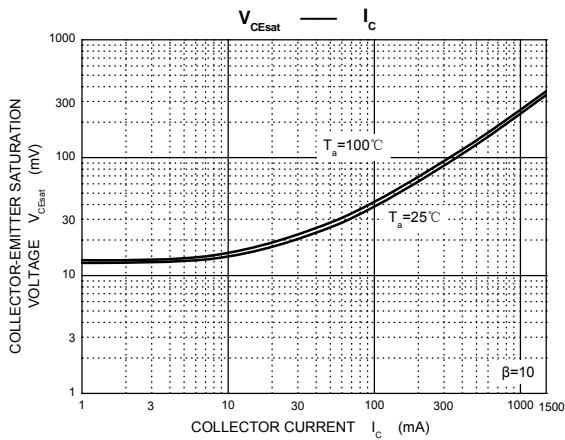
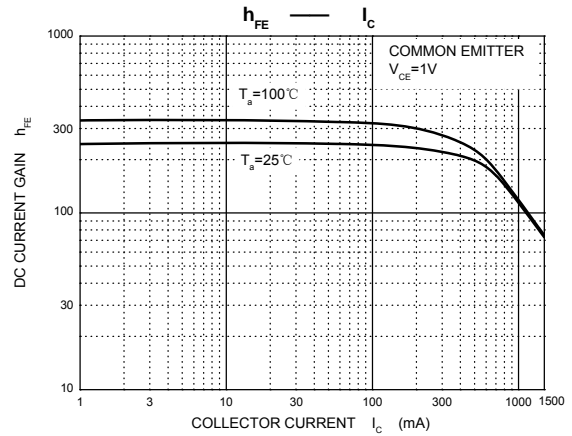
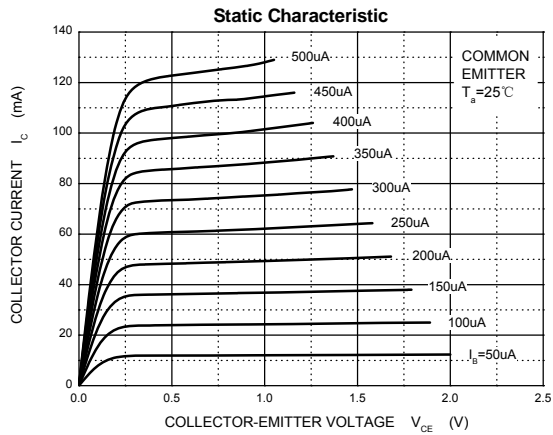


ELECTRICAL CHARACTERISTICS (T_a=25°C unless otherwise specified)

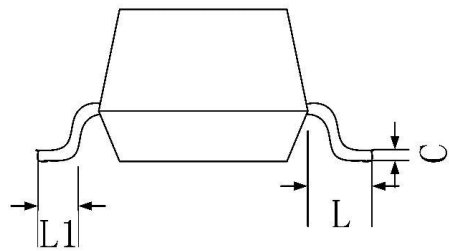
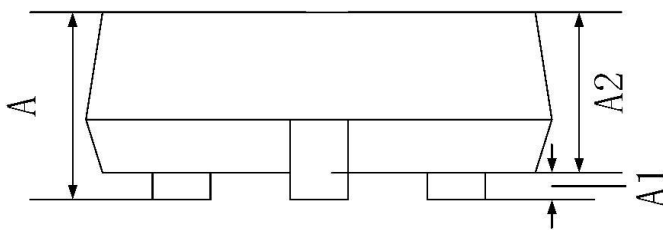
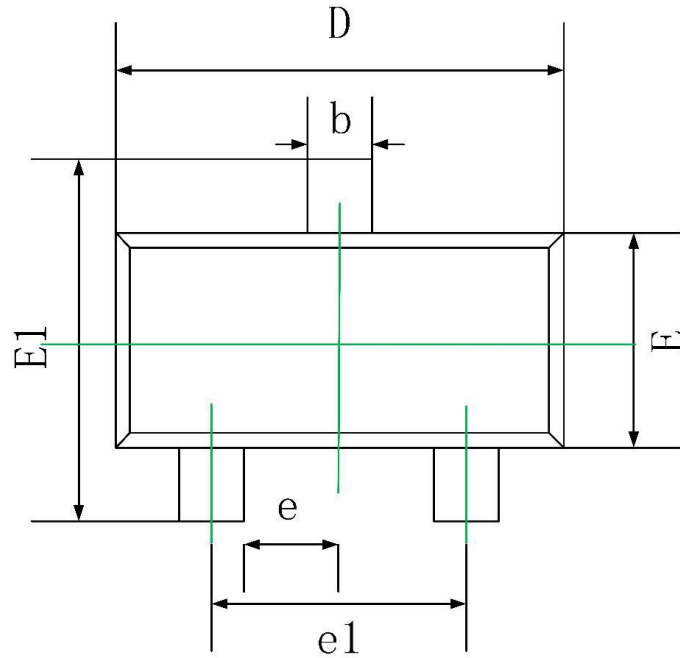
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C = 100μA, I _E =0	40			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = 0.1mA, I _B =0	25			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =100μA, I _C =0	5			V
Collector cut-off current	I _{CBO}	V _{CB} =40V, I _E =0			0.1	μA
Collector cut-off current	I _{CEO}	V _{CE} =20V, I _E =0			0.1	μA
Emitter cut-off current	I _{EBO}	V _{EB} = 5V, I _C =0			0.1	μA
DC current gain	h _{FE(1)}	V _{CE} =1V, I _C = 100mA	120		400	
	h _{FE(2)}	V _{CE} =1V, I _C = 800mA	40			
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =800mA, I _B = 80mA			0.5	V
Base-emitter saturation voltage	V _{BE(sat)}	I _C =800mA, I _B = 80mA			1.2	V
Transition frequency	f _T	V _{CE} =10V, I _C = 50mA f=30MHz	100			MHz

CLASSIFICATION OF h_{FE(1)}

Rank	L	H	J
Range	120-200	200-350	300-400



SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020

Pinning information

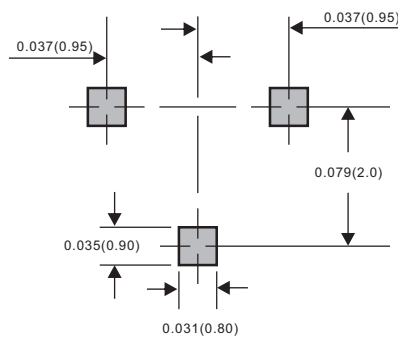
Pin	Simplified outline	Symbol
PinB Base PinC Collector PinE Emitter		

Marking

Type number	Marking code
SS8050	Y1

Suggested solder pad layout

SOT-23



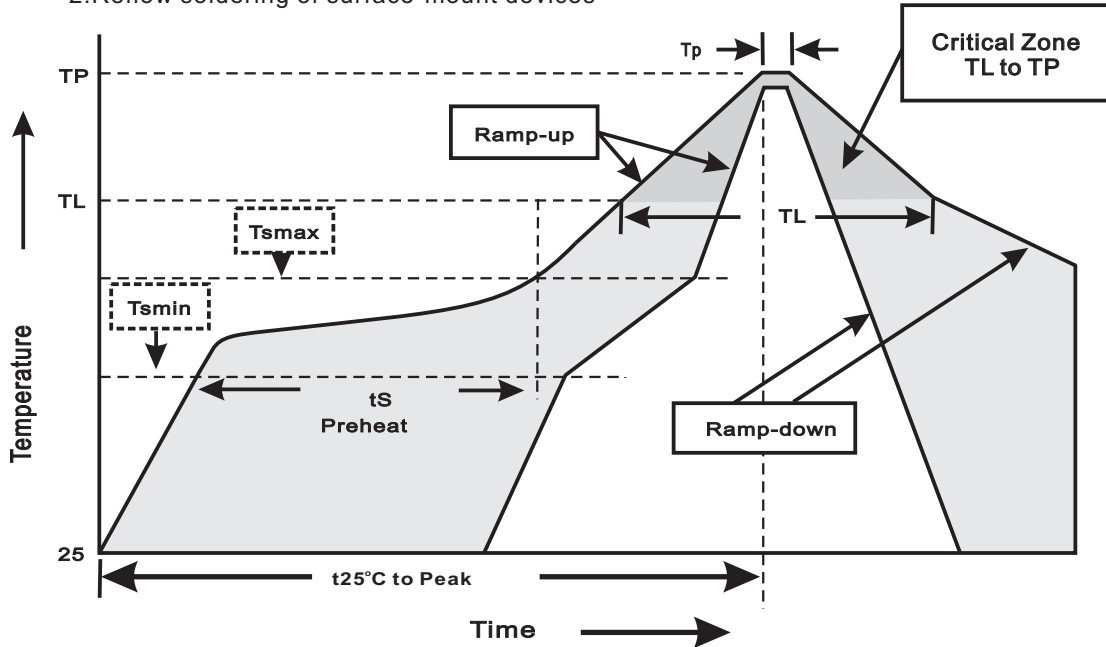
Dimensions in inches and (millimeters)

Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOT-23	7"	3,000	4.0	30,000	183*123*183	178	382*257*387	240,000	11.6

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(TL to TP)	<3°C/sec
Preheat -Temperature Min(Tsmin) -Temperature Max(Tsmax) -Time(min to max)(ts)	150°C 200°C 60~120sec
Tsmax to TL -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(TL) -Time(tL)	217°C 60~260sec
Peak Temperature(TP)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(tp)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes