

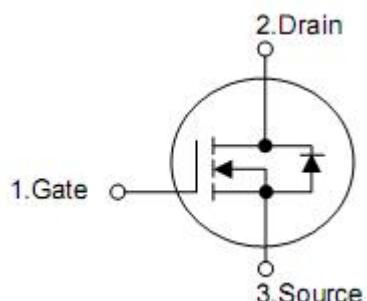
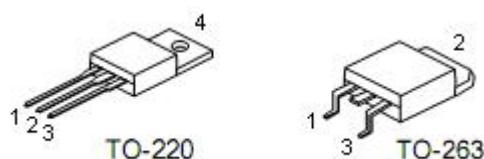
## 1. Features

- $R_{DS(ON),typ.}=3.0m\Omega$ (typ.)@ $V_{GS}=10V$
- Uses CRM(CQ) advanced Trench MOS technology
- Excellent QgxRDS(on) product(FOM)
- Extremely low on-resistance RDS(on)

## 2. Application

- Motor control and drive
- Battery management
- UPS

## 3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source
4	Drain

## 4. Ordering Information

Part Number	Package	Brand
KNP2804C	TO-220	KIA
KNB2804C	TO-263	KIA

## 5. Absolute maximum ratings

TC=25 °C unless otherwise specified

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	40	V
Gate-to-Source Voltage	V <sub>GSS</sub>	±20	
Continuous Drain Current	T <sub>C</sub> =25 °C(Silicon limited)	150	A
		90	
		80	
Pulsed Drain Current	I <sub>DM</sub>	320	
Avalanche Energy (L=0.5mH, RG=25Ω)	E <sub>AS</sub>	225	mJ
Maximum power Dissipation	P <sub>D</sub>	230	W
Junction & Storage Temperature Range	T <sub>J</sub> & T <sub>STG</sub>	-55 to 150	°C

## 6. Thermal characteristics

Parameter	Symbol	Ratings	Units
Thermal resistance, Junction-case	R <sub>θJC</sub>	0.54	°C/W
Thermal resistance, junction-ambient	R <sub>θJA</sub>	105	°C/W

## 7. Electrical characteristics

( $T_J=25^\circ\text{C}$ , unless otherwise notes)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static characteristics						
Drain-source breakdown voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	-	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=36\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	2.0	3.0	V
Gate leakage current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
Drain-source on-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=30\text{A}$	-	3.0	4.0	$\text{m}\Omega$
Forward Transconductance	$g_{\text{fs}}$	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=40\text{A}$	-	126	-	S
Dynamic characteristics						
Gate Resistance	$R_{\text{G}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}$ Frequency=1MHz	-	1.5	-	$\Omega$
Input capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V},$ F=1MHz	-	5900	-	pF
Output capacitance	$C_{\text{oss}}$		-	690	-	pF
Reverse transfer capacitance	$C_{\text{rss}}$		-	640	-	pF
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=40\text{A},$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=3\Omega$	-	28	-	ns
Rise time	$t_{\text{r}}$		-	68	-	ns
Turn-off delay time	$t_{\text{d}(\text{off})}$		-	110	-	ns
Fall time	$t_{\text{f}}$		-	32	-	ns
Gate Charge Characteristics						
Total gate charge	$Q_{\text{g}}$	$V_{\text{DS}}=32\text{V}, I_{\text{D}}=40\text{A},$ $V_{\text{GS}}=10\text{V}$	-	120	-	nC
Gate-source charge	$Q_{\text{gs}}$		-	18	-	nC
Gate-drain charge	$Q_{\text{gd}}$		-	34	-	nC
Diode characteristics						
Diode forward voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{SD}}=30\text{A}$	-	0.85	1.3	V
Drain Continuous Forward current	$I_{\text{s}}$		-	-	150	A
Reverse recovery time	$t_{\text{rr}}$	IF=40A $\text{di}/\text{dt}=100\text{A}/\mu\text{s}$	-	40	-	ns
Reverse recovery charge	$Q_{\text{rr}}$		-	41	-	nC

## 8. Typical Characteristics

Fig 1: Output Characteristics

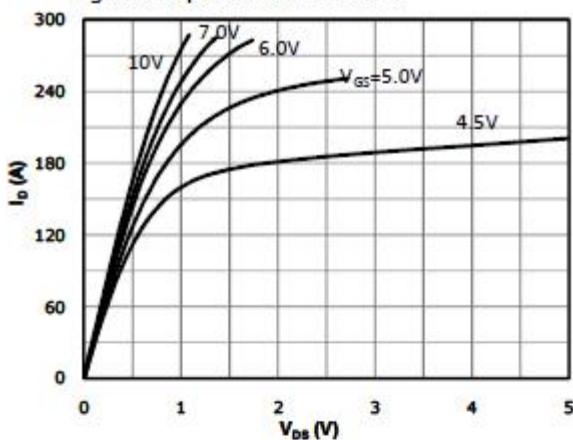


Fig 2: Transfer Characteristics

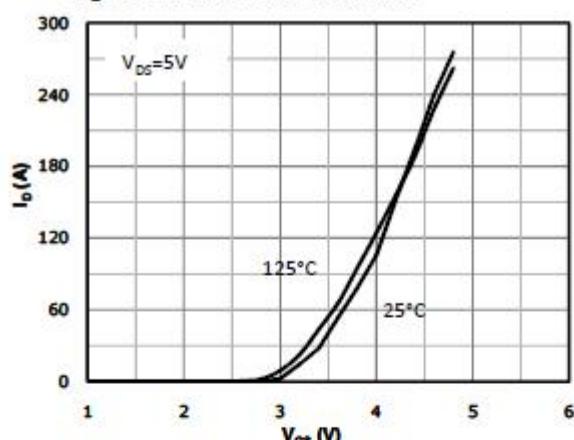


Fig 3: R<sub>ds(on)</sub> vs Drain Current and Gate Voltage

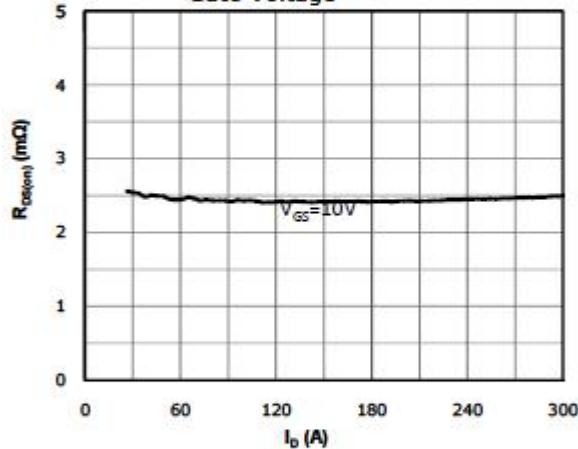


Fig 4: R<sub>ds(on)</sub> vs Gate Voltage

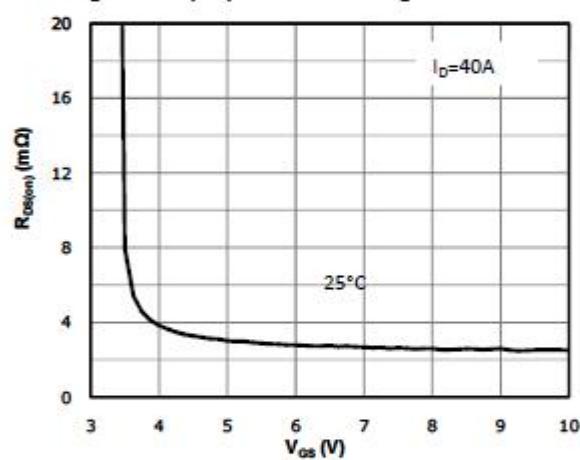


Fig 5: R<sub>ds(on)</sub> vs. Temperature

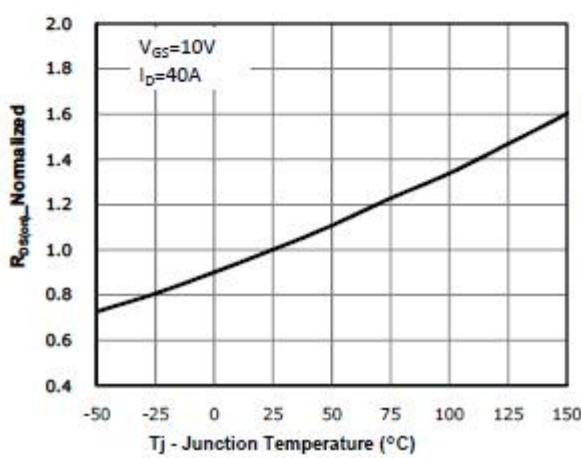


Fig 6: Capacitance Characteristics

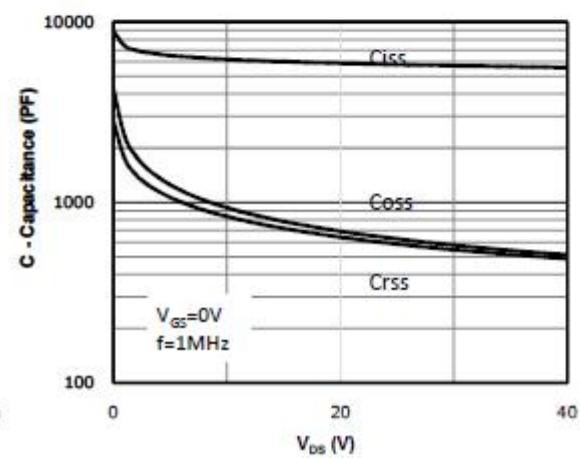


Fig 7: Gate Charge Characteristics

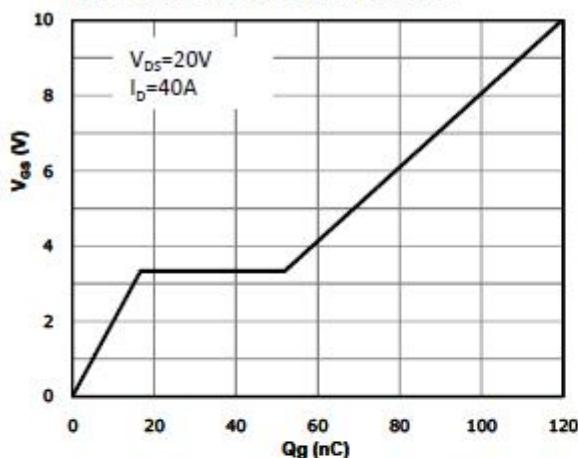


Fig 8: Body-diode Forward Characteristics

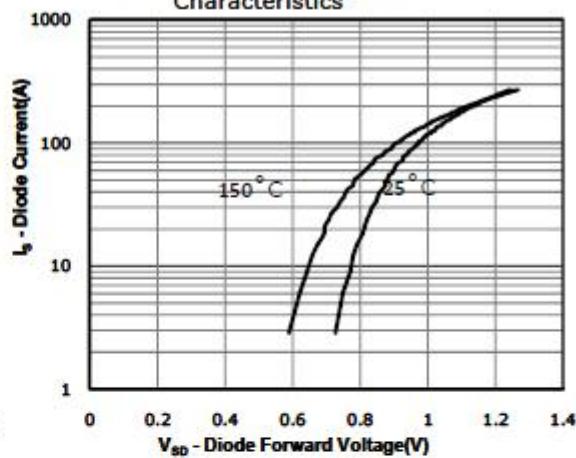


Fig 9: Safe Operating Area

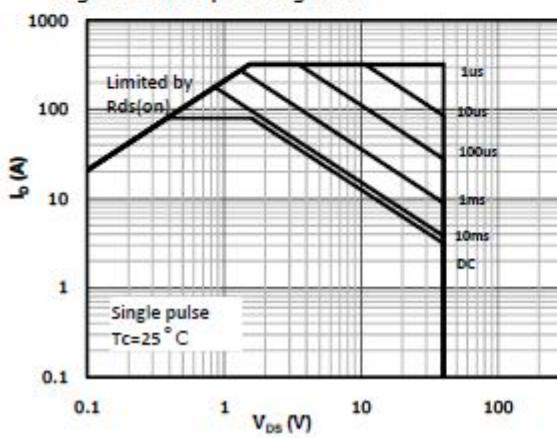


Fig 10: Drain Current Derating

