

Features

- N-Channel
100V/5A,
 $R_{DS(ON)} = 95\text{ m}\Omega @ V_{GS} = 10\text{V}$
- P-Channel
-100V/-4A,
 $R_{DS(ON)} = 185\text{ m}\Omega @ V_{GS} = -10\text{V}$

Application

- DC-DC primary bridge
- DC-DC Synchronous rectification
- Hot swap
- Fan drive

Product Summary

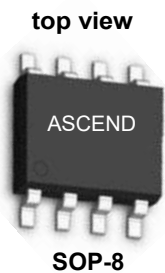


● N-Channel

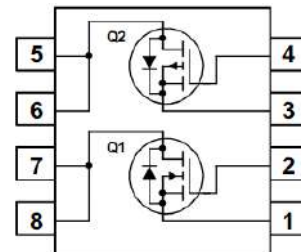
V_{DS}	100	V
$R_{DS(on), Typ @ V_{GS}=10\text{ V}}$	95	m Ω
I_D	5	A

● P-Channel

V_{DS}	-100	V
$R_{DS(on), Typ @ V_{GS}=-10\text{ V}}$	185	m Ω
I_D	-4	A



Simplified Schematic



Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	N-CH	P-CH	Units
V _{DSS}	Drain-Source Voltage	100	-100	V
V _{GSS}	Gate-Source Voltage	±20	±20	V
I _D	Drain Current - Continuous (Note 1a)	5	-4	A
	- Pulsed	20	-20	
P _D	Power Dissipation for Dual Operation	2.5		W
	Power Dissipation for Single Operation (Note 1a)	1.6		
	(Note 1b)	1		
	(Note 1c)	0.9		
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150		°C

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1a)	78	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 1)	40	$^\circ\text{C/W}$

Electrical Characteristics
 $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Type	Min	Typ	Max	Units
Off Characteristics							
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$ $V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	N-CH P-CH	100 -100			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250\text{ }\mu\text{A}$, Referenced to 25°C $I_D = -250\text{ }\mu\text{A}$, Referenced to 25°C	N-CH P-CH		25 -22		mV/ $^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}$ $V_{DS} = -24\text{ V}, V_{GS} = 0\text{ V}$	N-CH P-CH			1 -1	μA
I_{GSSF}	Gate-Body Leakage, Forward	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$	All			100	nA
I_{GSSR}	Gate-Body Leakage, Reverse	$V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$	All			-100	nA

On Characteristics (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$ $V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	N-CH P-CH	1 -2	1.6 -	3 -4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250\text{ }\mu\text{A}$, Referenced to 25°C $I_D = -250\text{ }\mu\text{A}$, Referenced to 25°C			-4.3 4		mV/ $^\circ\text{C}$
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 4\text{ A}, T_J = 25^\circ\text{C}$	N-CH			95	m Ω
		$V_{GS} = -10\text{ V}, I_D = -3\text{ A}, T_J = 25^\circ\text{C}$	P-CH			185	
$I_{D(on)}$	On-State Drain Current	$V_{GS} = 10\text{ V}, V_{DS} = 5\text{ V}$ $V_{GS} = -10\text{ V}, V_{DS} = -5\text{ V}$	N-CH P-CH	5 -4			A
g_{FS}	Forward Transconductance	$V_{DS} = 5\text{ V}, I_D = 7\text{ A}$ $V_{DS} = -5\text{ V}, I_D = -5\text{ A}$	N-CH P-CH		11 11		S

Dynamic Characteristics

C _{iss}	Input Capacitance	N-CH V _{DS} = 10 V, V _{GS} = 0 V, f = 1.0 MHz P-CH V _{DS} = -10 V, V _{GS} = 0 V, f = 1.0 MHz	N-CH		620		pF
C _{oss}	Output Capacitance		P-CH		620		pF
			N-CH		120		
C _{rss}	Reverse Transfer Capacitance		P-CH		220		pF
		N-CH		31			
			P-CH		65		

Switching Characteristics (Note 2)

t _{d(on)}	Turn-On Delay Time	N-CH V _{DD} = 10 V, I _D = 1 A, V _{GS} = 10V, R _{GEN} = 6 Ω	N-CH		12		ns
t _r	Turn-On Rise Time		P-CH		14		
t _{d(off)}	Turn-Off Delay Time	P-CH V _{DD} = -10 V, I _D = -1 A, V _{GS} = -10V, R _{GEN} = 6 Ω	N-CH		400		ns
t _f	Turn-Off Fall Time		P-CH		160		
Q _g	Total Gate Charge	N-CH V _{DS} = 15 V, I _D = 4 A, V _{GS} = 10 V	N-CH		20		ns
Q _{gs}	Gate-Source Charge		P-CH		35		
Q _{gd}	Gate-Drain Charge	P-CH V _{DS} = -15 V, I _D = -3 A, V _{GS} = -10 V	N-CH		120		ns
			P-CH		60		
		N-CH V _{DS} = 15 V, I _D = 4 A, V _{GS} = 10 V	N-CH		12		nC
			P-CH		21		
		P-CH V _{DS} = -15 V, I _D = -3 A, V _{GS} = -10 V	N-CH		2.5		nC
			P-CH		4.6		
		N-CH V _{DS} = -15 V, I _D = -3 A, V _{GS} = -10 V	N-CH		9.0		nC
			P-CH		11.5		

Drain-Source Diode Characteristics and Maximum Ratings

I_S	Maximum Continuous Drain-Source Diode Forward Current		N-CH P-CH			-5 -4	A
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 1.3\text{ A}$ (Note 2) $V_{GS} = 0\text{ V}, I_S = -1.3\text{ A}$ (Note 2)	N-CH P-CH			1.5 -1.2	V

Typical Characteristics:N-channel

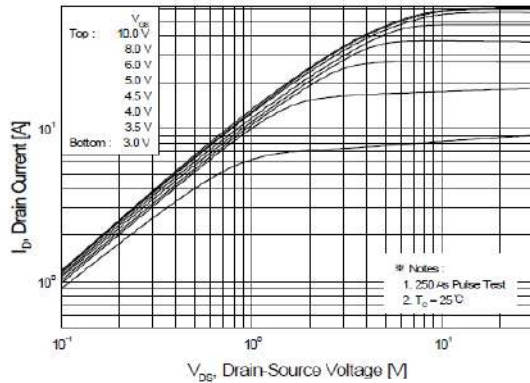


Figure 1. On-Region Characteristics

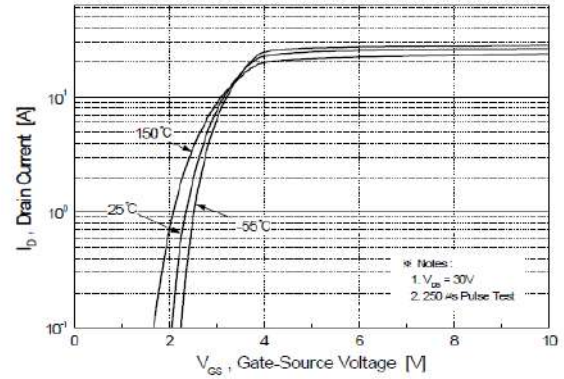


Figure 2. Transfer Characteristics

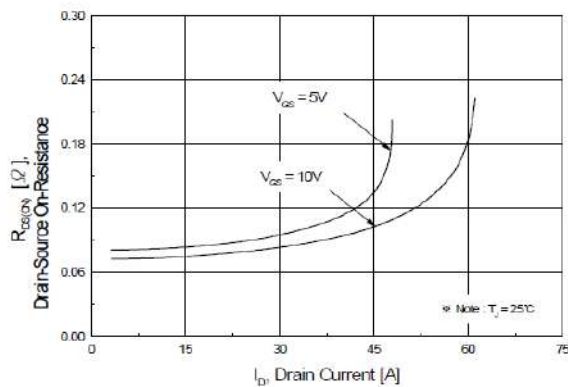


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

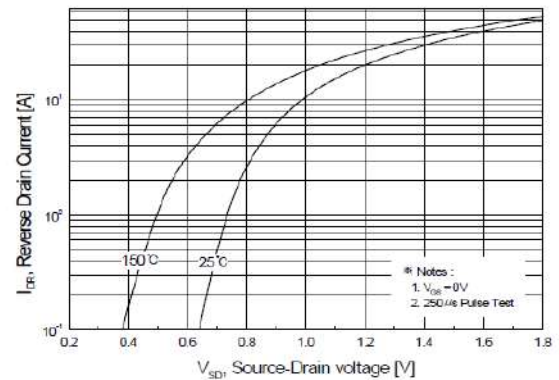


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

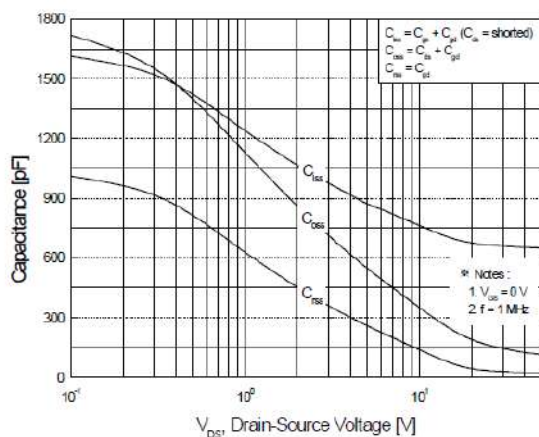


Figure 5. Capacitance Characteristics

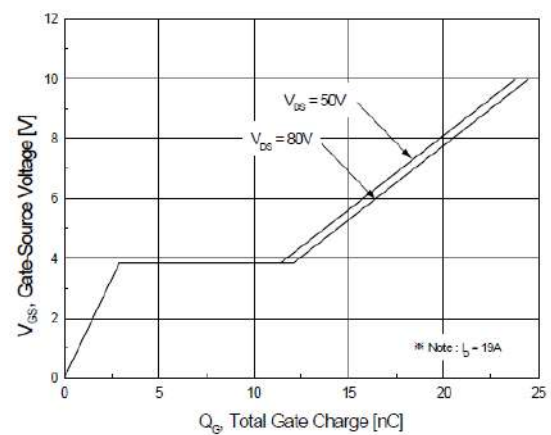


Figure 6. Gate Charge Characteristics

Typical Characteristics:N-channel

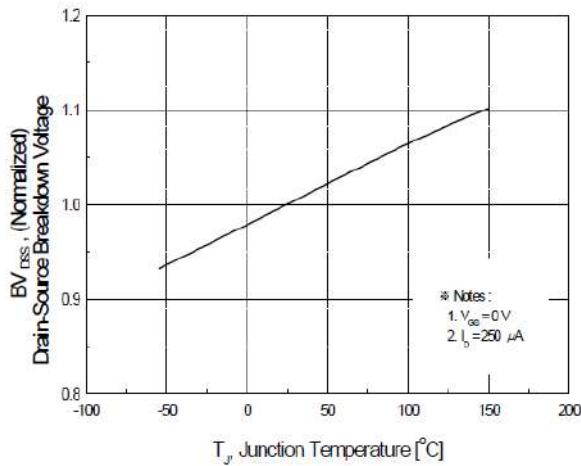


Figure 7. Breakdown Voltage Variation vs. Temperature

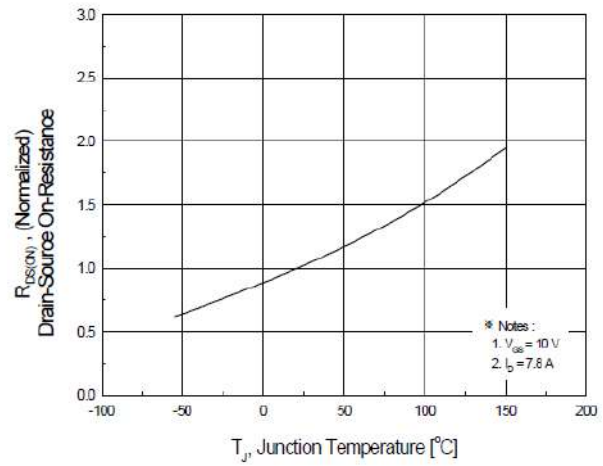


Figure 8. On-Resistance Variation vs. Temperature

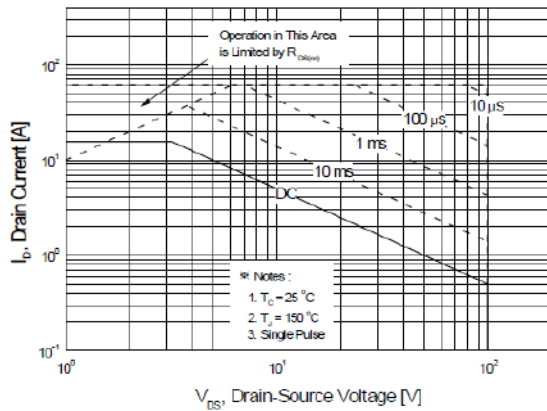


Figure 9. Maximum Safe Operating Area

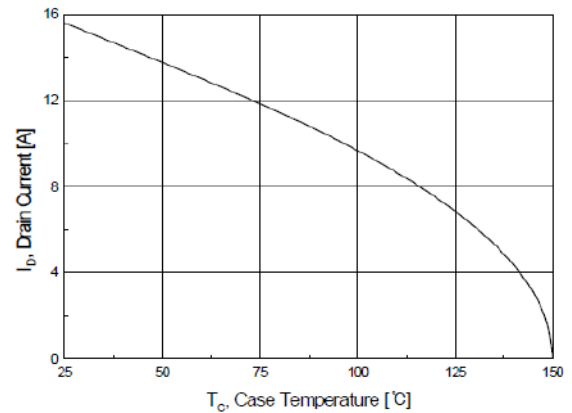


Figure 10. Maximum Drain Current vs. Case Temperature

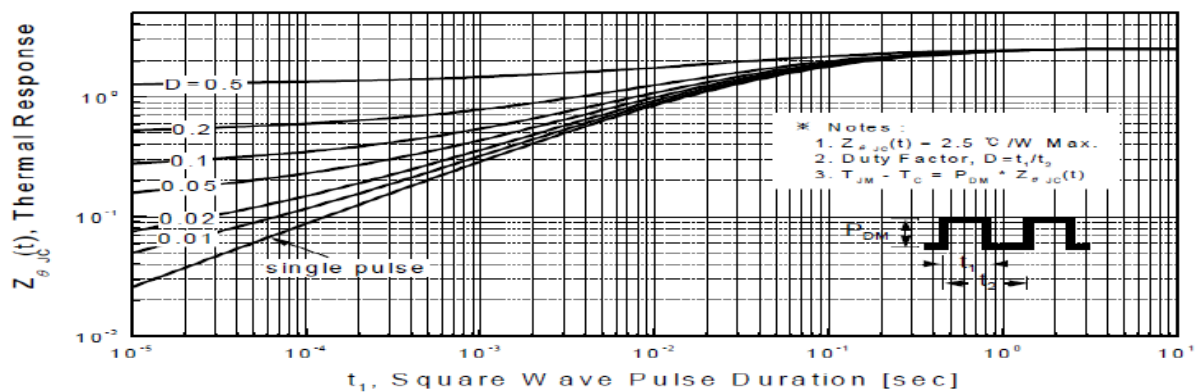


Figure 11. Transient Thermal Response Curve

Typical Characteristics:P-channel

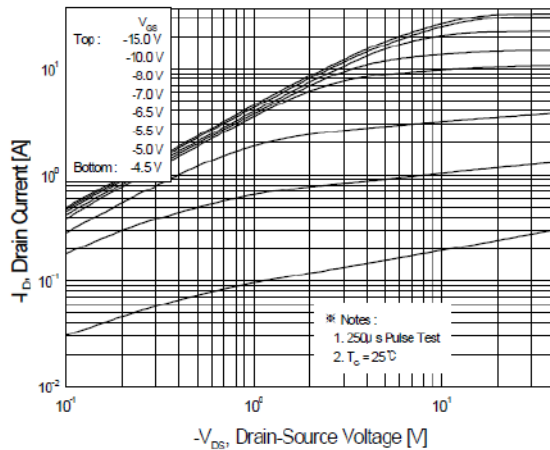


Figure 1. On-Region Characteristics

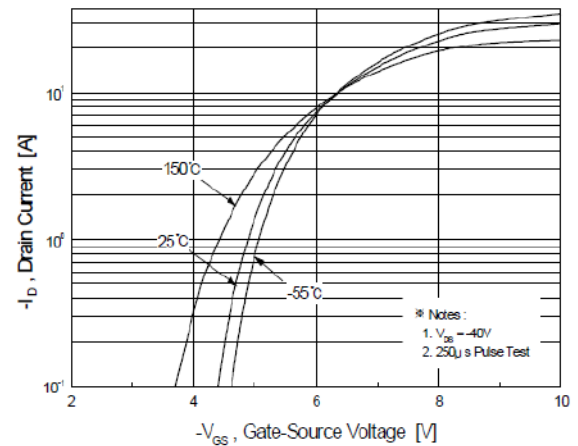


Figure 2. Transfer Characteristics

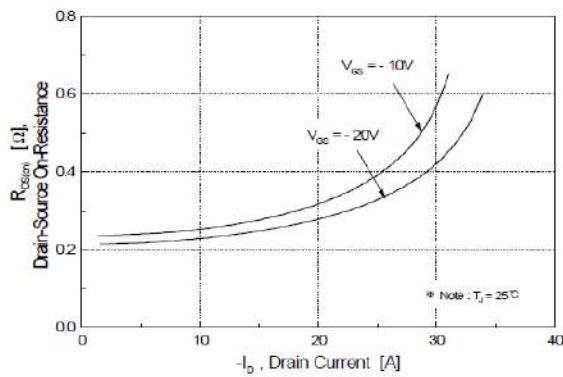


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

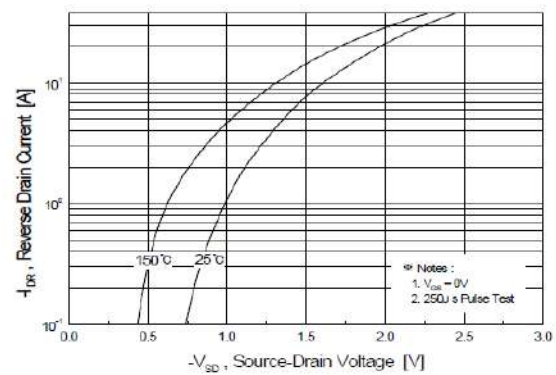


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

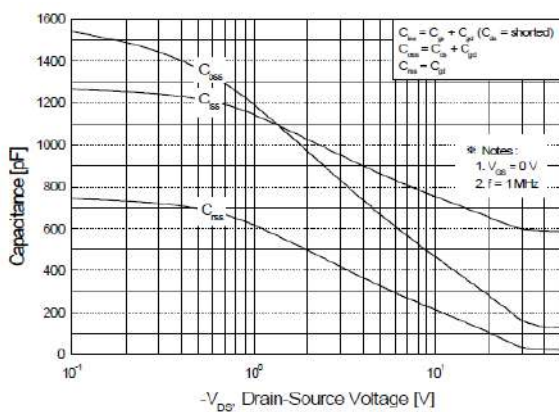


Figure 5. Capacitance Characteristics

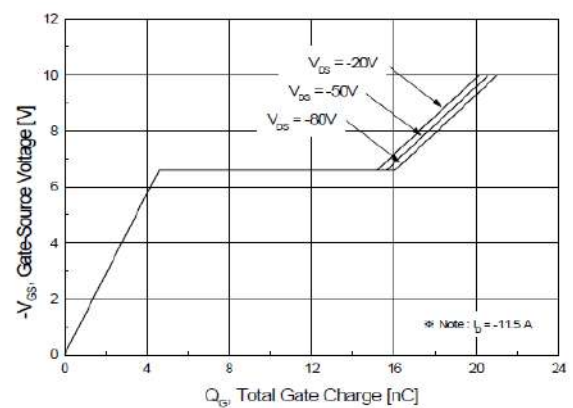


Figure 6. Gate Charge Characteristics

Typical Characteristics:P-channel

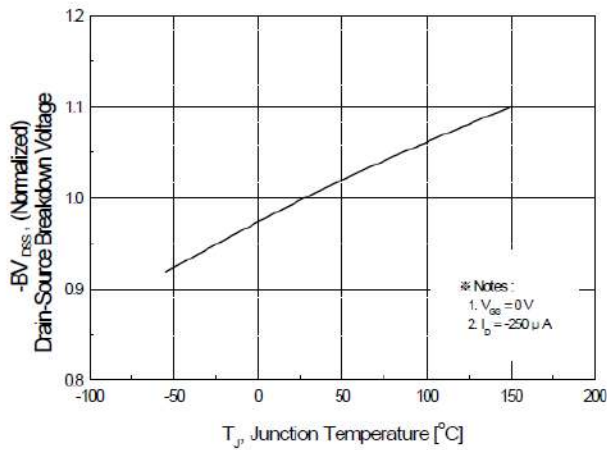


Figure 7. Breakdown Voltage Variation vs. Temperature

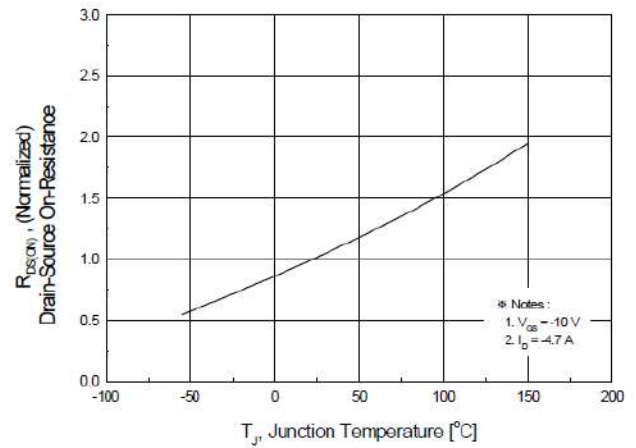


Figure 8. On-Resistance Variation vs. Temperature

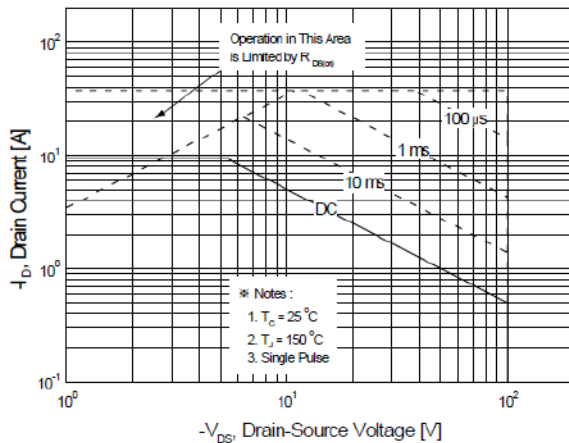


Figure 9. Maximum Safe Operating Area

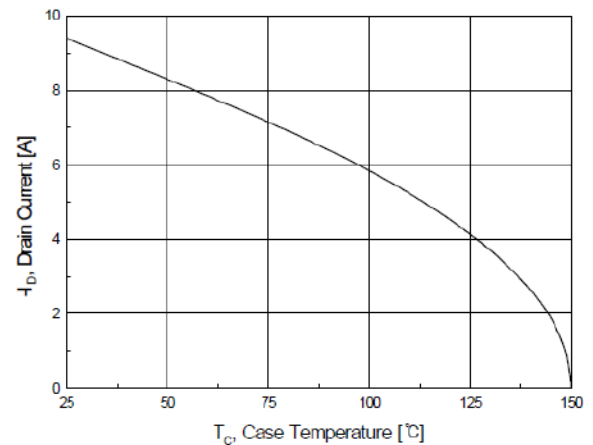


Figure 10. Maximum Drain Current vs. Case Temperature

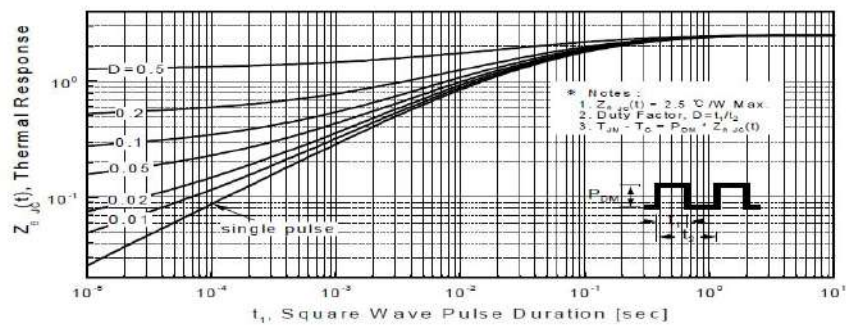
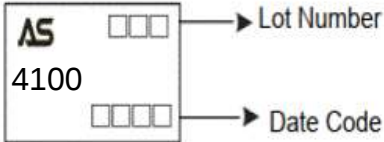


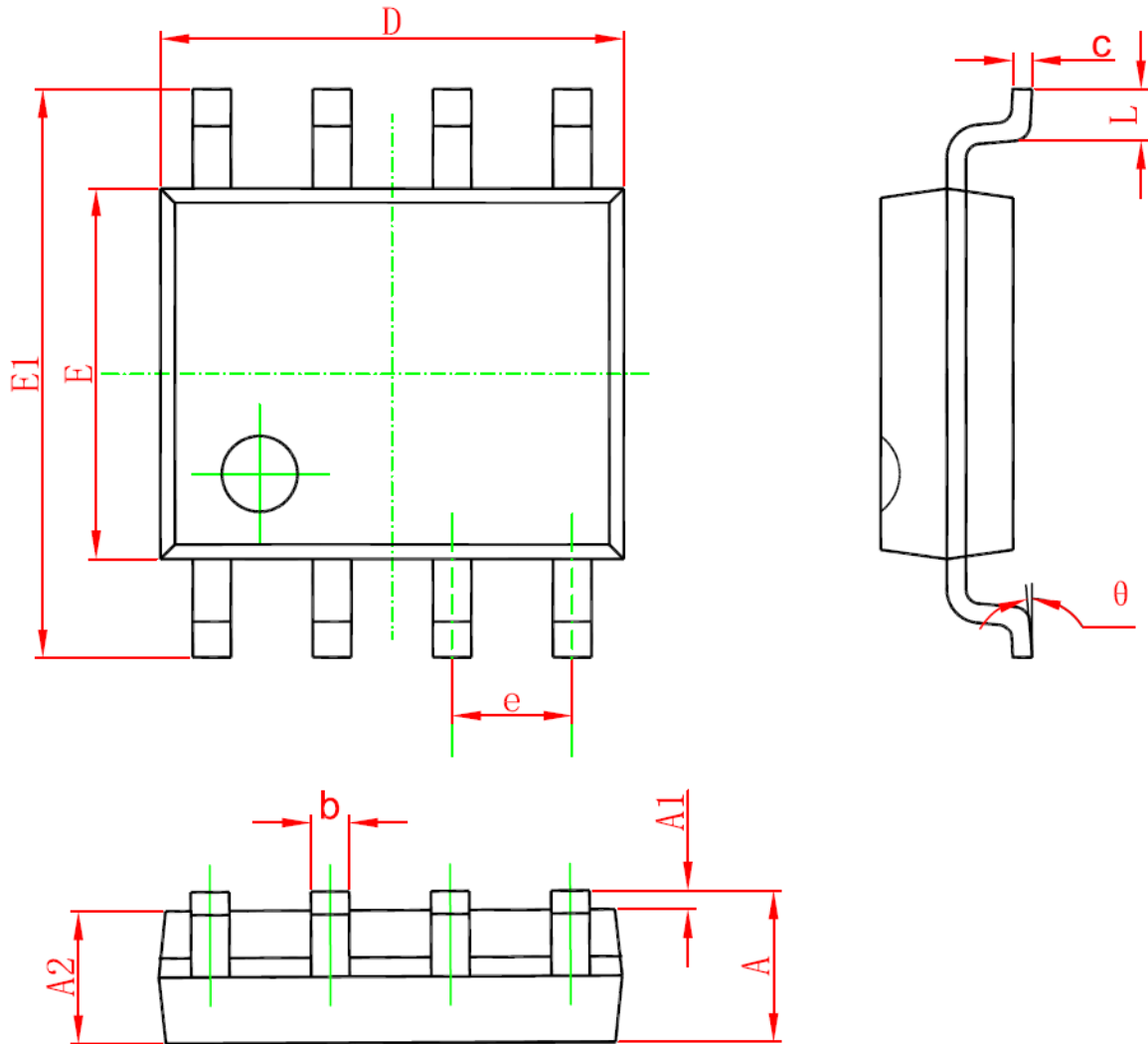
Figure 11. Transient Thermal Response Curve

Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM4100S-R	4100	SOP-8	Tape&Reel	4000

PACKAGE	MARKING
SOP-8	 The diagram shows a rectangular SOP-8 package with the following markings: 'AS' in the top left, '4100' in the center, and two sets of four empty boxes (one above and one below '4100'). Arrows point from the top set of boxes to 'Lot Number' and from the bottom set of boxes to 'Date Code'.

SOP-8 PACKAGE IN FORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

IMPORTANT NOTICE

Xi'an Ascend Semiconductor incorporated MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Xi'an Ascend Semiconductor Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Xi'an Ascend Semiconductor Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Xi'an Ascend Semiconductor Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume .

all risks of such use and will agree to hold Ascendsemi Incorporated and all the companies whose products are represented on Xi'an Ascend Semiconductor Incorporated website, harmless against all damages.

Xi'an Ascend Semiconductor Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Xi'an Ascend Semiconductor Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Xi'an Ascend Semiconductor Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

www.ascendsemi.com