

## SS52U THRU SS525U

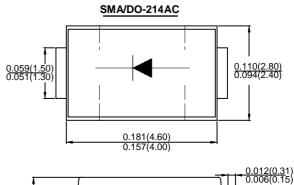
### 5.0 AMP SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

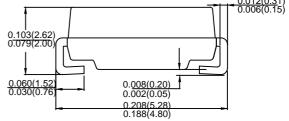
#### **Features**

- · Schottky Brrier Chip
- · Low Power Loss, High Efficiency
- · Ideally Suited for Automatic Assembly
- Surge Overload Rating to 110A Peak
- · Plastic Case Material has UL Flammability Classification Rating 94V-0

#### **Mechanical Data**

- · Case: Molded plastic SMA
- · Terminals: Plated leads solderable per MIL-STD-750, Method 2026 guaranteed
- · Making: Type Number
- · Polarity: Color band denotes cathode end · Mounting Position: Any





Dimensions in inches and (millimeters)

### **Maximum Ratings and Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	SS 52U	SS 53U	SS 54U	SS 545U	SS 55U	SS 56U	SS 58U	SS 510U	SS 515U	SS 520U	SS 525U	Unit
Maximum Recurrent Peak Reverse Voltage	VRRM	20	30	40	45	50	60	80	100	150	200	250	V
Maximum RMS Voltage	V <sub>RMS</sub>	14	21	28	31	35	42	56	70	105	140	175	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	20	30	40	45	50	60	80	100	150	200	250	V
Average Rectified Output Current @T <sub>L</sub> =100 °C	IF <sub>(AV)</sub>	5.0										Α	
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	lгsм	110									Α		
Rating for fusing (t<8.3ms)	l <sup>2</sup> t	50.22										A <sup>2</sup> s	
Forward Voltage @IF=5.0A	V <sub>FM</sub>	0.53 0.67				0.67	3.0	32	0	.90	0.92	V	
Peak Reverse Current @T <sub>A</sub> =25°C		0.1 0.05										A	
At Rated DC Blocking Voltage @TA=100°C	l <sub>R</sub>	10						5				mA	
Typical Junction Capacitance (Note 1)	CJ	200							110				pF
Typical Thermal Resistance (Note 2)	Reja Rejc Rejl	101 25 17									°C /W		
Operating Temperature Range	TJ	-55 to+150									$^{\circ}$		
Storage Temperature Range	Tstg	-55 to +150									$^{\circ}$		

### Note:

- 1. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C
- 2. Device mounted on FR-4 substrate, 1"\*1", 2oz, single-sided, PC boards with 0.1"\*0.15" copper pad.



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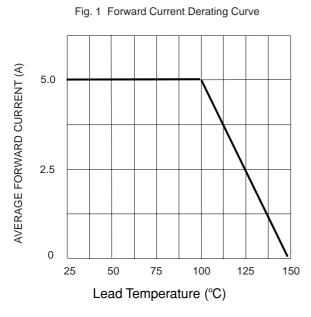
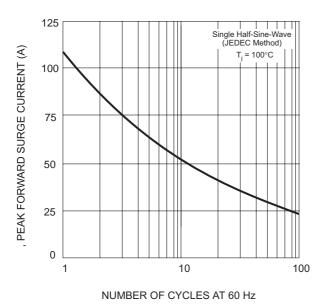


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current



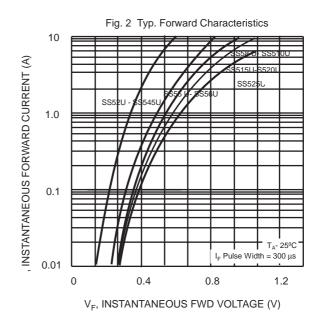
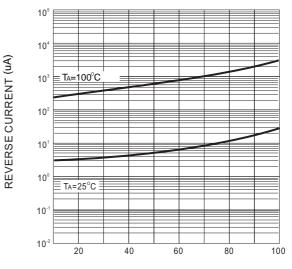


Fig.4 Typical Reverse Chracteristic



PERCENT OF RATED PEAK REVERSE VOLTAGE, %



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