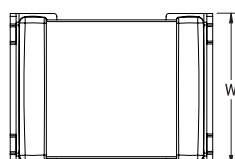
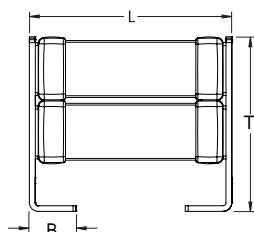


KPS SMD Comm X7R SnPb, Ceramic, 10 uF, 20%, 25 VDC, X7R, 1210-2

 TOP VIEW  
 Single or Double Chip Stack

 SIDE VIEW  
 Double Chip Stack



### Dimensions

<b>L</b>	3.5mm +/-0.3mm
<b>W</b>	2.6mm +/-0.3mm
<b>T</b>	6.15mm +/-0.15mm
<b>B</b>	0.8mm +/-0.15mm

### Packaging Specifications

<b>Weight:</b>	135 mg
<b>Packaging:</b>	T&R, 180mm, Plastic Tape
<b>Packaging Quantity:</b>	300

### General Information

<b>Series:</b>	KPS SMD Comm X7R SnPb
<b>Style:</b>	Stacked Chip
<b>Description:</b>	SMD, MLCC, Stacked, Double Chip, Temperature Stable
<b>Features:</b>	Temperature Stable
<b>RoHS:</b>	No
<b>Prop 65:</b>	 <b>WARNING:</b> Cancer and reproductive harm - <a href="http://www.p65warnings.ca.gov">www.p65warnings.ca.gov</a>
<b>REACH:</b>	SVHC (Pb – CAS 7439-92-1)
<b>SCIP Number:</b>	795b247e-78a9-405c-8728-223c2d9782a2
<b>Termination:</b>	Lead (SnPb)
<b>AEC-Q200:</b>	No
<b>Chip Size:</b>	1210-2
<b>Shelf Life:</b>	78 Weeks
<b>MSL:</b>	1

### Specifications

<b>Capacitance:</b>	10 uF
<b>Measurement Condition:</b>	1 kHz 1.0Vrms
<b>Capacitance Tolerance:</b>	20%
<b>Voltage DC:</b>	25 VDC
<b>Dielectric Withstanding Voltage:</b>	62.5 VDC
<b>Temperature Range:</b>	-55/+125°C
<b>Temperature Coefficient:</b>	X7R
<b>Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):</b>	15%, 1kHz 1.0Vrms
<b>Dissipation Factor:</b>	10% 1 kHz 1.0Vrms
<b>Aging Rate:</b>	3% Loss/Decade Hour: Referee Time is 1000 Hours
<b>Insulation Resistance:</b>	50 MOhms