

## General Description

The WST6401 is the highest performance trench P-ch MOSFET with extreme high cell density , which provide excellent R<sub>DS(on)</sub> and gate charge for most of the small power switching and load switch applications.

The WST6401 meet the RoHS and Green Product requirement with full function reliability approved.

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent C<sub>dv/dt</sub> effect decline
- Green Device Available

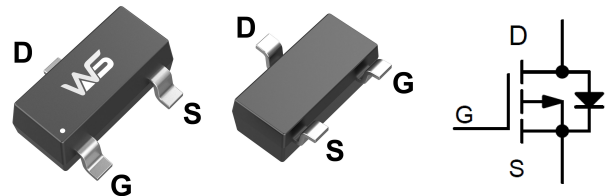
## Product Summary

BVDSS	R <sub>DS(on)</sub>	I <sub>D</sub>
-20V	135mΩ	-2.5A

## Applications

- High Frequency Point-of-Load Synchronous Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

## SOT-23N Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>GS</sub>	Gate-Source Voltage	±12	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup>	-2.5	A
I <sub>D</sub> @T <sub>c</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup>	-1.9	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	-10	A
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	0.7	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	---	178	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	80	°C/W

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	---	-0.016	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	---	135	165	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A	---	150	186	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-1.5A	---	250	355	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-0.5	-0.7	-1.2	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	3.97	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	-5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2A	---	.	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	13.1	---	Ω
Q <sub>g</sub>	Total Gate Charge (-4.5V)	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	---	3.0	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	0.5	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	0.8	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-15V, V <sub>GS</sub> =-4.5V, R <sub>G</sub> =3.0Ω I <sub>D</sub> =-2A	---	10	---	ns
T <sub>r</sub>	Rise Time		---	5.0	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	21	---	
T <sub>f</sub>	Fall Time		---	7	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	---	290	---	pF
C <sub>oss</sub>	Output Capacitance		---	60	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	34	---	

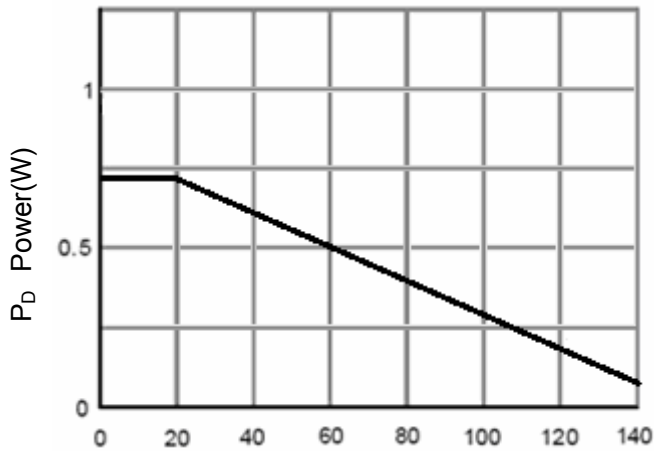
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-2.5	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,4</sup>		---	---	-10	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1.2	V

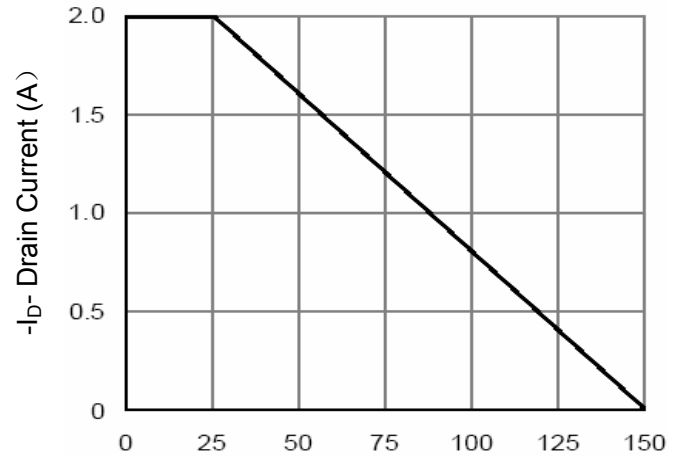
**Notes:**

- 1、Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2、Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3、Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- 4、Guaranteed by design, not subject to production

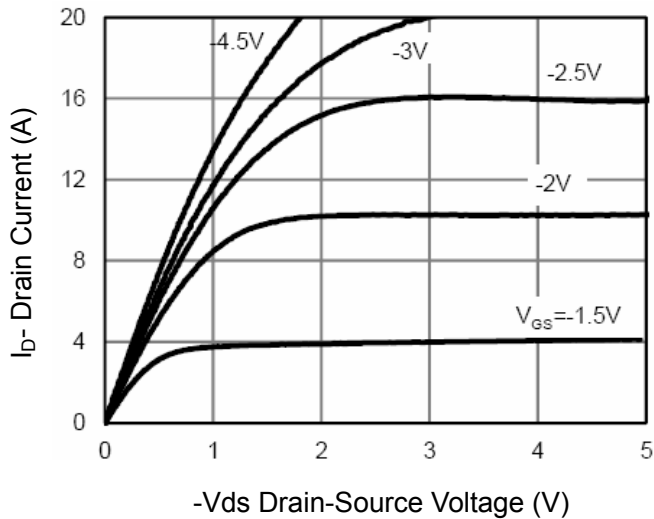
**Typical Characteristics**



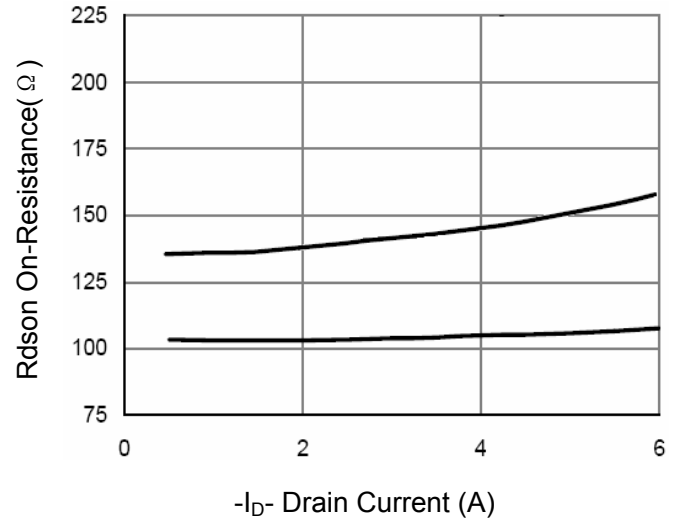
T<sub>J</sub>-Junction Temperature(°C)  
**Figure 1 Power Dissipation**



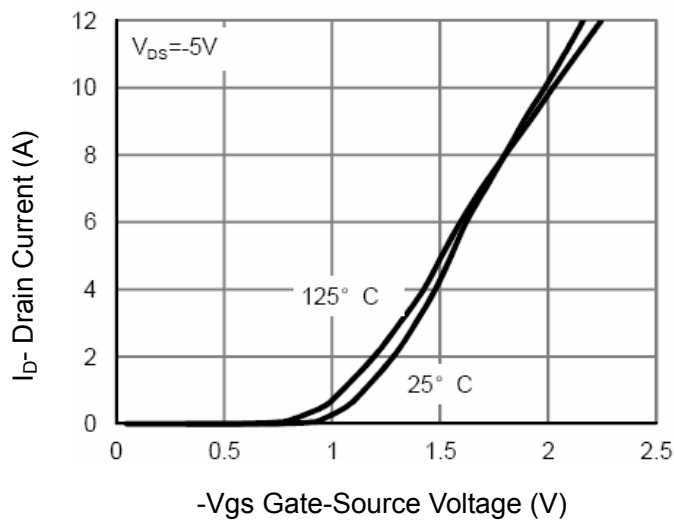
T<sub>J</sub>-Junction Temperature(°C)  
**Figure 2 Drain Current**



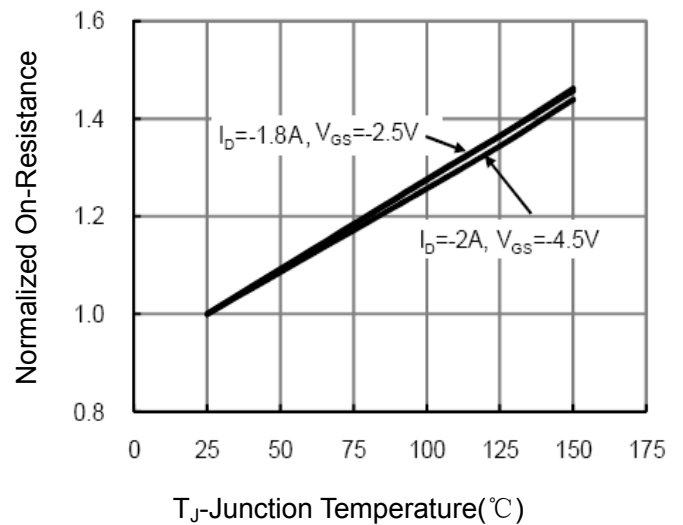
**Figure 3 Output Characteristics**



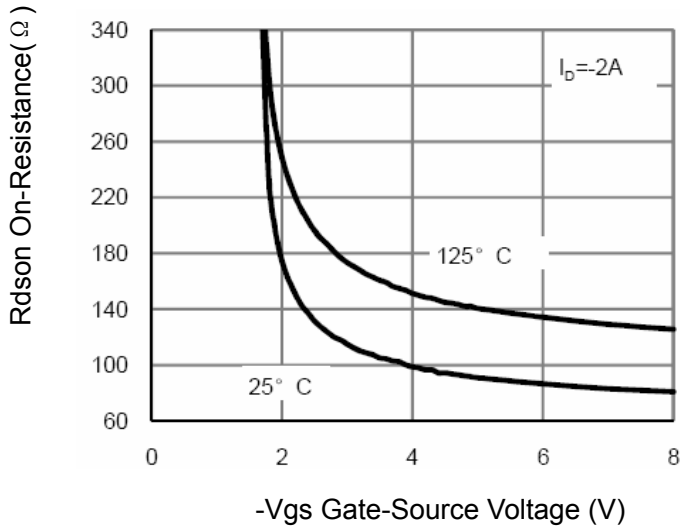
**Figure 4 Drain-Source On-Resistance**



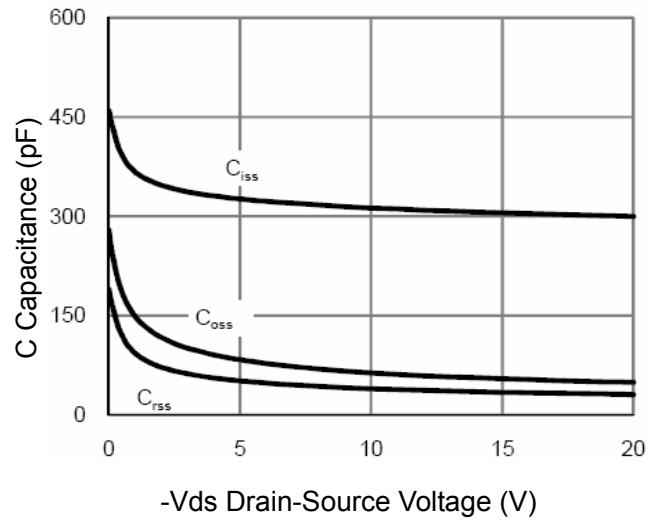
**Figure 5 Transfer Characteristics**



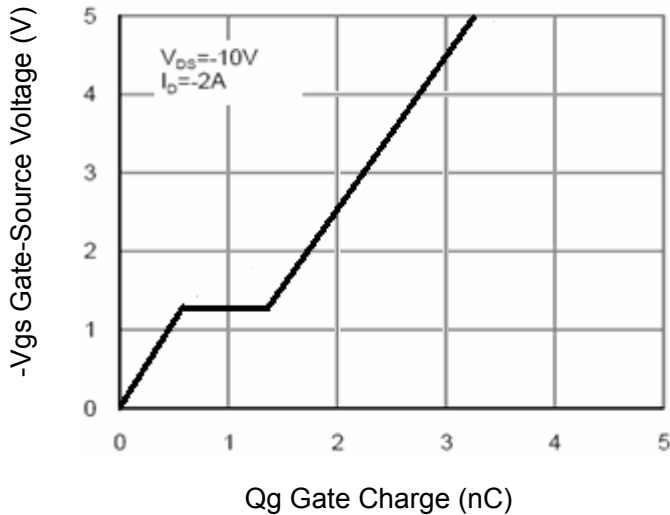
**Figure 6 Drain-Source On-Resistance**



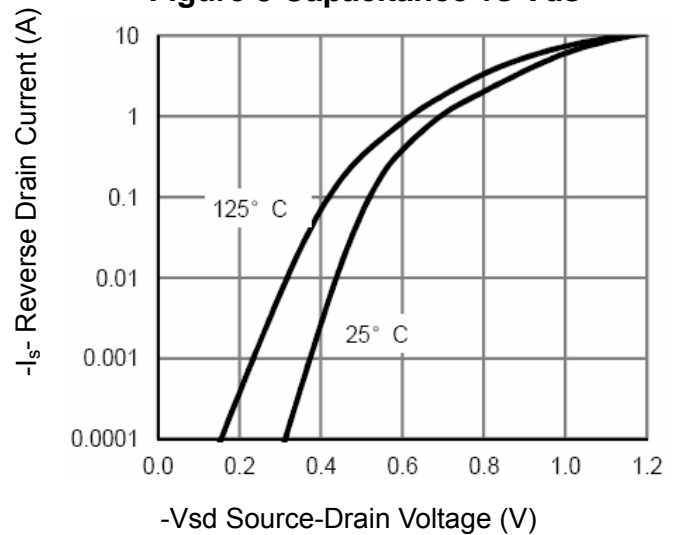
**Figure 7 Rdson vs Vgs**



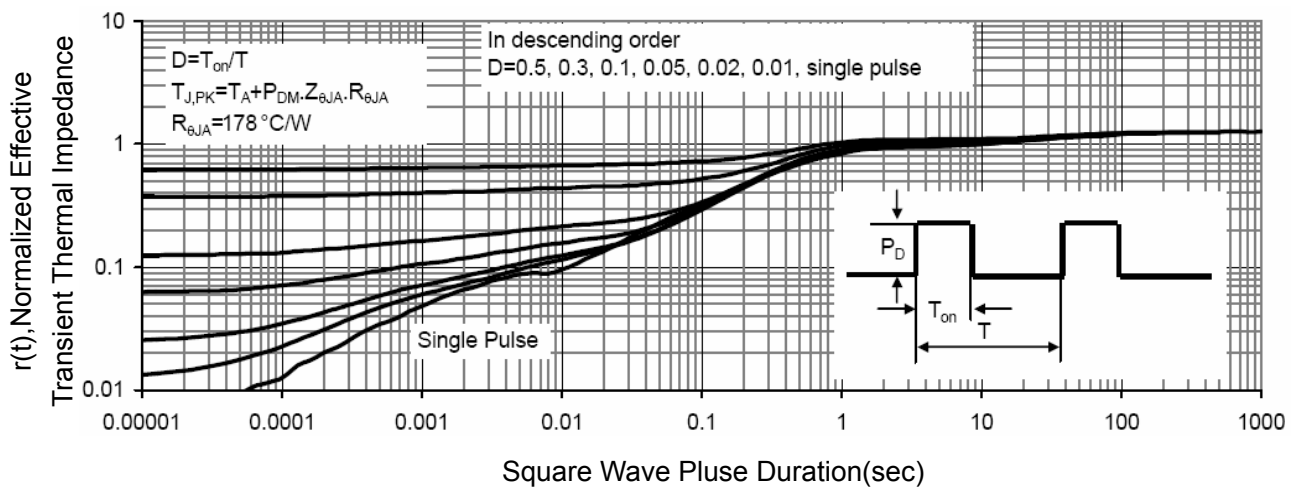
**Figure 8 Capacitance vs Vds**



**Figure 9 Gate Charge**



**Figure 10 Source- Drain Diode Forward**



**Figure 11 Normalized Maximum Transient Thermal Impedance**



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