

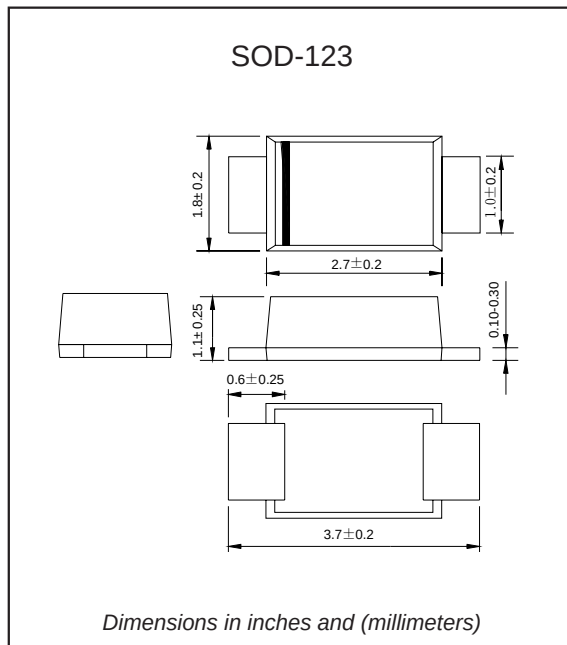
Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Low reverse leakage
- ◆ Built-in strain relief, ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed: 260°C/10 seconds at terminals
- ◆ Glass passivated chip junction

Mechanical data

- ◆ **Case:** JEDEC SOD-123 molded plastic body
- ◆ **Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- ◆ **Polarity:** Color band denotes cathode end
- ◆ **Mounting Position:** Any

Package outline



Maximum ratings and Electrical Characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOLS	DFR1A	DFR1B	DFR1D	DFR1G	DFR1J	DFR1K	DFR1M	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current 0.375”(9.5mm) lead length at $T_A=75^{\circ}\text{C}$	$I_{(AV)}$	1.0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	25.0							A
Maximum instantaneous forward voltage at 1.0A	V_F	1.3							V
Maximum DC reverse current $T_A=25^{\circ}\text{C}$ at rated DC blocking voltage $T_A=100^{\circ}\text{C}$	I_R	5.0 50.0							μA
Maximum reverse recovery time (NOTE 1)	t_{rr}	150				250	500		ns
Typical junction capacitance (NOTE 2)	C_J	15.0							pF
Typical thermal resistance (NOTE 3)	$R_{\theta JA}$	85.0							$^{\circ}\text{C/W}$
Operating junction and storage temperature range	T_J, T_{STG}	-55 to +150							$^{\circ}\text{C}$

Note: 1. Reverse recovery condition $I_F=0.5\text{A}$, $I_R=1.0\text{A}$, $I_{rr}=0.25\text{A}$

2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

3. Thermal resistance from junction to ambient at 0.375"(9.5mm) lead length, P.C.B. mounted

Rating and characteristic curves

FIG.1-TYPICAL FORWARD
CHARACTERISTICS

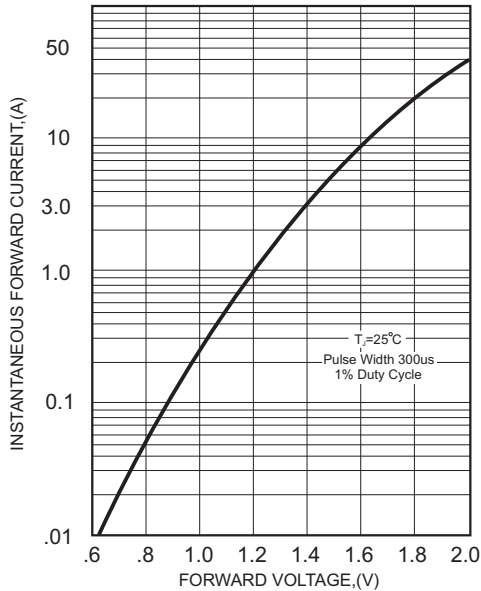


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

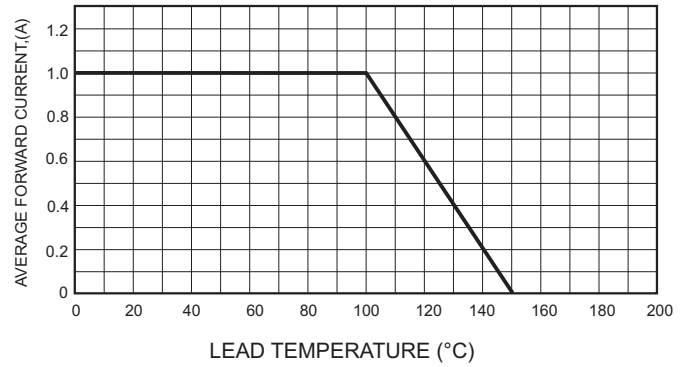


FIG.4-MAXIMUM NON-REPETITIVE FORWARD
SURGE CURRENT

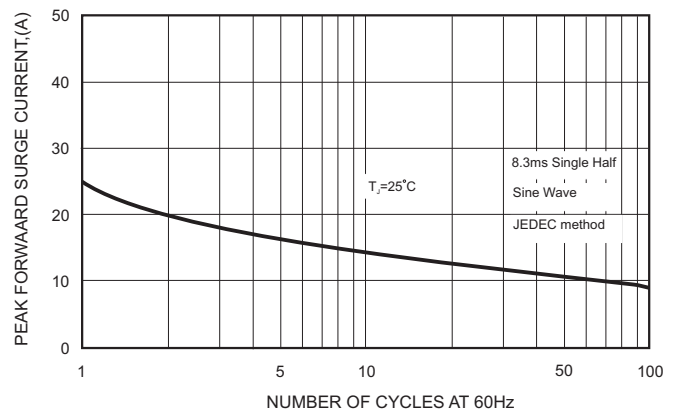
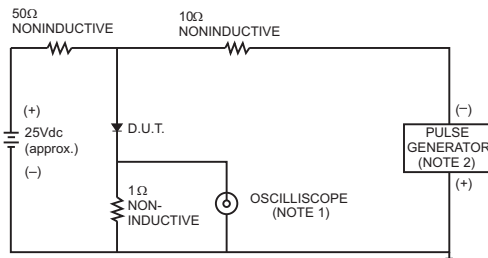


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE
RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm, 22pF.
2. Rise Time= 10ns max., Source Impedance= 50 ohms.

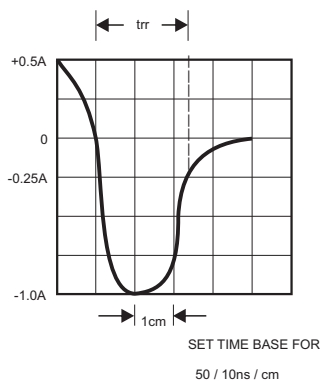
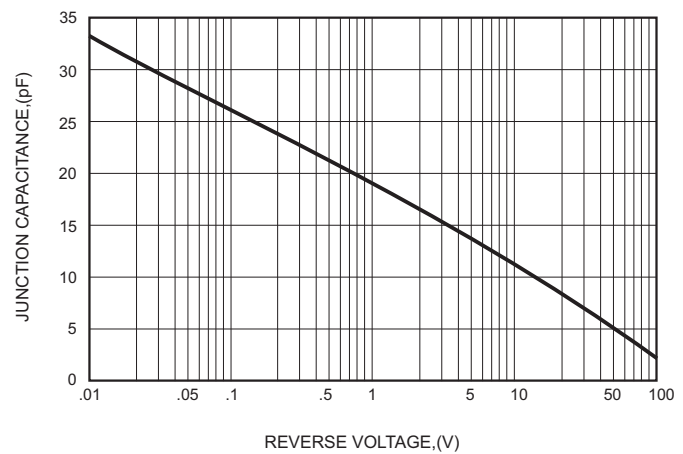


FIG.5-TYPICAL JUNCTION CAPACITANCE



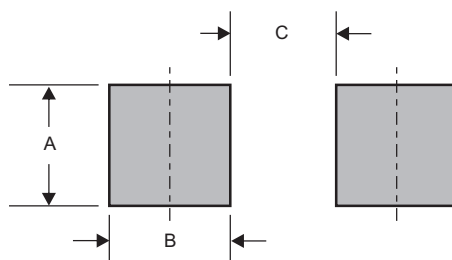
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
DFR1A	F1
DFR1B	F2
DFR1D	F3
DFR1G	F4
DFR1J	F5
DFR1K	F6
DFR1M	F7

Suggested solder pad layout

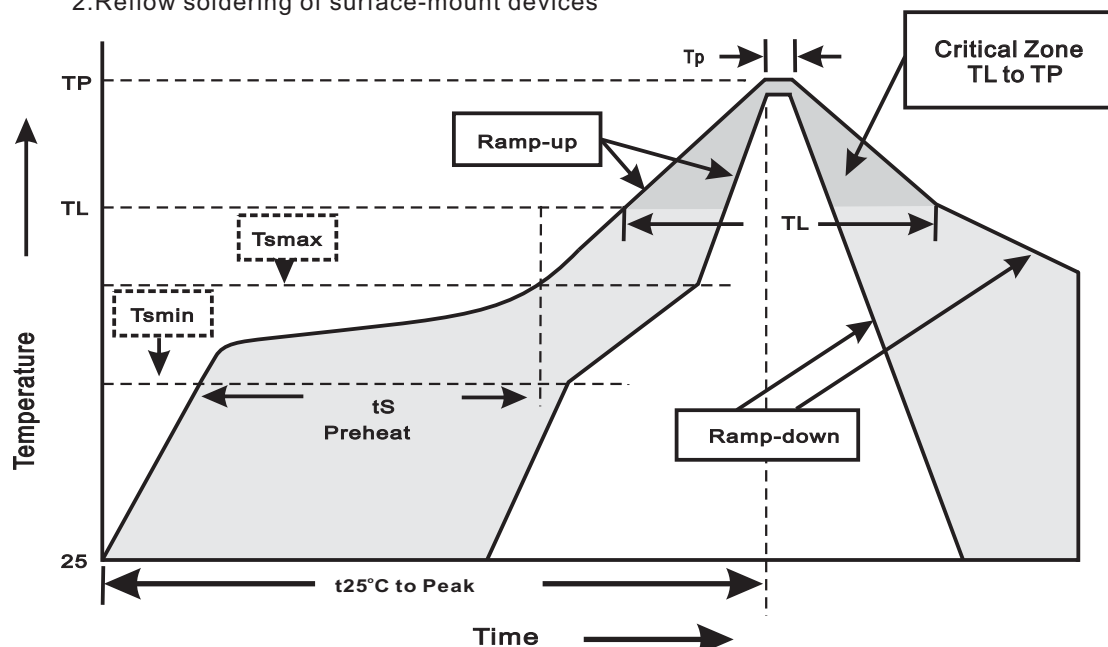


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-123	0.075 (1.90)	0.055 (1.40)	0.075 (1.90)

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes