1Mbit/s High Speed Transistor Photo Coupler

Description

The SLM501series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon high speed photo transistor in a plastic SOP5 package.

With the robust coplanar double mold structure, SLM501 series provide the most stable isolation feature.

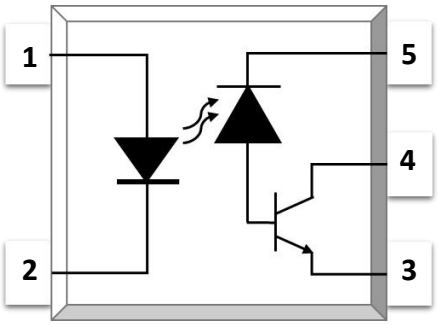
Features

- High isolation 3750 VRMS
- DC input with high speed transistor
- Operating temperature range - 40 °C to 100 °C
- REACH compliance
- Halogen free
- MSL class 1
- Regulatory Approvals
  - UL - UL1577
  - VDE - EN60747-5-5(VDE0884-5)
  - CQC - GB4943.1, GB8898
  - cUL- CSA Component Acceptance Service Notice No. 5A

Applications

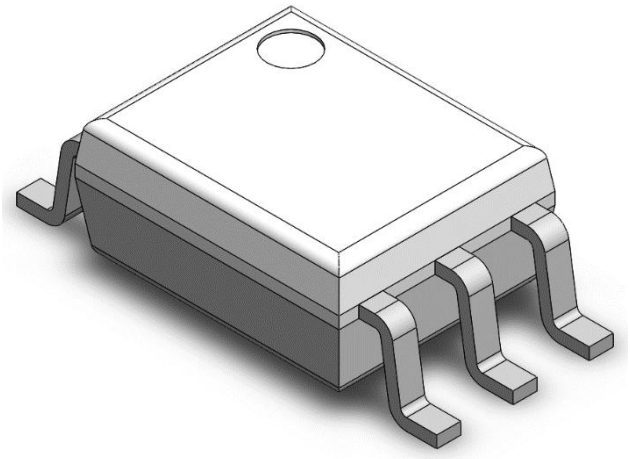
- Line receivers
- Telecommunication equipment
- Out interface to CMOS-LSTTL-TTL
- Wide bandwidth analog coupling
- Pulse transformer replacement
- Computer-peripheral interface

SCHEMATIC



PIN DEFINITION

1.Anode	5.VCC
	4.VO
2.Cathode	3.GND



ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	VALUE	UNIT	Note
INPUT				
Forward Current	I_F	25	mA	
Peak Forward Current	I_{FP}	50	mA	1
Peak Transient Current	$I_{F(trans)}$	1	A	2
Reverse Voltage	V_R	5	V	
Input Power Dissipation	P_I	100	mW	
OUTPUT				
Supply Voltage	V_{CC}	-0.5~30	V	
Output Voltage	V_O	-0.5~20	V	
Output Current	I_O	50	mA	
Output Power Dissipation	P_O	100	mW	
COMMON				
Total Power Dissipation	Ptot	200	mW	
Isolation Voltage	Viso	3750	Vrms	3
Operating Temperature	Topr	-40~100	°C	
Storage Temperature	Tstg	-55~125	°C	
Soldering Temperature	Tsol	260	°C	4

Note 1. 50% duty, 1ms P.W

Note 2. ≤1μs P.W,300pps

Note 3. AC For 1 Minute, R.H. = 40 ~ 60%

Note 4. For 10 seconds

ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25°C							
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
INPUT							
Forward Voltage	V_F	-	1.45	1.8	V	I_F=16mA	
Reverse Current	I_R	-	-	10	μA	V_R=5V	
Input Capacitance	Cin	-	60	-	pF	V=0, f=1MHz	
OUTPUT							
High Level Supply Current	I_{CCH}	-	0.01	1	μA	I_F=0mA, V_O=Open, V_{CC}=15V, Ta=25°C	
		-	-	2	μA	I_F=0mA, V_O=Open, V_{CC}=15V	
Low Level Supply Current	I_{CCL}	-	200	-	μA	I_F=16mA, V_O=Open, V_{CC}=15V	
Logic High Output Current	I_{OH}	-	0.001	0.5	μA	I_F=0mA, V_O=V_{CC}=5.5V, Ta=25°C	
		-	0.01	1	μA	I_F=0mA, V_O=V_{CC}=15V, Ta=25°C	
		-	-	50	μA	I_F=0mA, V_O=V_{CC}=15V	
TRANSFER CHARACTERISTICS(at Ta=0 to 70°C , unless specified otherwise)							
Current Transfer Ratio	CTR	7	-	50	%	I_F = 16mA ,V_O = 0.4V, V_{CC}=4.5V, Ta=25°C	
Logic Low Output Voltage	V_{OL}	-	0.18	0.4	μA	I_F = 16mA ,I_O = 1.1mA, V_{CC}=4.5V, Ta=25°C	
Isolation Resistance	Riso	10^12	10^14	-	Ω	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	C_{IO}	-	0.3	-	pF	V=0, f=1MHz	

ELECTRICAL OPTICAL CHARACTERISTICS							
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS(at Ta=0 to 70°C, I_F=16mA ,V_{CC}=5V, unless specified otherwise)							
Propagation Delay Time to Logic Low	TPHL	-	0.4	0.8	µs	R_L=1.9kΩ,T_A=25°C	Fig.13
		-	-	1.0		R_L=1.9kΩ	
Propagation Delay Time to Logic High	TPLH	-	0.35	0.8	µs	R_L=1.9kΩ,T_A=25°C	Fig.13
		-	-	1.0		R_L=1.9kΩ	
Common Mode Transient Immunity at Logic High	CM_H	15	-	-	kV/µs	I_F = 0mA , V_{CM}=1500Vpp, RL=1.9kΩ, T_A =25°C	Fig.15
Common Mode Transient Immunity at Logic Low	CM_L	15	-	-	kV/µs	I_F = 16mA , VCM=1500Vpp, R_L=1.9kΩ, T_A =25°C	Fig.15

CHARACTERISTIC CURVES

Fig.1 Forward Current vs. Forward Voltage

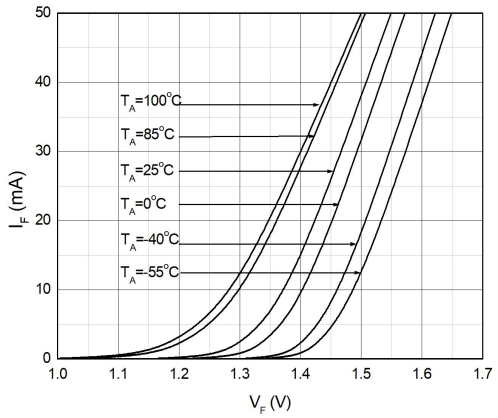


Fig.2 Forward Voltage vs. Ambient Temperature

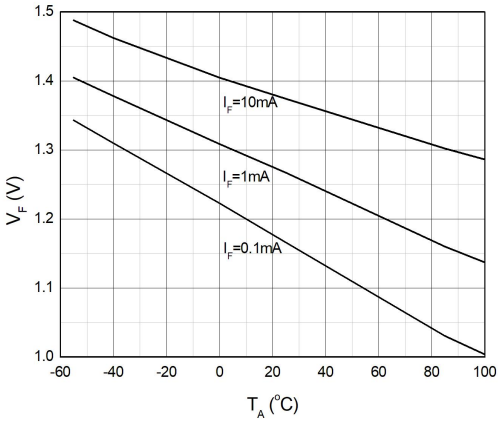


Fig.3 Input Threshold Current vs. Ambient Temperature

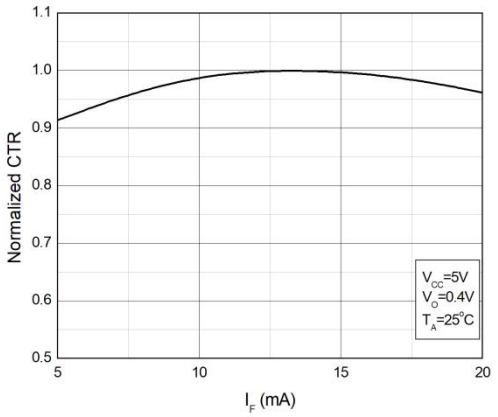


Fig.4 Input Threshold Current vs. Ambient Temperature

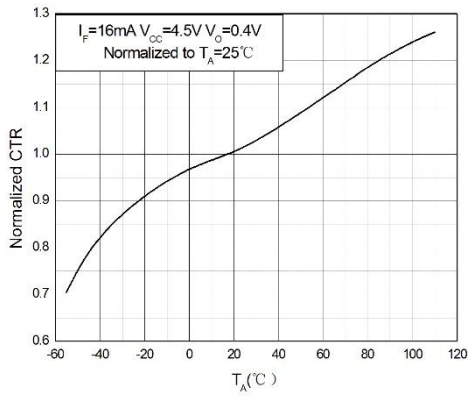


Fig.5 Low Level Output Current vs. Ambient Temperature

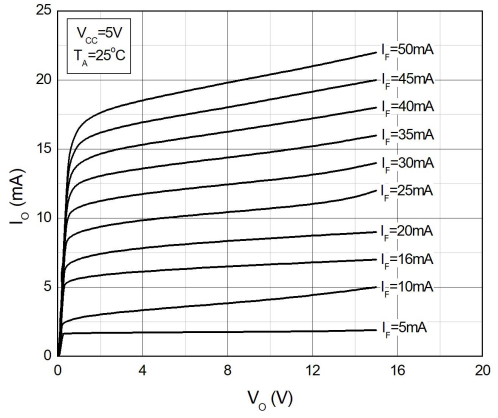
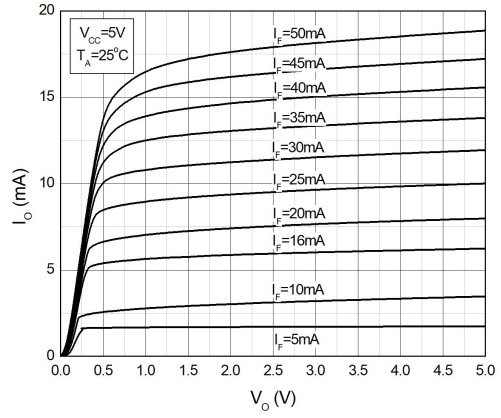


Fig.6 Low Level Output Current vs. Ambient Temperature



CHARACTERISTIC CURVES

Fig.7 Low Level Output Voltage vs. Ambient Temperature

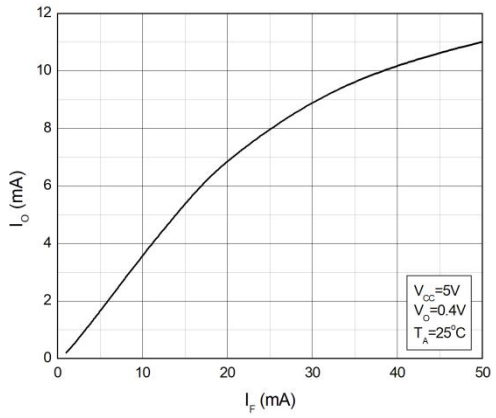


Fig.8 Low Level Output Voltage vs. Ambient Temperature

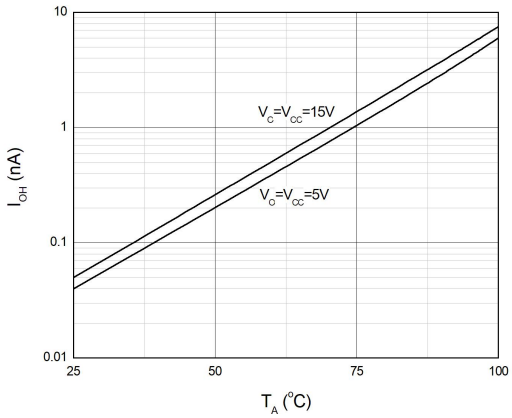


Fig.9 High Level Output Current vs. Ambient Temperature

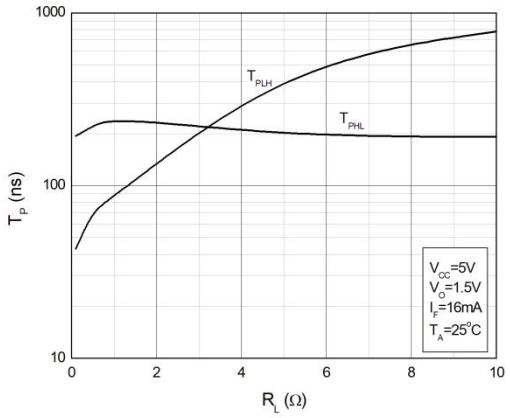


Fig.10 High Level Output Current vs. Ambient Temperature

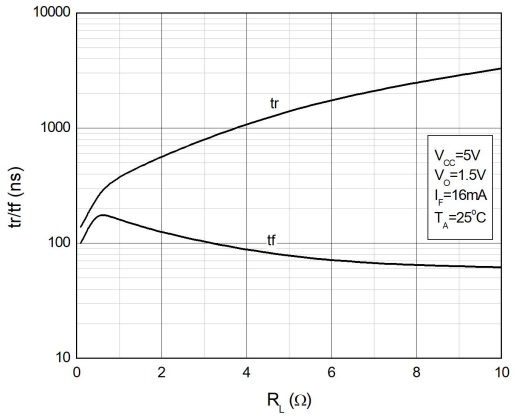


Fig.11 Output Voltage vs. Forward Current

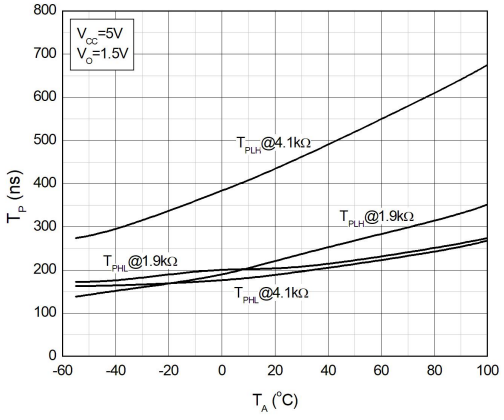
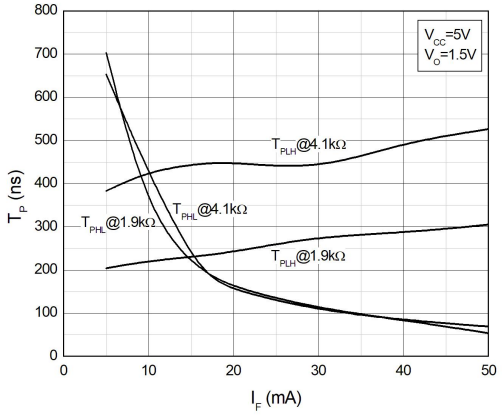


Fig.12 Output Voltage vs. Forward Current



TEST CIRCUITS

Fig.13 Test Circuits for TPHL, TPLH, tr, tf

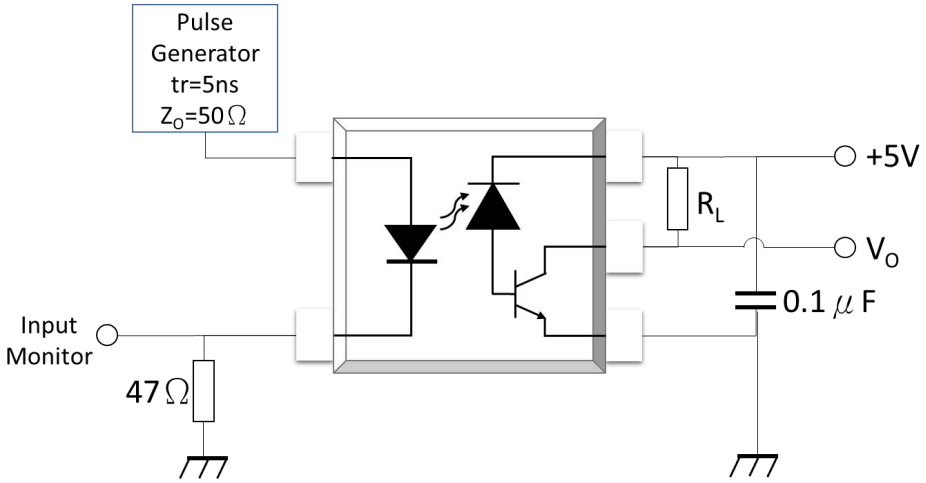
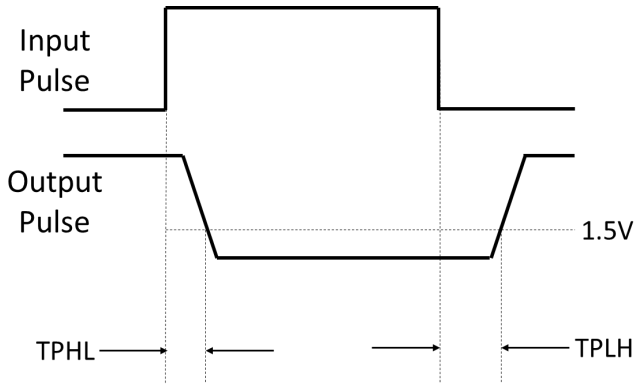
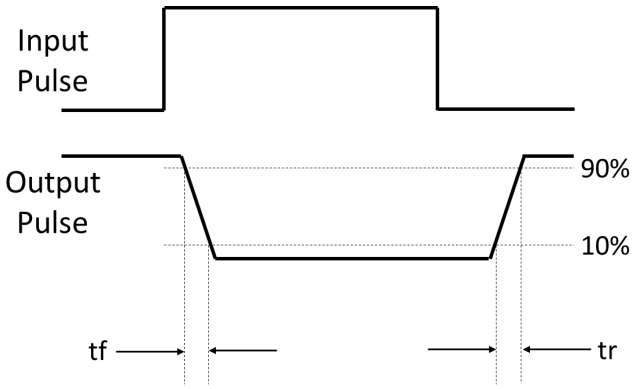


Fig.14 Waveforms of TPHL, TPLH, tr, tf





TEST CIRCUITS

Fig.15 Test Circuits for Common Mode Transient Immunity

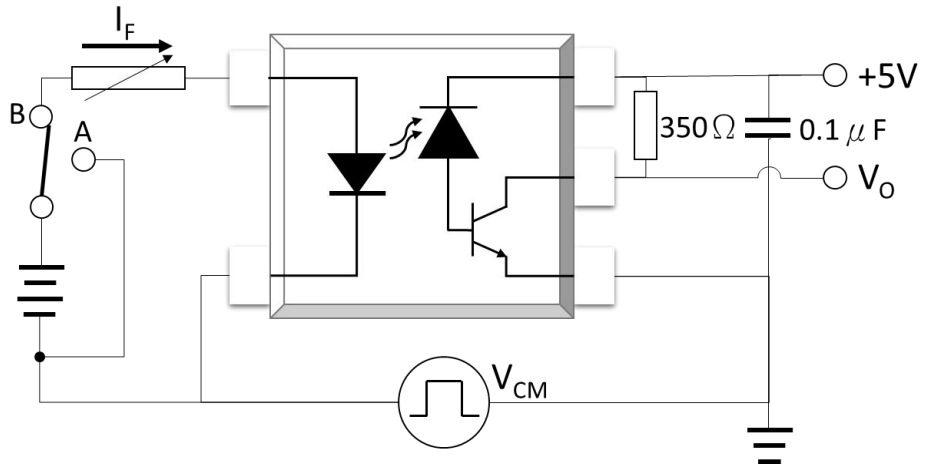
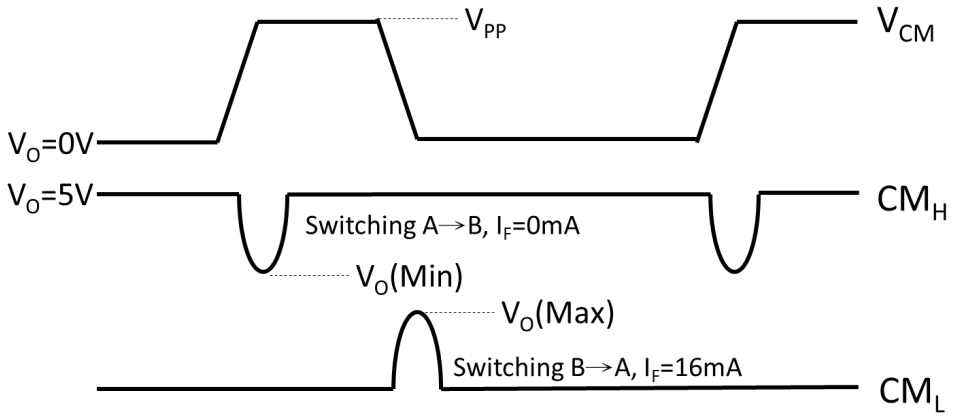
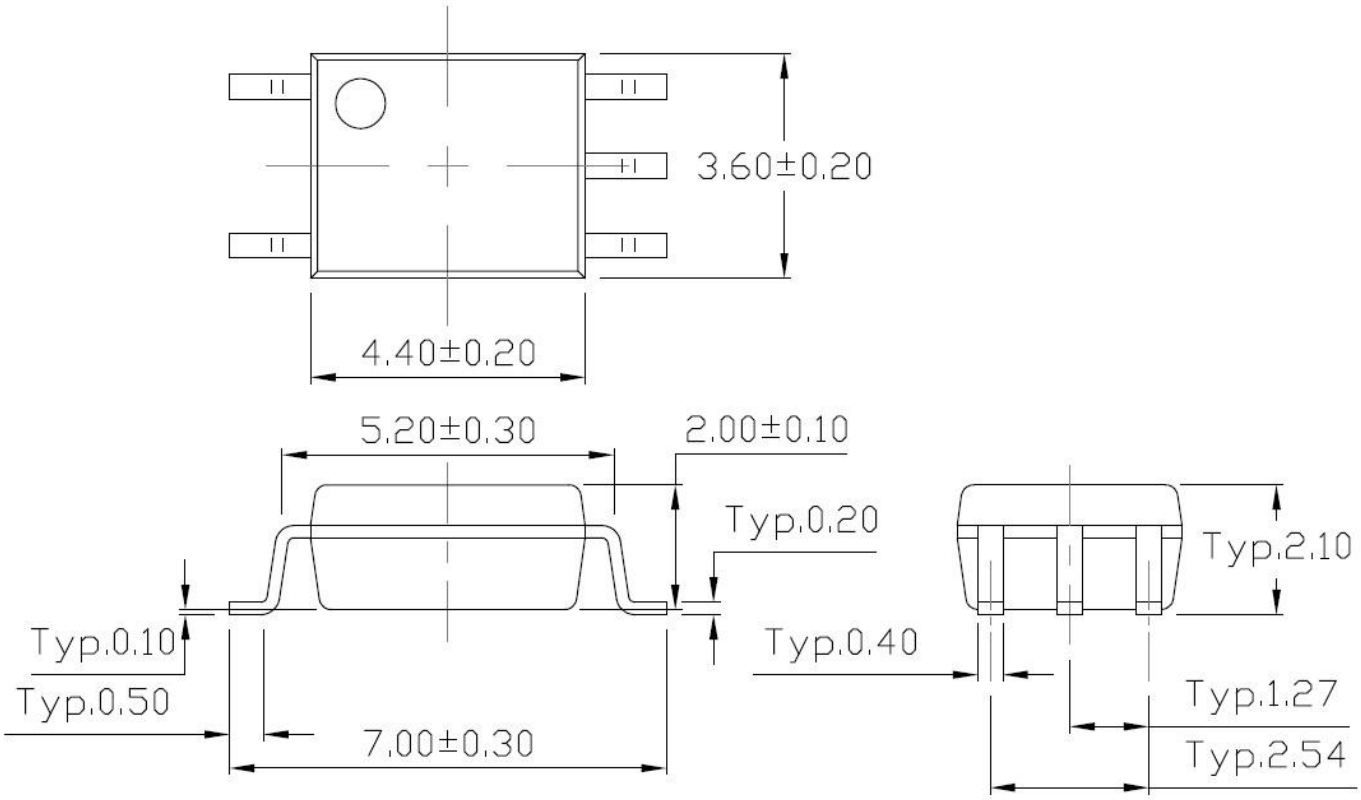


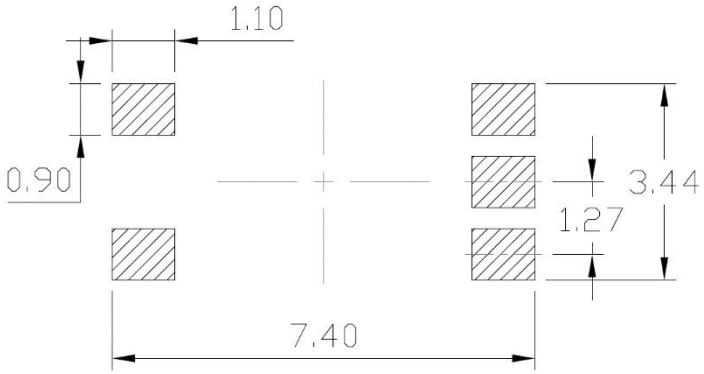
Fig.16 Waveforms of Common Mode Transient Immunity



PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

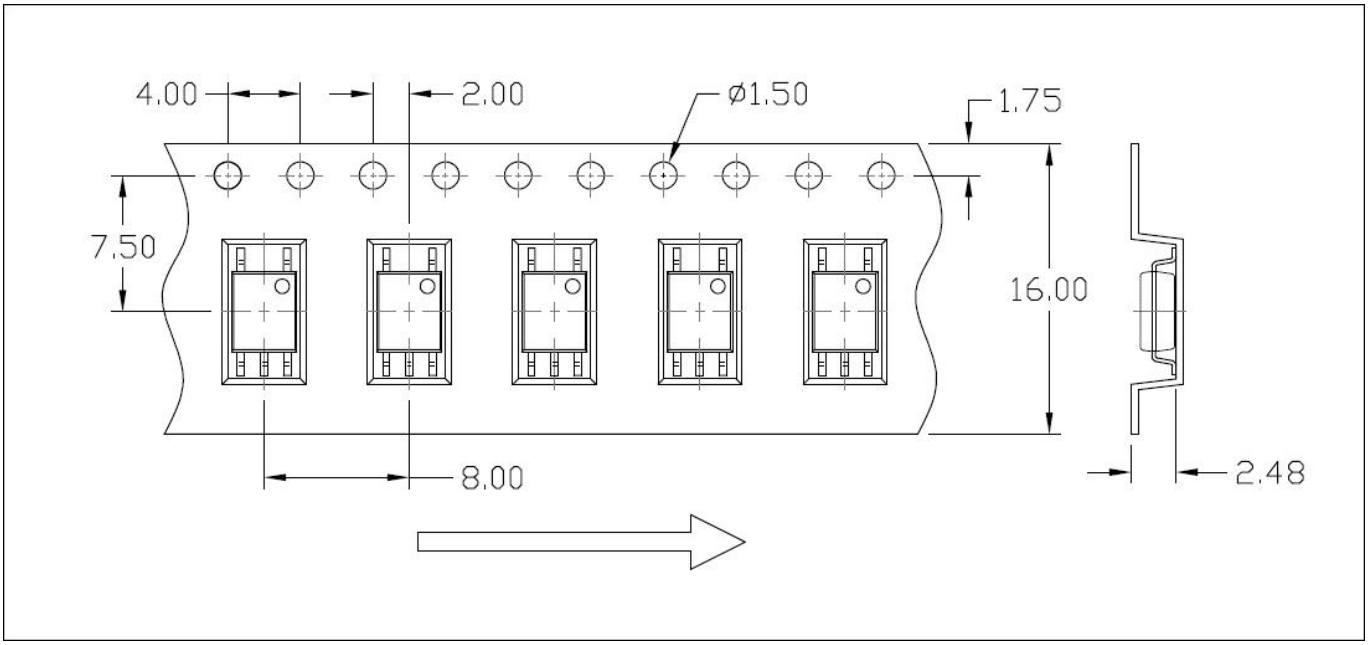


Recommended Solder Mask (Dimensions in mm unless otherwise stated)

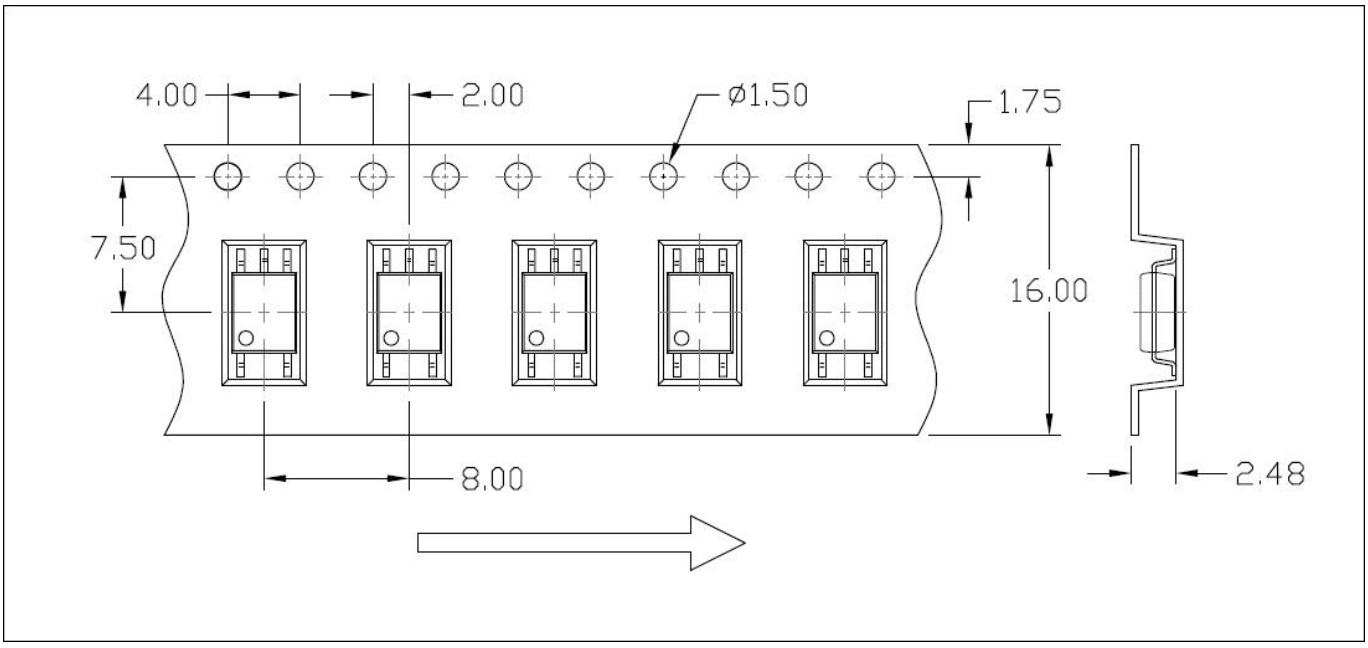


CARRIER TAPE SPECIFICATIONS (Dimensions in mm unless otherwise stated)

Option T1

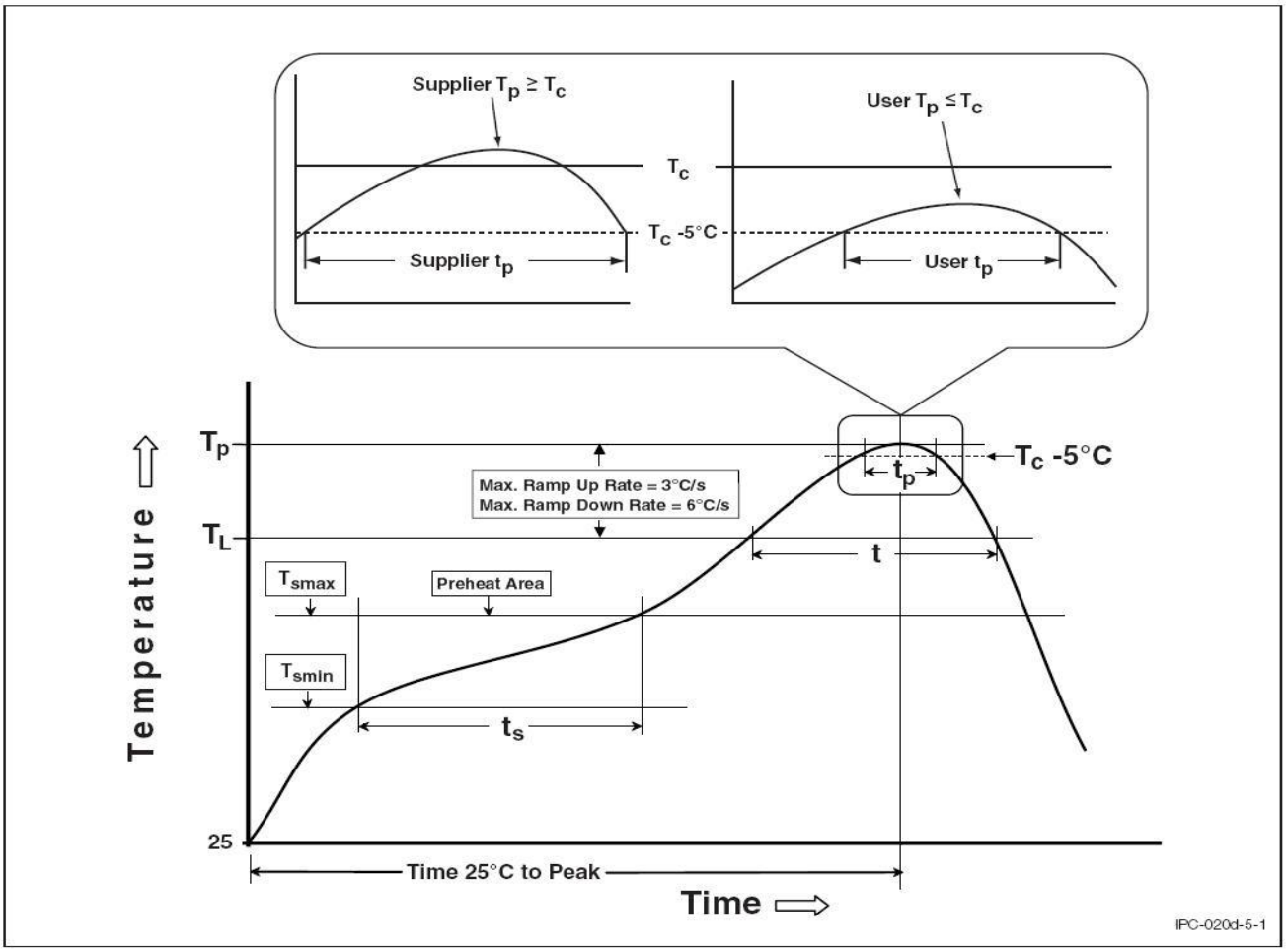


Option T2



REFLOW INFORMATION

REFLOW PROFILE



Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	100	150°C
Temperature Max. (Tsmax)	150	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.
Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.