

# FH8712A1

## N- Channel Enhancement Mode Power MOSFET

### Description

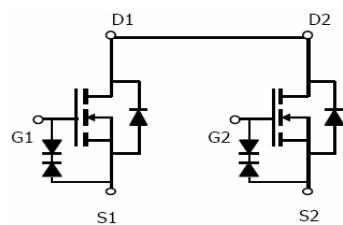
The FH8712A1 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications. It is ESD protected.

### Application

- PWM application
- Load switch

### General Features

- $V_{DS} = 20V, I_D = 7.8A$
- $R_{DS(ON)} < 14.5m\Omega$  (MAX) @  $V_{GS}=4.5V$
- $R_{DS(ON)} < 15m\Omega$  (MAX) @  $V_{GS}=3.8V$
- $R_{DS(ON)} < 18.5m\Omega$  (MAX) @  $V_{GS}=2.5V$
- ESD Rating: 2000V HBM
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package



Schematic diagram



Marking and pin Assignment



TSSOP-8 top view

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	7.8	A
Drain Current-Pulsed (Note 1)	$I_{DM}$	31	A
Maximum Power Dissipation	$P_D$	1.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

### Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	86	°C/W
---	-----------------	----	------

**Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	20		-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=16V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	$\pm 10$	$\mu A$
Gate-Body Leakage Current 1	$I_{GSS1}$	$V_{GS}=\pm 8V, V_{DS}=0V$	-	-	$\pm 1$	$\mu A$
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.40	0.7	1.00	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=5.0A$	-	10	14.5	$m\Omega$
		$V_{GS}=3.8V, I_D=5.0A$	-	11	15	$m\Omega$
		$V_{GS}=2.5V, I_D=4A$	-	14	18.5	$m\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=7A$	-	20	-	S
<b>Dynamic Characteristics (Note 4)</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=10V, V_{GS}=0V, F=1.0MHz$	-	1150	-	PF
Output Capacitance	$C_{oss}$		-	185	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	145	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=10V, R_L=1.35\Omega$ $V_{GS}=5V, R_{GEN}=3\Omega$	-	6	-	nS
Turn-on Rise Time	$t_r$		-	13	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	52	-	nS
Turn-Off Fall Time	$t_f$		-	16	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=10V, I_D=7A, V_{GS}=4.5V$	-	15	-	nC
Gate-Source Charge	$Q_{gs}$		-	0.8	-	nC
Gate-Drain Charge	$Q_{gd}$		-	3.2	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=1A$	-	-	1.2	V
Diode Forward Current (Note 2)	$I_S$		-	-	7	A

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

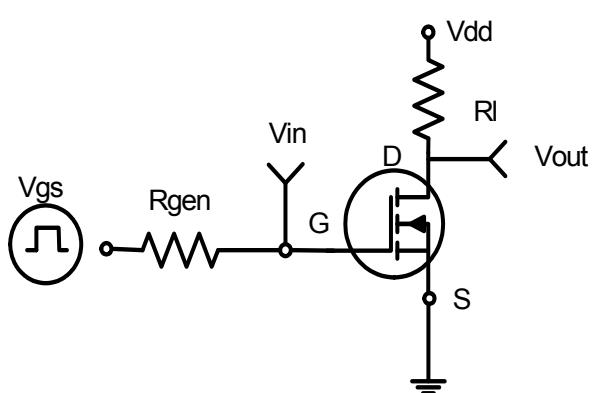


Figure 1:Switching Test Circuit

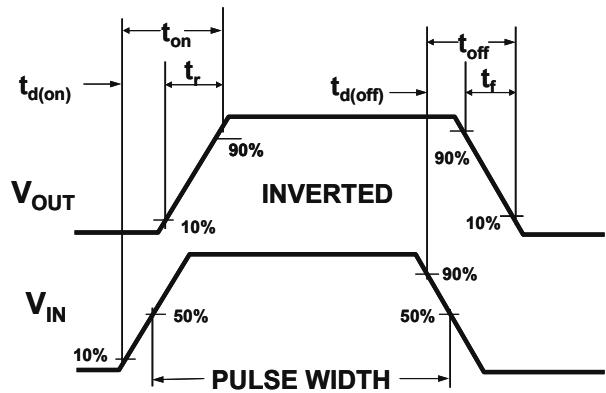


Figure 2:Switching Waveforms

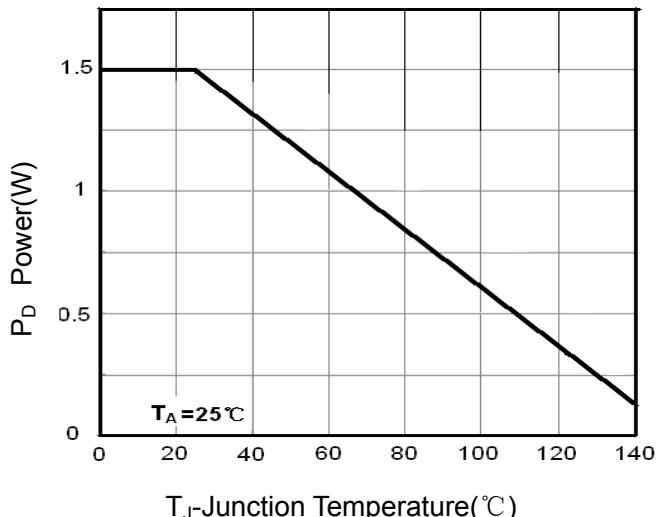


Figure 3 Power Dissipation

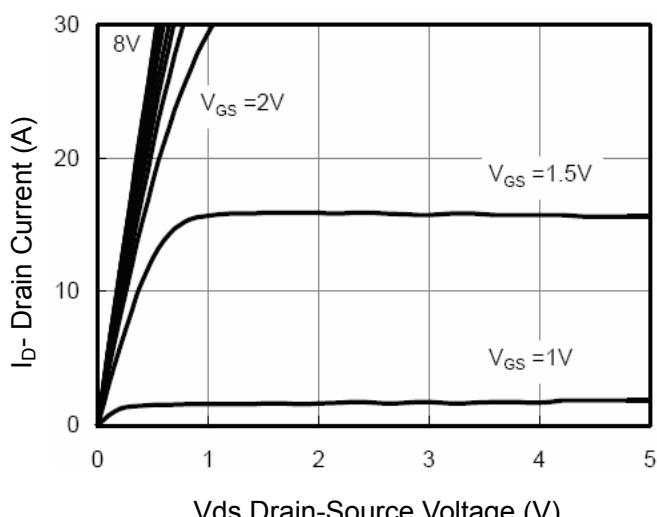


Figure 5 Output CHARACTERISTICS

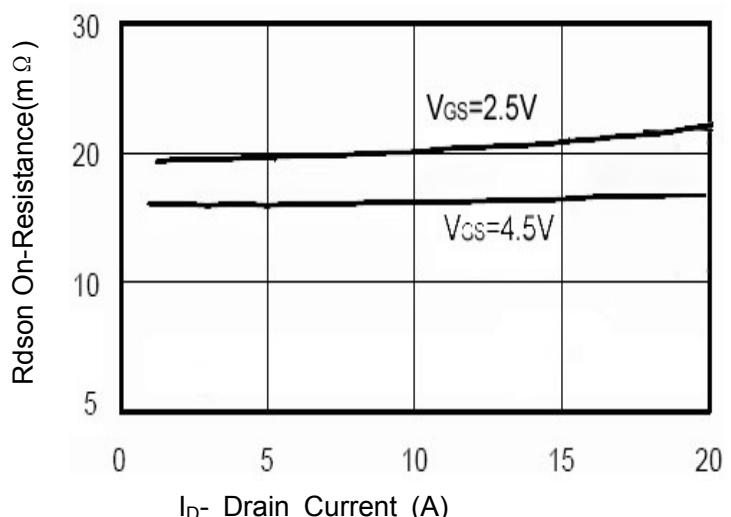


Figure 6 Drain-Source On-Resistance

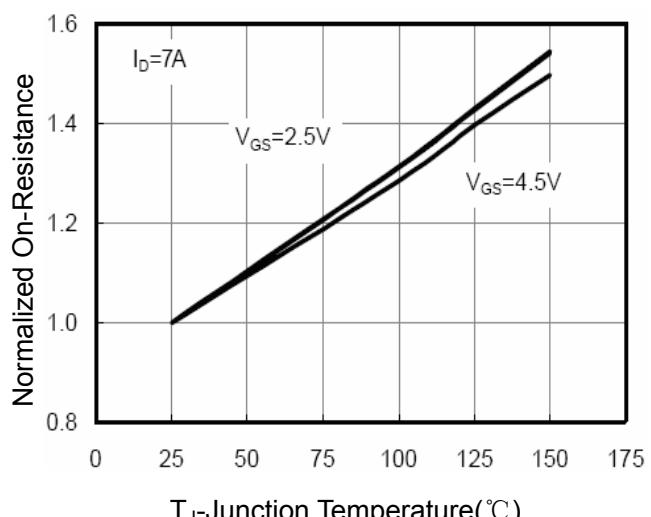
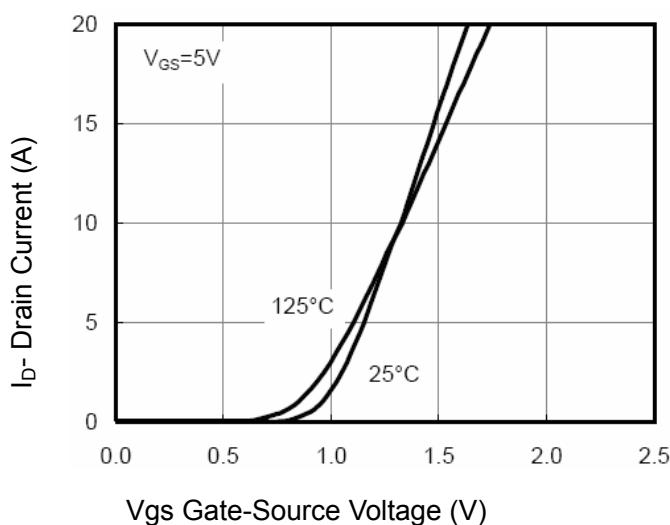
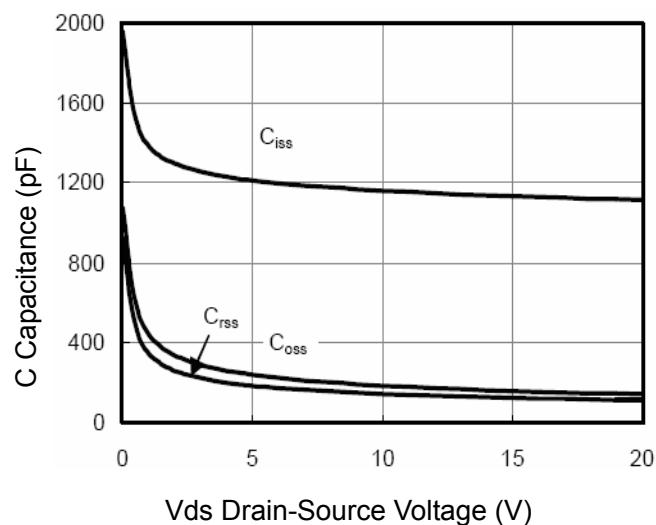


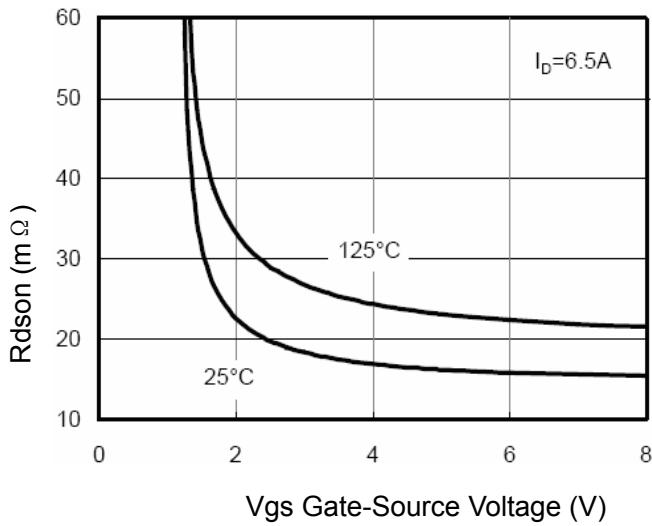
Figure 8 Drain-Source On-Resistance



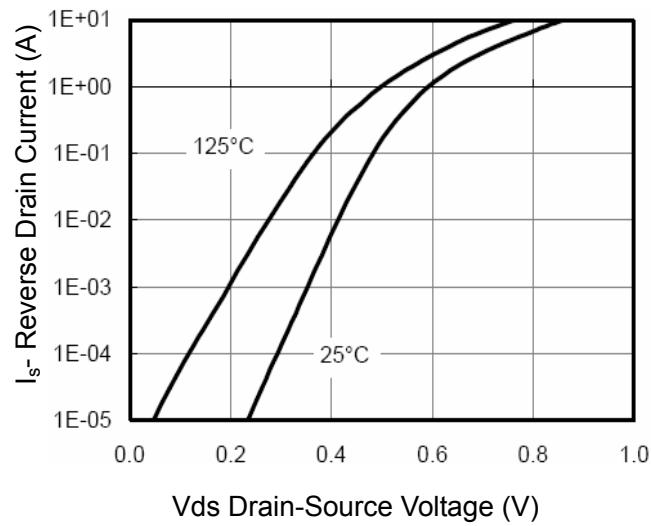
**Figure 7 Transfer Characteristics**



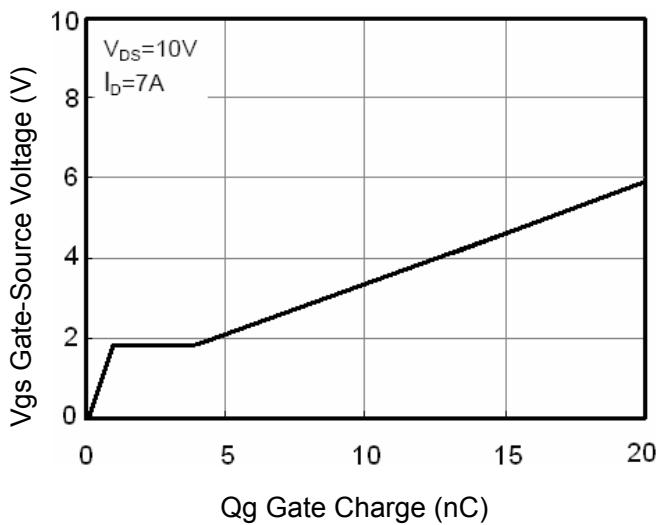
**Figure 8 Capacitance vs Vds**



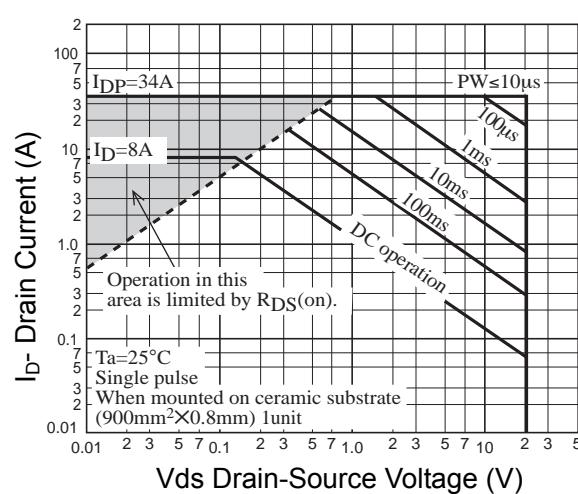
**Figure 9  $R_{DS(on)}$  vs  $V_{GS}$**



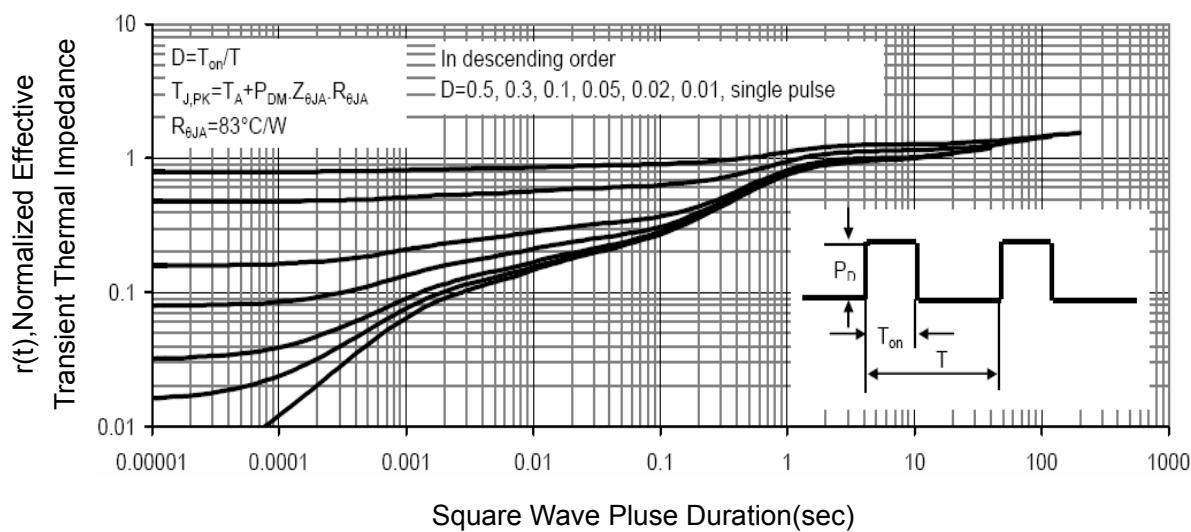
**Figure 10 Capacitance vs  $V_{DS}$**



**Figure 11 Gate Charge**



**Figure 13 Safe Operation Area**



**Figure 14 Normalized Maximum Transient Thermal Impedance**

MARKING DESCRIPTION

TSSOP-8



**NOTE:**

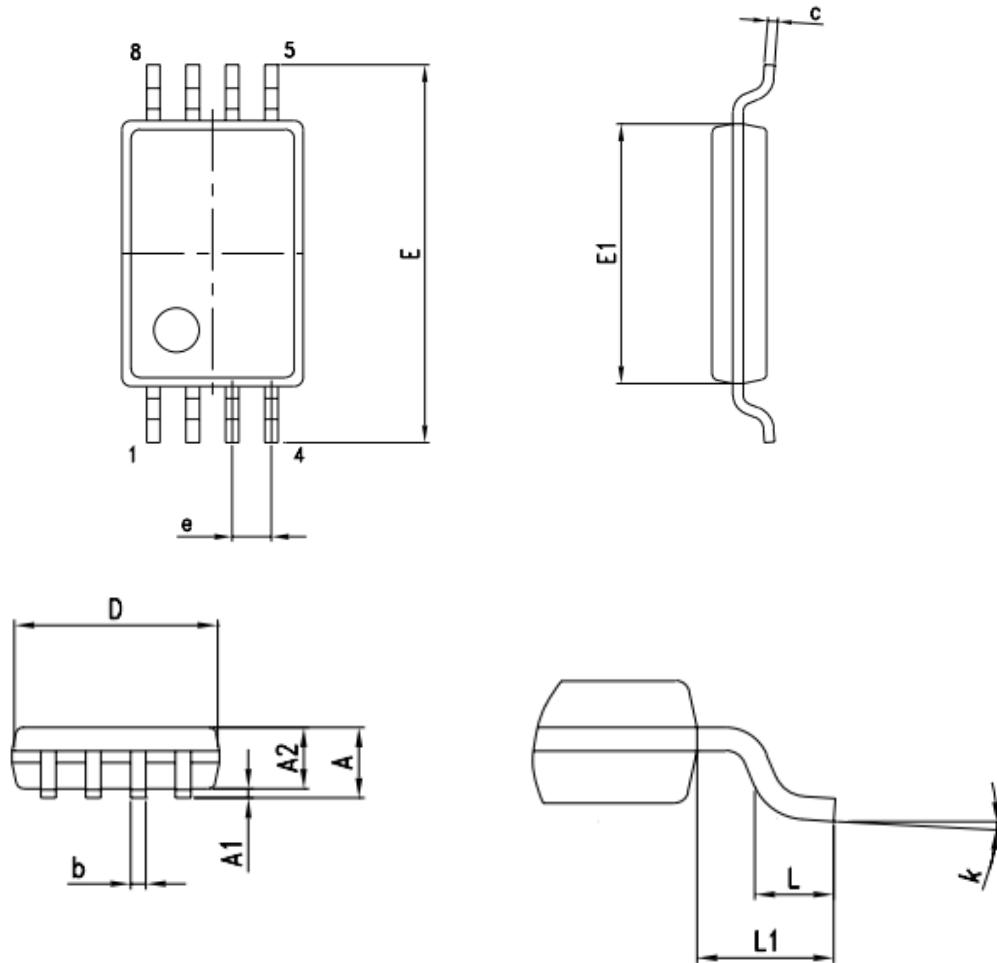
AAAA : Internal Code

BB : Year Code

CC : Week Code

DD : Serial Code

## Package Outline Dimensions : TSSOP-8



DIM.	mm.			inch.		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	1.05		1.20	0.041		0.047
A1	0.05		0.15	0.002		0.006
A2	0.80		1.05	0.032		0.041
b	0.19		0.30	0.008		0.012
c	0.090		0.20	0.003		0.007
D	2.90		3.10	0.114		0.122
E	6.20		6.60	0.240		0.260
E1	4.30		4.50	0.170		0.177
e		0.65			0.025	
L	0.45		0.75	0.018		0.030
L1		1.00			0.039	
k	0°		8°	0.192		0.208