

FH3117

P-Channel Enhancement Mode MOSFET

Description

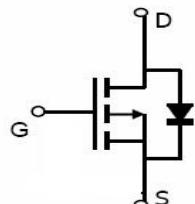
The FH3117 is the P-Channel enhancement mode MOSFET in a plastic package (SOT-23-3L) using the Trench technology.

Applications

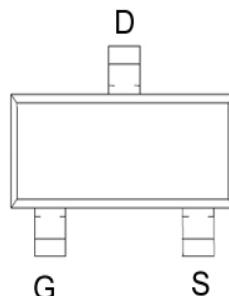
- ◆ High Speed Switch
- ◆ DC-DC Converters
- ◆ Lithium-Ion Battery

Features

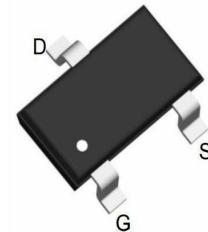
- ◆ $V_{DS} = -30V$; $I_D = -6.2A$
- ◆ $R_{DS(ON)}(\text{Typ.}) = 19\text{ m}\Omega$ @ $V_{GS} = -10V$
- ◆ $R_{DS(ON)}(\text{Typ.}) = 23\text{ m}\Omega$ @ $V_{GS} = -4.5V$
- ◆ LogicLevelCompatible
- ◆ SMD Package(SOT-23-3L)
- ◆ TrenchTechnology
- ◆ FastSwitching



Schematic diagram



Marking and Pin Assignment



SOT-23-3L top view

■ Absolute Maximum Ratings ($T_A = 25^\circ C$, unless otherwise specified)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_J = 150^\circ C$)	I_D	-6.2	A
Pulsed Drain Current (Note 3)	I_{DM}	-24.8	A
Power Dissipation	P_D	1.25	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$
Thermal Resistance-Junction to Ambient (Note 1)	R_{thJA}	100	$^\circ C/W$

■ Electrical Characteristics ($T_A = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = -250\mu\text{A}$	-30	-34	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = -250\mu\text{A}$	-1.1	-1.5	-1.9	V
Gate-Body Leakage Current	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = -30\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	-1	μA
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_{\text{D}} = -4.0\text{A}$	-	19	22.5	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -4.0\text{A}$	-	23	29	
Forward Transconductance	g_{FS}	$V_{\text{DS}} = -5\text{V}, I_{\text{D}} = -5.0\text{A}$	-	15		S
Diode Forward Voltage (Note 2)	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_{\text{S}} = -1.0\text{A}$	-	-	-1.2	V
Diode Forward Current (Note 1)	I_{S}		-	-	-3.2	A
Dynamic						
Total Gate Charge	Q_g	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = -10\text{V}, I_{\text{D}} = -1\text{A}$	-	30	-	nC
Gate-Source Charge	Q_{gs}		-	4	-	
Gate-Drain Charge	Q_{gd}		-	3	-	
Input Capacitance	C_{iss}	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$	-	1270	-	pF
Output Capacitance	C_{oss}		-	190	-	
Reverse Transfer Capacitance	C_{rss}		-	134	-	
Switching						
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}} = -15\text{V}, R_{\text{L}} = 15\Omega, I_{\text{D}} = -1\text{A}, V_{\text{GS}} = -4.5\text{V}, R_{\text{GEN}} = 10\Omega$	-	8	-	nS
Rise Time	t_r		-	3.2	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	32	-	
Fall-Time	t_f		-	11	-	

- Note:**
1. Mounted on FR4 board, $t \leq 5\text{sec}$.
 2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
 3. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ Typical Electrical and Thermal Characteristics

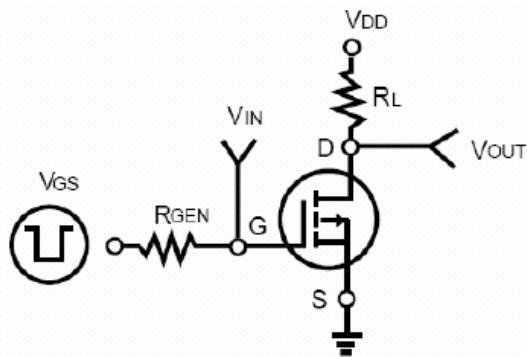


Figure 1: Switching Test Circuit

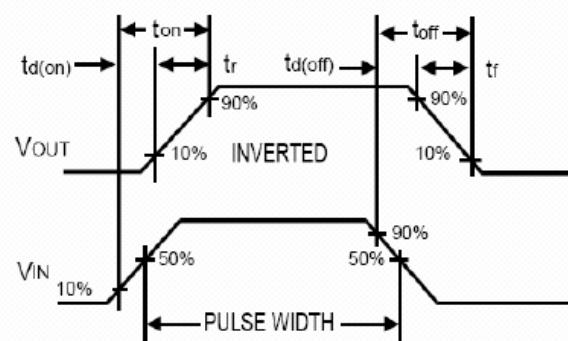


Figure 2: Switching Waveforms

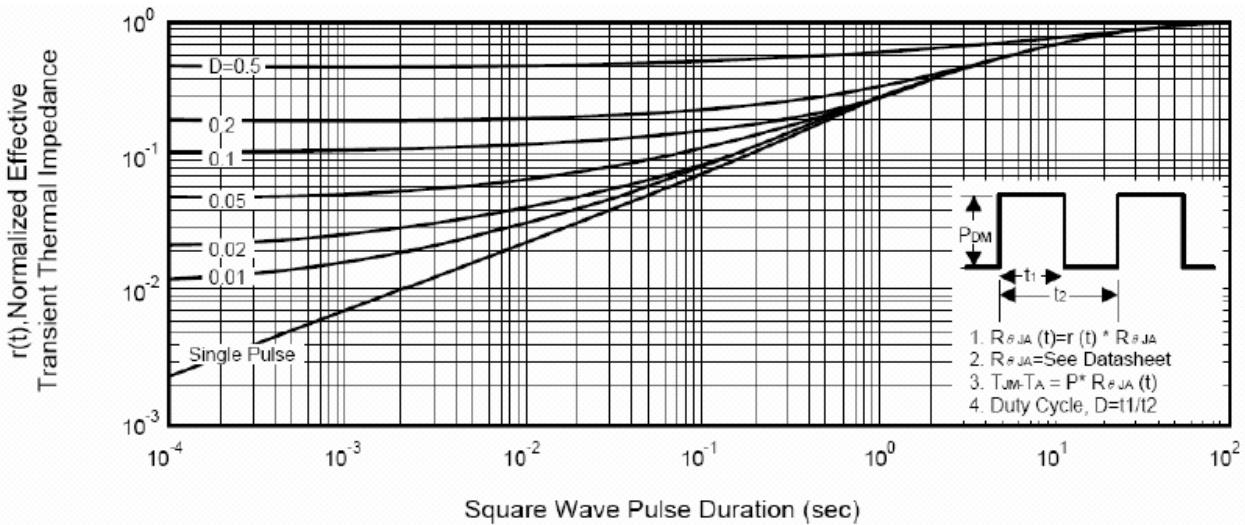
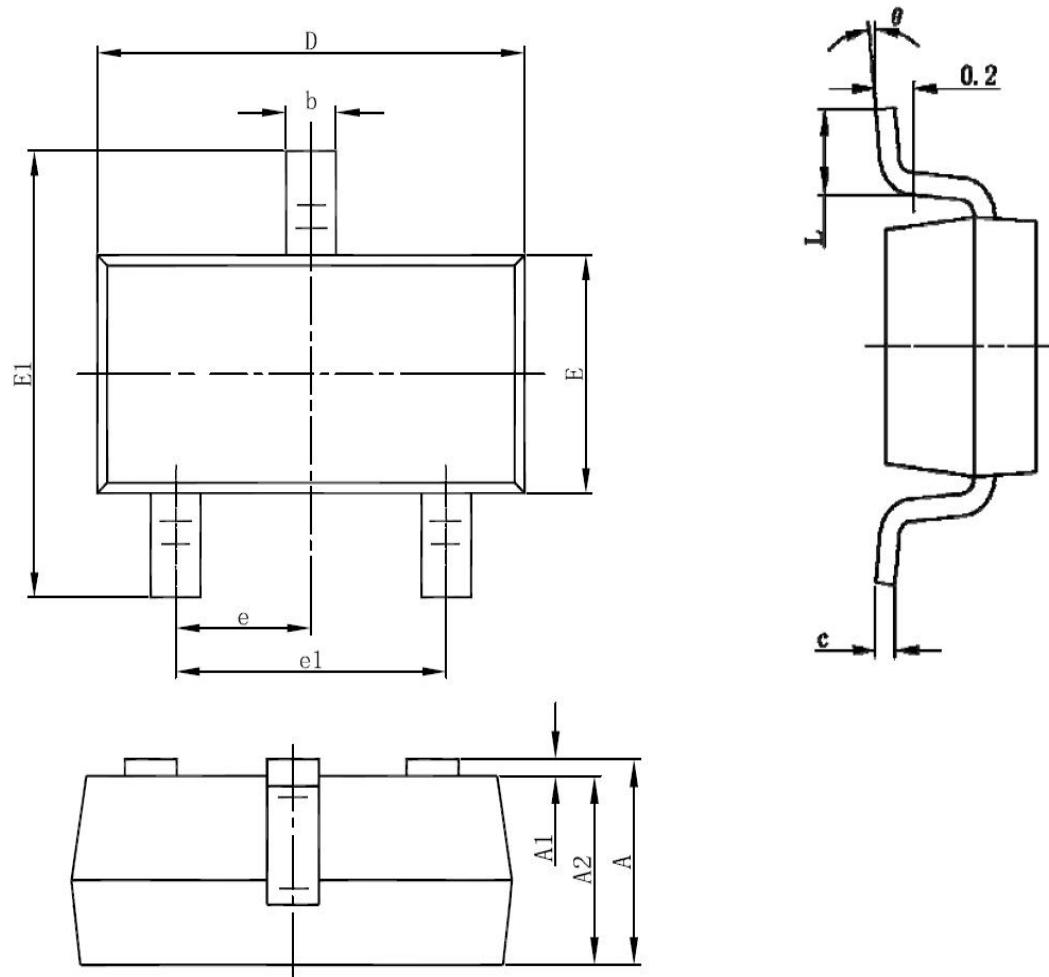


Figure 3: Normalized Maximum Transient Thermal Impedance

■ Package Dimensions : SOT-23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°