

# Specification for Approval

Date: 2021/12/21

**Customer:** 

立创

	TAI-TECH P/N:	WCM3216F2SF-102T03			
	CUSTOMER P/N:				
	DESCRIPTION:				
	QUANTITY:				
REMARK:					
	Customer Ap	proval Feedback			
		技 股 份 有 限 公司 ed Electronics Co Ltd			

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APPROVED	CHECKED	DRAWN
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## Wire Wound Type Common Mode Filter

WCM3216F2SF-102T03

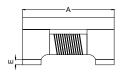
## 1. Features

- 1. High common mode impedance at high frequency effects excellent noise suppression performance.
- 2. WCM3216F2SF series realizes small size and low profile. 3.2x1.6x2.0 mm.
- 3. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

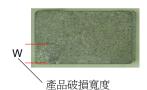




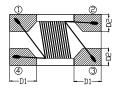
## 2. Dimension







當破損面積<0.3mm²,產品列入允收品範圍



Series	A(mm)	B(mm)	C(mm)	D1(mm)	D2(mm)	E(mm)
3216F2SF	3.2±0.2	1.6±0.2	2.0±0.2	0.5±0.1	0.5±0.1	0.15±0.1

Units: mm

## 3. Part Numbering



A: Series B: Dimension

C: Material Ferrite Core
D: Number of Lines 2=2 lines

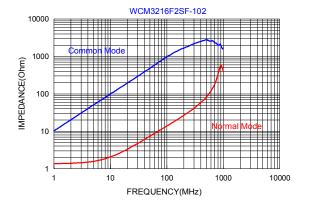
E: Type S=Shielded , N=Unshielded

F: Lead free type

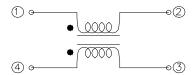
G: Impedance  $102=1000\,\Omega$  H: Packaging T=Taping and Reel I: Rated Current 03=300mA

## 4. Specification

TAI-TECH Part Number	Common mode Impedance $(\Omega)$	Test Frequency (MHz)	DC Resistance $(\Omega)$ max.	Rated Current (mA)max.	Rated Volt. (Vdc)max.	Withstand Volt. (Vdc) Max.	IR $(\Omega)$ min.
WCM3216F2SF-102T03	1000±25%	100	1.00	300	50	125	10M

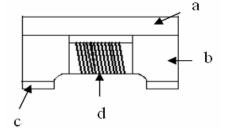


## 5. Schematic Diagram



## 6. Materials

No.	Description	Specification
a.	Туре	Shielded
b.	Core	Ferrite Core
С	Termination	Tin Pb Free
d	Wire	Enameled Copper Wire



## 7. Reliability and Test Condition

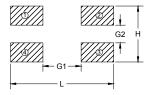
Item		Pe	erformance		Test Condition
Electrical Characteris	stics Test				
Z(common mode)					Agilent-4291A+ Agilent -16197A
DCR	Refer to	standard electrical ch	aracteristics lis	t.	Agilent-4338B
I.R.				Agilent4339	
Operating Temperature	-40℃~+8	85℃			
Storage Temperature	-40°C∼+	85℃(For products in ເ	unopened tape	package, less than 40℃)	
Temperature Rise Test		urrent < 1A $\Delta$ T 20°C urrent $\geq$ 1A $\Delta$ T 40°C		Applied the allowed DC current.     Temperature measured by digital surface thermometer	
Mechanical Performanc	e Test				
Solderability Test ANSI /J-STD-002	More tha	ın 95% of terminal ele	ctrode should l	Preheating Dipping Natural cooling  235°C  150°C  60  second  4±1  second  After fluxing,component shall be dipped in a melted solder bath at 235±5°C for 4±1 seconds.	
Solder Heat Resistance MIL-STD-202 Method210	damage.	onents should have no lance:within ±30% of		Preheating Dipping Natural cooling  260°C  150°C  60	
			Ī	1	The device should be reflow soldered(255±5℃ for 10sec.)to a tinned copper substrate.A dynometer
		Series No.	F(Kg)		force gauge should be applied the side of the component. The device must with-ST-F Kg without ailure of the termination attached to component.
Component Adhesion		WCM3216F2S WCM2012F2S	0.8(min.) 0.5(min.)		·
(Push test)		WCM3216F2N	0.8(min.)		Core Glass Epoxy Substrate with Copper Clad
		WCM2012F2N	0.5(min.)	1	Gic sit

Item	Performance					Test Condition		
		Series No.	F(Kg)		bend ,	t 10cm wire into the ren the ends of even wire ler		
						wind together. 2.Terminal shall not be remarkably damaged.		
Component Adhesion						X		
(Pull test)		WCM3216F2N	0.8(min.)	1				
		WCM2012F2N	0.5(min.)					
Reliability Test	1				ls	2 14000		
High Temperature Life Test MIL-STD-202 METHOD 108					Tempe Durati Measu	Current 100% erature:85±2°C. on:500±8hrs. ured at room temperature a to 3hrs. erature:-40±2°C	ofter placing for 2	
Low Temperature Life Test JESD22-A119					Time:	500±8hr. very: 4 to 24hrs of recovery standard condition aft from test chamber.		
					Ste	p Temperature(°C)	Times(min.)	
						-55±2	30±3	
	1 Anna	aranaa Na damaa		2	Room Temp.	5		
Thermal shock (Unload Test)	Appearance:No damage.     Impedance:within ±30% of initial value.     No disconnection or short circuit.					85±2	30±3	
MIL-STD-202 METHOD 107	ivo dis	sconnection of short cir	cuit.		4	Room Temp.	5	
					Condition for 1 cycle Step1:- 55±2°C 30±3 min.			
						:- 55±2°C 30±3 min. :Room temperature 5 min.		
						: 85±2℃ 30±3 min.		
						: Room temperature 5 min. er of cycles:100		
Humidity Resistance Test MIL-STD-202 METHOD 103					Tempe Humic Rated Time:	erature:40±2°C lity:90~ 95% Current 100% 500±8hr. very:4 to 24hrs of recovery standard condition aft from test chamber.		
						ency: 10-55-10Hz for 15 m	in.	
Random Vibration Test	Appea	arance: Cracking, shipp	ing and any	other defects harmful to the		tude: 1.52mm ions and times:		
MIL-STD-202		cteristics should not be	,			I directions for 15 min.		
Method 204	Imped	lance: within±30%			This c	cycle shall be performed 12	2 times in each of	
						mutually perpendicular dire I 9hours).	ections	

## 8. Soldering and Mounting

#### 8-1. Recommended PC Board Pattern

	WCM2012F2S/F2N	WCM3216F2S/F2N
L	2.60	3.70
Н	1.25	1.60
G1	1.10	1.90
G2	0.45	0.40



PC board should be designed so that products can prevent damage from mechanical stress when warping the board. Products shall be positioned in the sideway direction to against the mechanical stress to prevent failure.

### 87-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### 8-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

#### 8-2.2 Soldering Iron(Figure 2):

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150°C Never contact the ceramic with the iron tip Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355°C tip temperature (max) 1.0mm tip diameter (max)
- Limit soldering time to 4~5 sec.

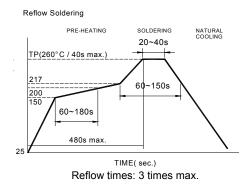
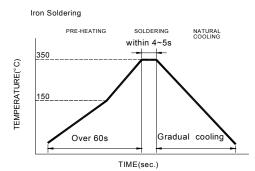


Fig.1

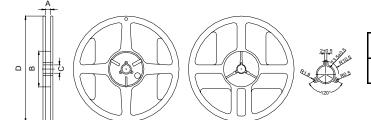


Iron Soldering times: 1 times max.

Fig.2

## 9. Packaging Information

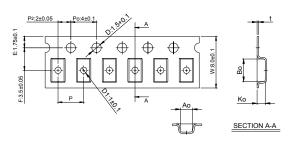
#### 9-1. Reel Dimension



7"x12mm

Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2

#### 9-2. Tape Dimension / 8mm

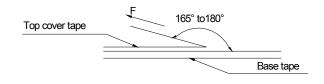


Series	size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
WCM2012F2S	201212	2.35±0.1	1.50±0.1	1.45±0.1	4.0±0.1	0.22±0.05
WCM3216F2S	321620	3.50±0.1	1.88±0.1	2.10±0.1	4.0±0.1	0.22±0.05
WCM2012F2N	201209	2.50±0.1	1.60±0.1	1.25±0.1	4.0±0.1	0.22±0.05
WCM3216F2N	321615	3.50±0.1	1.88±0.1	1.80±0.1	4.0±0.1	0.22±0.05

### 9-3. Packaging Quantity

Chip size	Chip size Chip/Reel I		Middle Box	Carton
WCM2012F2S/F2N	2000/3000	10000/15000	50000/75000	100000/150000
WCM3216F2S/F2N	2000	10000	50000	100000

#### 9-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

#### **Application Notice**

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.