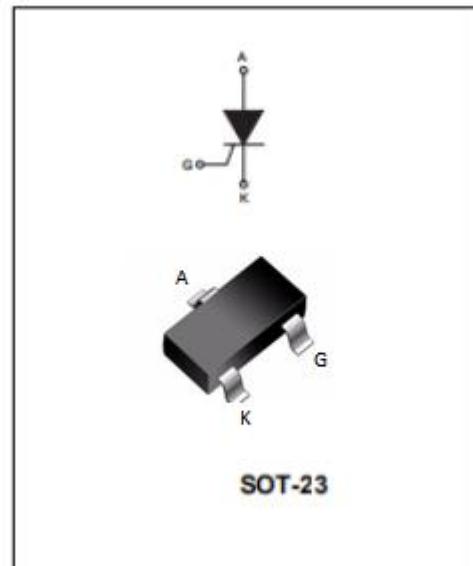


**K08 Series 1A SCRs****DESCRIPTION:**

Thanks to highly sensitive triggering levels, the K08 SCR series is suitable for all applications where the available gate current is limited, such as ground fault circuit interruptors, overvoltage crowbar protection in low power supplies, capacitive ignition circuits, ...

**MAIN FEATURES**

Symbol	Value	Unit
$I_{T(RMS)}$	1.0	A
$V_{DRM}$ $V_{RRM}$	600	V
$I_{GT}$	200	$\mu$ A

**ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Value	Unit
Storage junction temperature range	$T_{stg}$	-40 ~150	°C
Operating junction temperature range	$T_j$	-40~125	°C
Repetitive peak off-state voltage ( $T = 25^\circ C$ )	$V_{DRM}$	600	V
Repetitive peak reverse voltage ( $T = 25^\circ C$ )	$V_{RRM}$	600	V
RMS on-state current	$I_{T(RMS)}$	1.0	A
Non repetitive surge peak on-state current (180° conduction angle, $F=50Hz$ )	$I_{TSM}$	12	A
Average on-state current (180° conduction angle)	$I_{T(AV)}$	0.6	A
$I^2t$ value for fusing ( $t_p=10ms$ )	$I^2t$	0.72	$A^2S$
Critical rate of rise of on-state current ( $I = 2 \times I_{GT}$ , $t_r \leq 100$ ns)	$dI/dt$	50	$A/\mu s$
Peak gate current	$I_{GM}$	1.0	A
Average gate power dissipation	$P_{G(AV)}$	0.1	W

**ELECTRICAL CHARACTERISTICS (T=25 °C unless oth)**

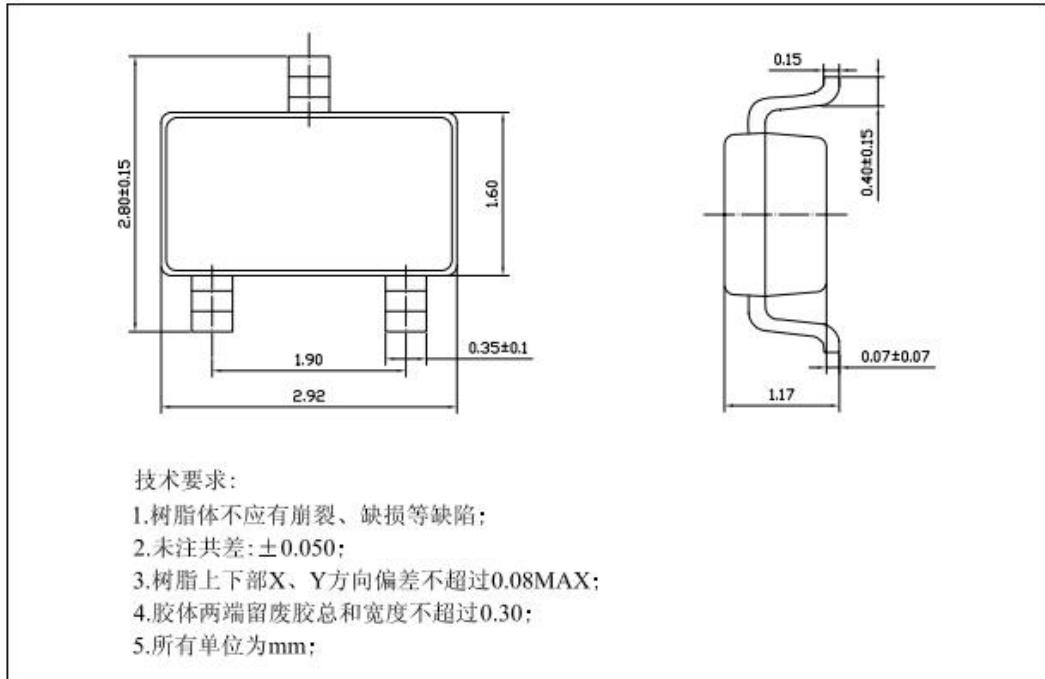
<b>Symbol</b>	<b>Test Condition</b>		<b>Value</b>	<b>Unit</b>
I <sub>GT</sub>	V =12V R =140Ω	MAX.	200	μA
V <sub>GT</sub>		MAX.	1.0	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C R <sub>GK</sub> =1KΩ	MIN.	0.2	V
I <sub>L</sub>	I <sub>G</sub> =1.2I <sub>GT</sub>	MAX.	6	mA
I <sub>H</sub>	I <sub>T</sub> =50mA	MAX.	5	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open R <sub>GK</sub> =1KΩ T <sub>j</sub> =110°C	MIN.	200	V/μs

**STATIC CHARACTERISTICS**

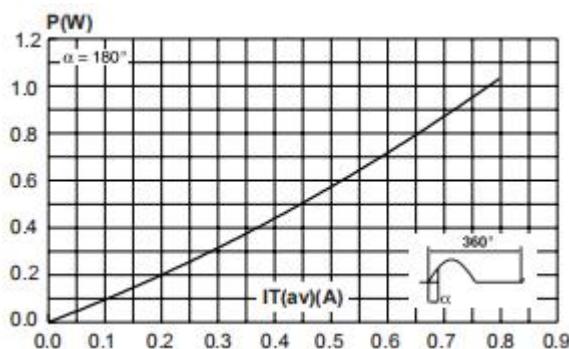
<b>Symbol</b>	<b>Parameter</b>		<b>Value(MAX.)</b>	<b>Unit</b>
V <sub>TM</sub>	I <sub>TM</sub> =2.0A tp=380μs	T <sub>j</sub> =25°C	1.75	V
I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25°C	5	μA
I <sub>RRM</sub>		T <sub>j</sub> =125°C	0.5	mA

**Thermal Resistances**

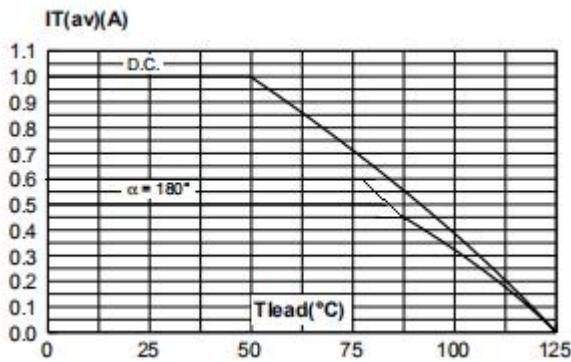
<b>Symbol</b>	<b>Parameter</b>	<b>Value(MAX.)</b>	<b>Unit</b>
R <sub>th(j-a)</sub>	junction to ambient	150	°C/W
R <sub>th(j-t)</sub>	Junction to tab (DC)	40	

**SOT-23-3L Package Mechanical Data**

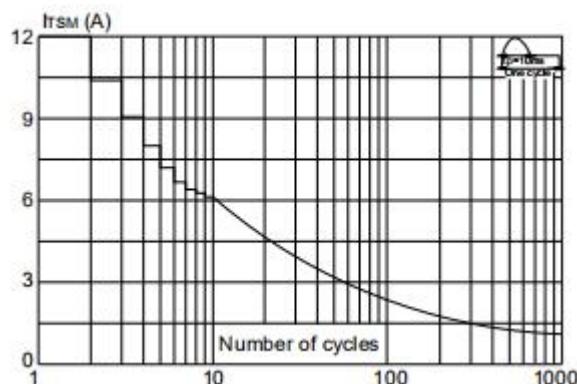
**FIG.1 Maximum power dissipation versus Average on-state current**



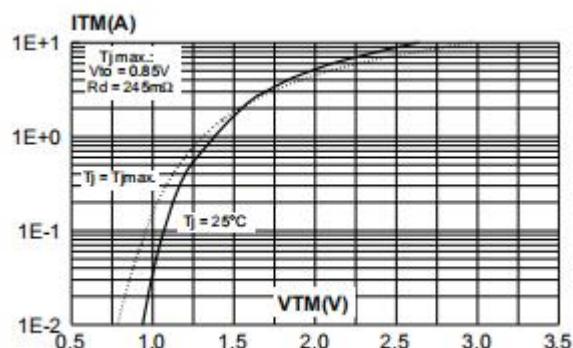
**FIG.2: Average on-state current versus case temperature**



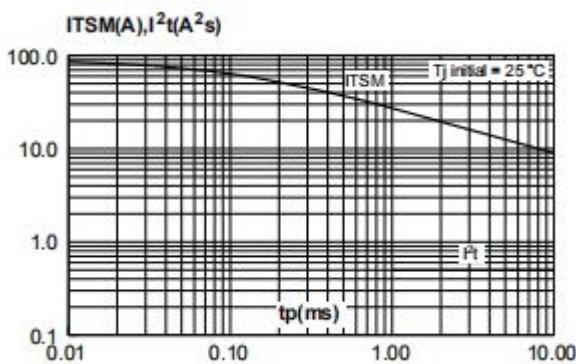
**FIG.3: Surge peak on-state current versus number of cycles**



**FIG.4: On-state characteristics (maximum values)**



**FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponding value of I<sup>2</sup> t (di/dt < 50A/μs)**



**FIG.6: Relative variations of gate trigger current holding current and latching current versus junction temperature**

