

## -100V P-Channel Enhancement Mode MOSFET

### Description

The AP3P10S uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

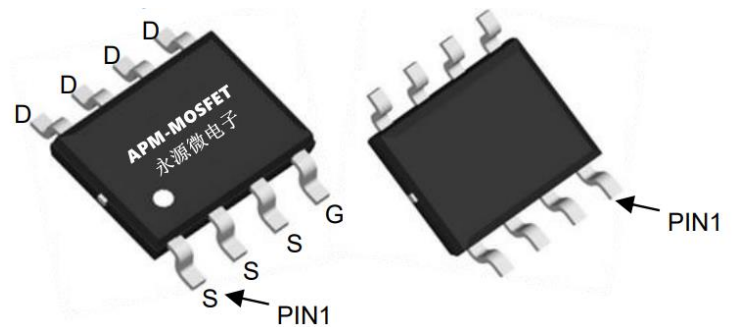
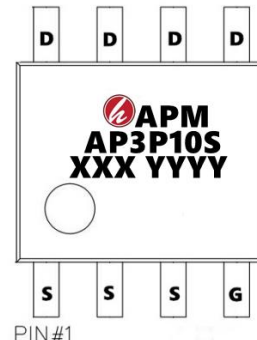
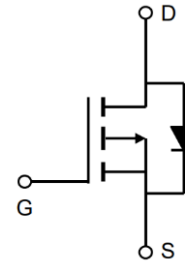
### General Features

$V_{DS} = -100V$   $I_D = -3A$

$R_{DS(ON)} < 600m\Omega$  @  $V_{GS}=10V$  (Type: 450m $\Omega$ )

### Application

Battery protection  
Load switch  
Uninterruptible power supply



### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP3P10S	SOP-8	AP3P10S XXX YYYY	3000

### Absolute Maximum Ratings ( $T_C=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_A=25^{\circ}C$	Continuous Drain Current, $V_{GS}$ @ -10V <sup>1</sup>	-3	A
$I_D@T_A=70^{\circ}C$	Continuous Drain Current, $V_{GS}$ @ -10V <sup>1</sup>	-1.7	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-9.8	A
$P_D@T_A=25^{\circ}C$	Total Power Dissipation <sup>3</sup>	1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^{\circ}C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^{\circ}C$
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup>	85	$^{\circ}C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	80	$^{\circ}C/W$

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### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

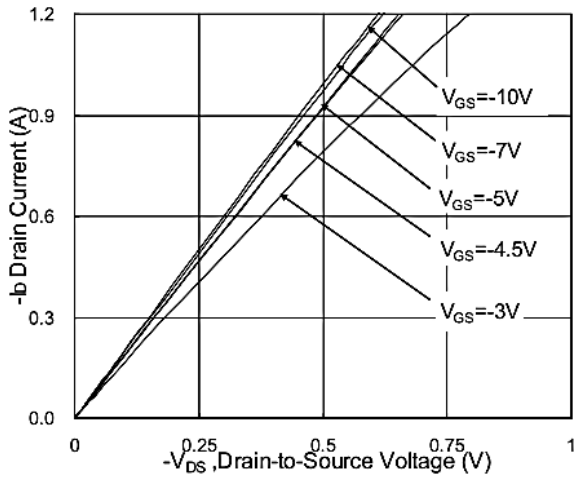
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-100	-111	---	V
ΔBVDSS/ΔT <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	---	-0.0624	---	V/°C
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-0.8A	---	450	600	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.4A	---	560	700	mΩ
VGS(th)	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.5	-2.5	V
ΔVGS(th)	VGS(th) Temperature Coefficient		---	4.5	---	mV/°C
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =-80V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	10	uA
		V <sub>DS</sub> =-80V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	100	uA
IGSS	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-0.8A	---	3	---	S
Rg	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	16	32	Ω
Qg	Total Gate Charge (-4.5V)	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.5A	---	4.5	---	nC
Qgs	Gate-Source Charge		---	1.14	---	nC
Qgd	Gate-Drain Charge		---	1.5	---	nC
Td(on)	Turn-On Delay Time	V <sub>DD</sub> =-50V, V <sub>GS</sub> =-10V, R <sub>G</sub> =3.3Ω, I <sub>D</sub> =-0.5A	---	13.6	---	ns
Tr	Rise Time		---	6.8	---	ns
Td(off)	Turn-Off Delay Time		---	34	---	ns
Tf	Fall Time		---	3	---	ns
Ciss	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	---	553	---	pF
Coss	Output Capacitance		---	29	---	pF
Crss	Reverse Transfer Capacitance		---	20	---	pF
IS	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-0.9	A
ISM	Pulsed Source Current <sup>2,4</sup>		---	---	-1.8	A
VSD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1.2	V

#### Note :

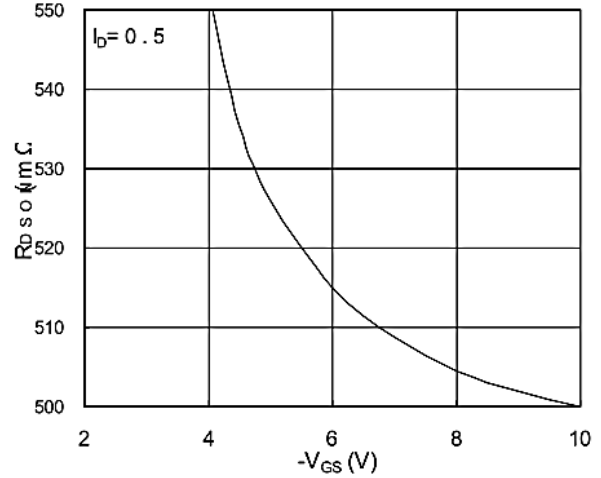
- 1、The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2、The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3、The power dissipation is limited by 150°C junction temperature
- 4、The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

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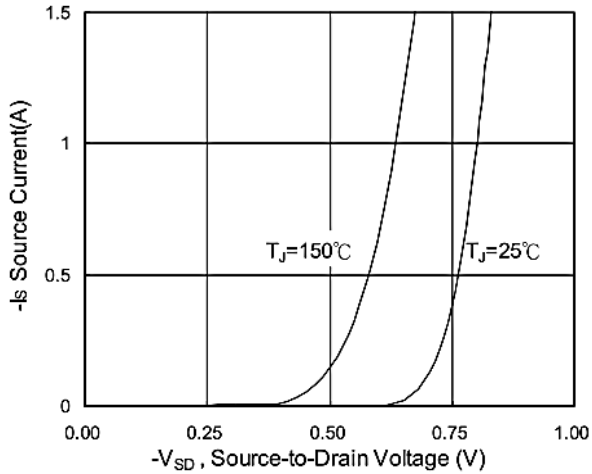
### Typical Characteristics



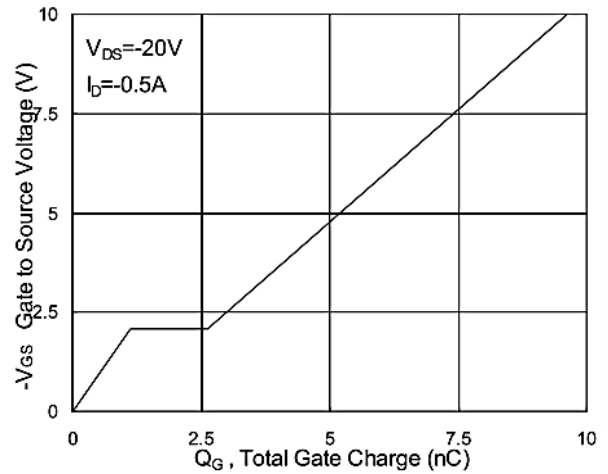
**Fig.1 Typical Output Characteristics**



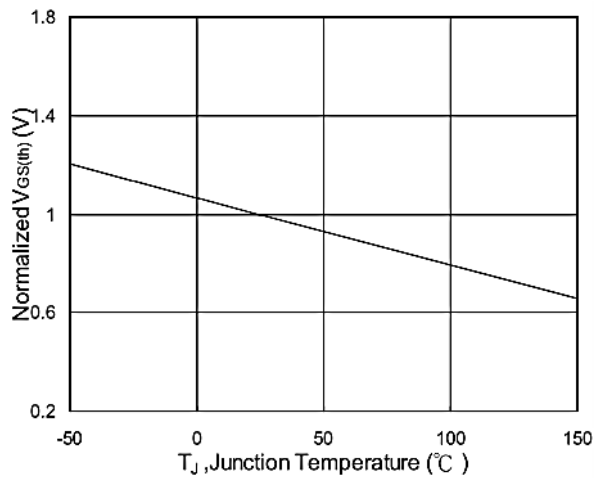
**Fig.2 On-Resistance vs. Gate-Source**



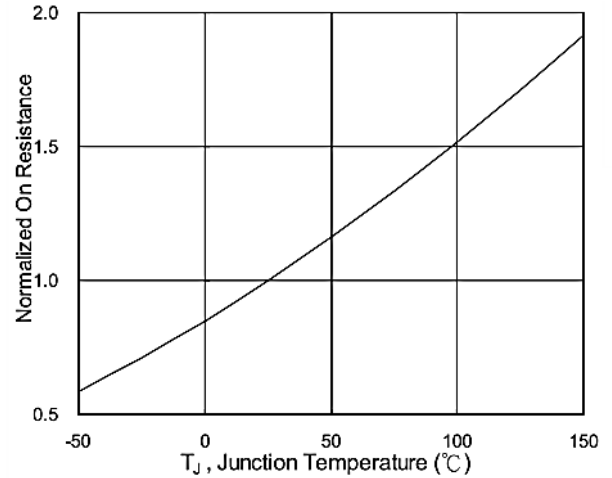
**Fig.3 Forward Characteristics Of Reverse**



**Fig.4 Gate-Charge Characteristics**

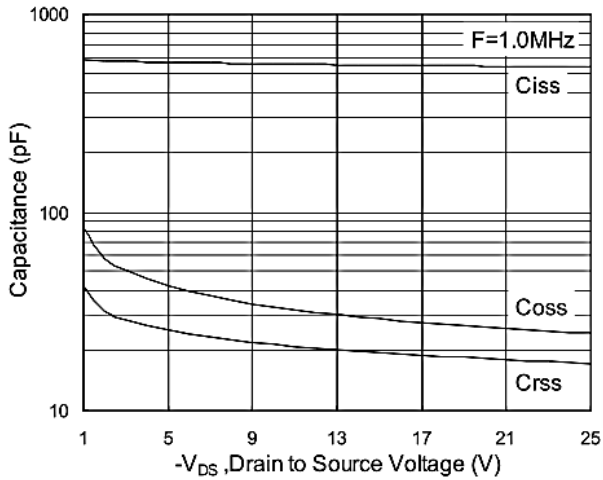


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**

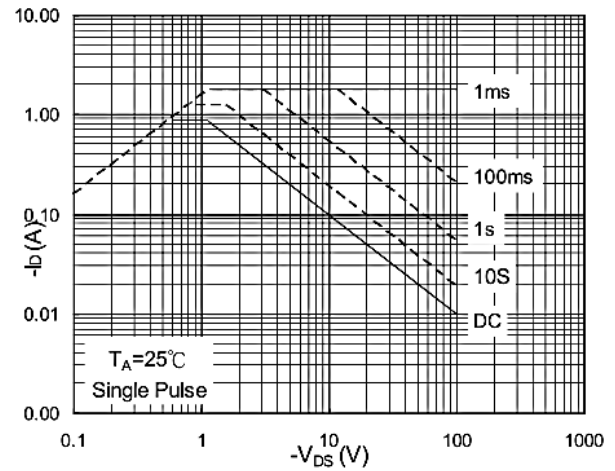


**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**

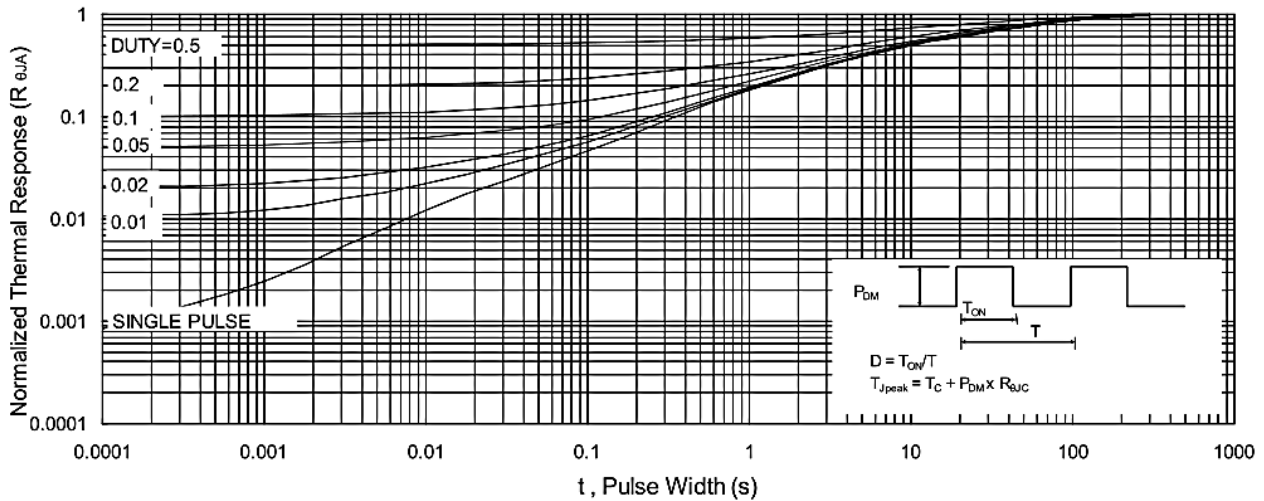
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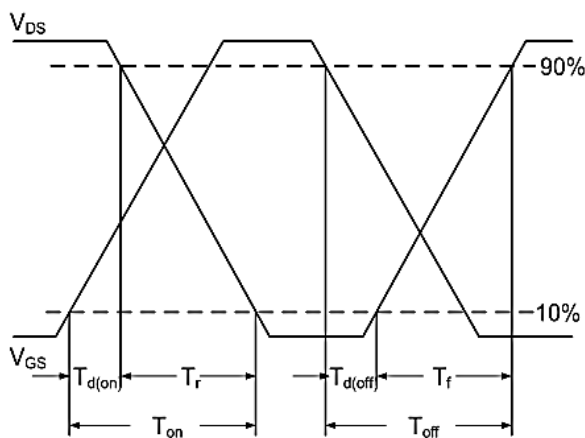
**Fig.7 Capacitance**



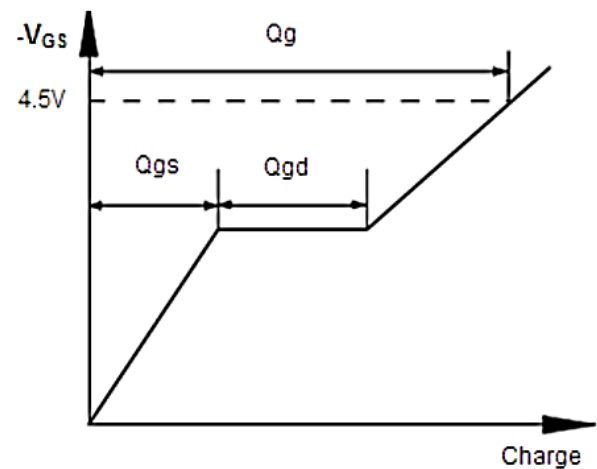
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



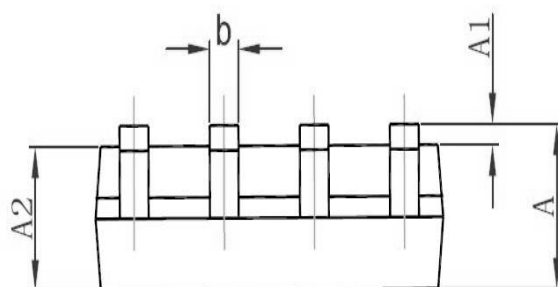
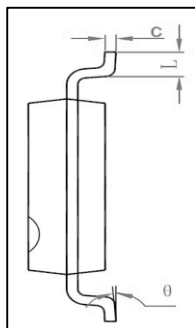
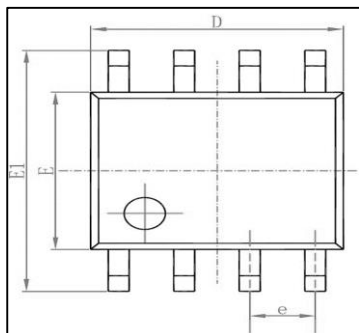
**Fig.10 Switching Time Waveform**



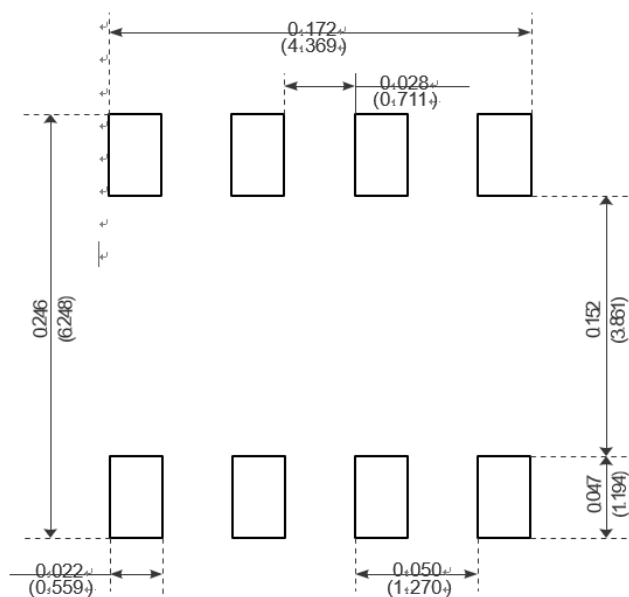
**Fig.11 Gate Charge Waveform**

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### Package Mechanical Data-SOP-8L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads

**-100V P-Channel Enhancement Mode MOSFET****Attention**

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Edition	Date	Change
Rve1.0	2020/12/1	Initial release

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