

#### **Description**

The AP90N02NF uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 20V I<sub>D</sub> =90 A

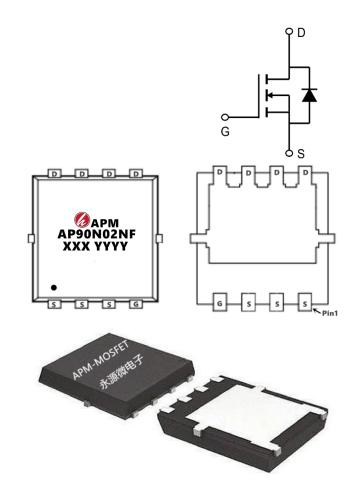
 $R_{DS(ON)} < 2m\Omega$  @  $V_{GS}=10V$ 

#### **Application**

Battery protection

Load switch

Uninterruptible power supply



**Package Marking and Ordering Information** 

rackage marking and Ordering information					
Product ID	Pack	Marking	Qty(PCS)		
AP90N02NF	PDFN5*6-8L	AP90N02NF XXX YYYY	5000		

### Absolute Maximum Ratings (T<sub>c</sub>=25<sup>°</sup>Cunless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	20	V
VGS	Gate-Source Voltage	±12	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current <sup>1</sup>	90	А
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current <sup>1</sup>	48	Α
IDM	Pulsed Drain Current <sup>2</sup>	270	Α
EAS	Single Pulse Avalanche Energy <sup>3</sup>	80	mJ
IAS	Avalanche Current	40	Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	83	W
TSTG	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$
R <sub>θ</sub> JA	Thermal Resistance Junction-ambient ¹(t≦10S)	20	°C/W
R <sub>θ</sub> JA	Thermal Resistance Junction-ambient ¹(Steady State)	55	°C/W
R₀JC	Thermal Resistance Junction-case <sup>1</sup> 1.5		°C/W



# Electrical Characteristics (T<sub>C</sub>=25°Cunless otherwise noted)

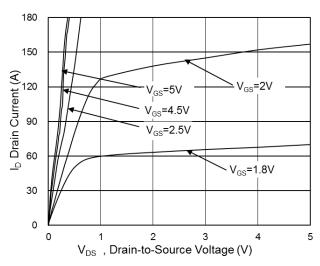
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	20	23		V
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	0.5	0.68	1.0	V
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		1.6	2.0	mΩ
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A		1.9	2.5	mΩ
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =2.5V , I <sub>D</sub> =20A		2.8	3.8	mΩ
IDOO	Drain-Source Leakage Current	$V_{DS}$ =16V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			1	
IDSS		V <sub>DS</sub> =16V , V <sub>GS</sub> =0V , T <sub>J</sub> =125℃			5	uA
IGSS	Gate-Source Leakage Current	$V_{GS}$ =±10V , $V_{DS}$ =0V			±10	uA
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.2		Ω
Qg	Total Gate Charge (10V)			77		
Qgs	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , I <sub>D</sub> =20A		8.7		nC
Qgd	Gate-Drain Charge			14		
Td(on)	Turn-On Delay Time			10.2		
Tr	Rise Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V , R <sub>G</sub> =3□,		11.7		
Td(off)	Turn-Off Delay Time	I <sub>D</sub> =20A		56.4		ns
T <sub>f</sub>	Fall Time			16.2		
Ciss	Input Capacitance	V <sub>DS</sub> =10V , V <sub>GS</sub> =0V , f=1MHz		4307		
Coss	Output Capacitance			501		pF
Crss	Reverse Transfer Capacitance			321		
IS	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			50	Α
VSD	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.2	V
trr	Reverse Recovery Time	IF=20A , di/dt=100A/μs ,		22		nS
Qrr	Reverse Recovery Charge	T <sub>J</sub> =25℃		72		nC

#### Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- $2_{\times}$  The data tested by pulsed , pulse width  $\leqq$  300us , duty cycle  $\leqq$  2%
- $3\$  The EAS data shows Max. rating . The test condition is VDD=16V,VGS=10V,L=0.1mH,IAS=39A
- 4. The power dissipation is limited by 175  $^{\circ}$ C junction temperature
- 5. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.



## **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

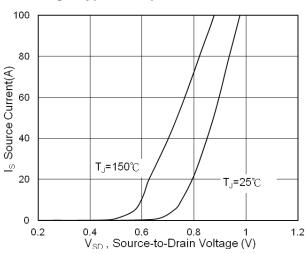


Fig.3 Forward Characteristics of Reverse

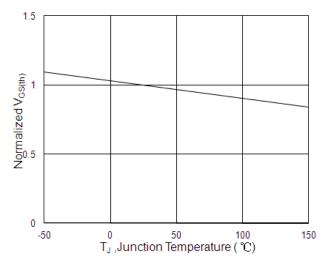


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

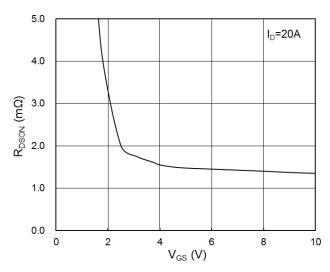


Fig.2 On-Resistance vs. Gate-Source Voltage

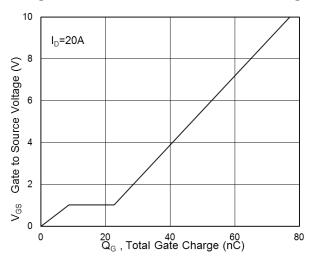


Fig.4 Gate-Charge Characteristics

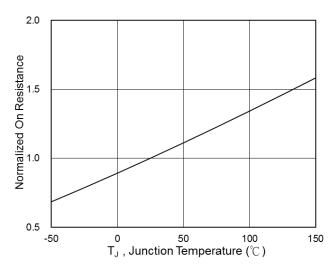
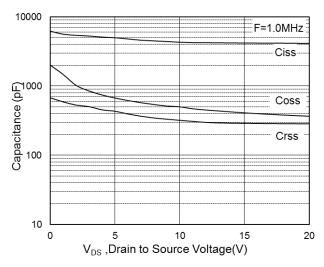


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>







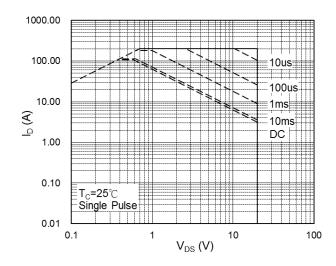


Fig.7 Capacitance

Fig.8 Safe Operating Area

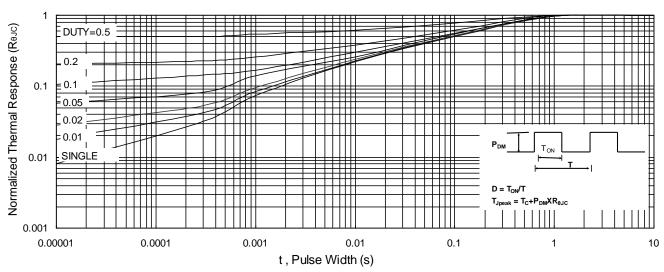


Fig.9 Normalized Maximum Transient Thermal Impedance

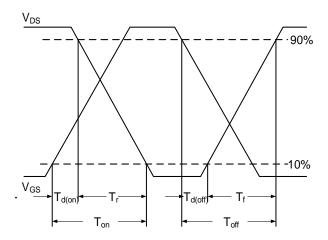


Fig.10 Switching Time Waveform

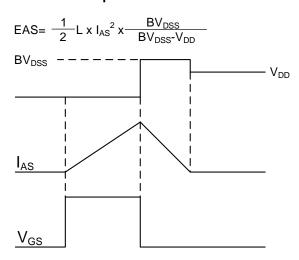
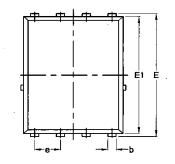


Fig.11 Unclamped Inductive Switching Waveform

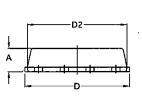


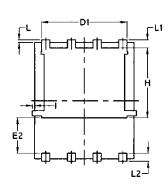


# Package Mechanical Data-DFN5\*6-8L-JQ Single









		Com	mon		
Symbol	mm		Inch		
	Mim	Max	Min	Max	
Α	1.03	1.17	0.0406	0.0461	
b	0.34	0.48	0.0134	0.0189	
С	0.824	0.0970	0.0324	0.082	
D	4.80	5.40	0.1890	0.2126	
D1	4.11	4.31	0.1618	0.1697	
D2	4.80	5.00	0.1890	0.1969	
E	5.95	6.15	0.2343	0.2421	
E1	5.65	5.85	0.2224	0.2303	
E2	1.60	/	0.0630	/	
е	1.27 BSC		0.05	BSC	
L	0.05	0.25	0.0020	0.0098	
L1	0.38	0.50	0.0150	0.0197	
L2	0.38	0.50	0.0150	0.0197	
Н	3.30	3.50	0.1299	0.1378	
I	/	0.18	/	0.0070	



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# AP90N02NF

# **20V N-Channel Enhancement Mode MOSFET**

Edition	Date	Change
Rve1.0	2020/3/31	Initial release

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