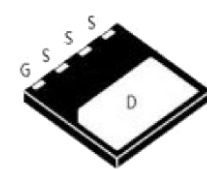
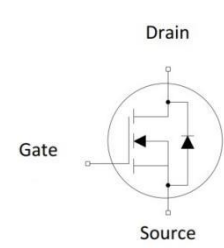



N-channel 650V, 15A, 0.28Ω Super-Junction Power MOSFET

<p>Description Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFET, designed according to the SJ principle. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.</p> <p>Features</p> <ul style="list-style-type: none"> ◆ Very low FOM $R_{DS(on)} \times Q_g$ ◆ 100% UIS tested ◆ RoHS compliant <p>Applications</p> <ul style="list-style-type: none"> ◆ Power factor correction (PFC). ◆ Switched mode power supplies (SMPS). ◆ Uninterrupted power supply (UPS). 	<p>Product Summary</p> <table> <tr> <td>$V_{DS} @ T_{j,25^\circ C}$</td> <td>650V</td> </tr> <tr> <td>$R_{DS(on),max}$</td> <td>0.28Ω</td> </tr> <tr> <td>I_D</td> <td>15A</td> </tr> <tr> <td>$Q_{g,typ}$</td> <td>26nC</td> </tr> </table> <div style="text-align: center;">  <p>DFN8*8</p>   <p>N-Channel MOSFET</p> </div>	$V_{DS} @ T_{j,25^\circ C}$	650V	$R_{DS(on),max}$	0.28Ω	I_D	15A	$Q_{g,typ}$	26nC
$V_{DS} @ T_{j,25^\circ C}$	650V								
$R_{DS(on),max}$	0.28Ω								
I_D	15A								
$Q_{g,typ}$	26nC								

Marking information

Product	Package	Marking	Packing method
RMK65R280SN	DFN8*8	RMK65R280SN	Reel

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	650	V
Continuous drain current ($T_C = 25^\circ C$) ($T_C = 100^\circ C$)	I_D	15	A
		9	A
Pulsed drain current ¹⁾	I_{DM}	45	A
Gate-Source voltage	V_{GSS}	± 30	V
Avalanche energy, single pulse ²⁾	E_{AS}	320	mJ
Avalanche current, repetitive ³⁾	I_{AR}	2.2	A
Power Dissipation DFN8*8 ($T_C = 25^\circ C$) - Derate above $25^\circ C$	P_D	96	W
		0.8	W/ $^\circ C$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ C$
Continuous diode forward current	I_S	35	A
Diode pulse current	$I_{S,pulse}$	45	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
		DFN8*8	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.32	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	45	$^{\circ}C/W$
Soldering temperature, wave soldering only allowed at leads. (1.6mm from case for 10s)	T_{sold}	260	$^{\circ}C$

Electrical Characteristics
 $T_c = 25^{\circ}C$ unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	650	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.5		4.0	V
Drain cut-off current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V,$ $T_j = 25^{\circ}C$ $T_j = 125^{\circ}C$	-	-	1	μA
			-	10		
Gate leakage current, Forward	I_{GSSF}	$V_{GS}=30V, V_{DS}=0V$	-	-	100	nA
Gate leakage current, Reverse	I_{GSSR}	$V_{GS}=-30V, V_{DS}=0V$	-	-	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=5.5A$ $T_j = 25^{\circ}C$ $T_j = 150^{\circ}C$	-			Ω
			-	0.24	0.28	
			-	0.62		
Dynamic characteristics						
Input capacitance	C_{iss}	$V_{DS} = 100V, V_{GS} = 0V,$ $f = 1MHz$	-	1126	-	pF
Output capacitance	C_{oss}		-	41	-	
Reverse transfer capacitance	C_{rss}		-	2.4	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 400V, I_D = 7.5A$ $R_G = 25\Omega, V_{GS}=10V$	-	20	-	ns
Rise time	t_r		-	40	-	
Turn-off delay time	$t_{d(off)}$		-	95	-	
Fall time	t_f		-	43	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	$V_{DD}=520V, I_D=5.5A,$ $V_{GS}=0$ to 10 V	-	3.6	-	nC
Gate to drain charge	Q_{gd}		-	10.5	-	
Gate charge total	Q_g		-	26	-	
Gate plateau voltage	$V_{plateau}$		-	5.5	-	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	$V_{GS}=0V, I_F=7.5A$	-	0.85	-	V
Reverse recovery time	t_{rr}	$V_R=400V, I_F=7.5A,$ $di_F/dt=100A/\mu s$	-	405	-	ns
Reverse recovery charge	Q_{rr}		-	4	-	μC
Peak reverse recovery current	I_{rm}		-	21	-	A

Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- $I_{AS} = 4A, V_{DD} = 50V,$ Starting $T_j = 25^{\circ}C.$

Electrical Characteristics Diagrams

Figure 1. Output Characteristics

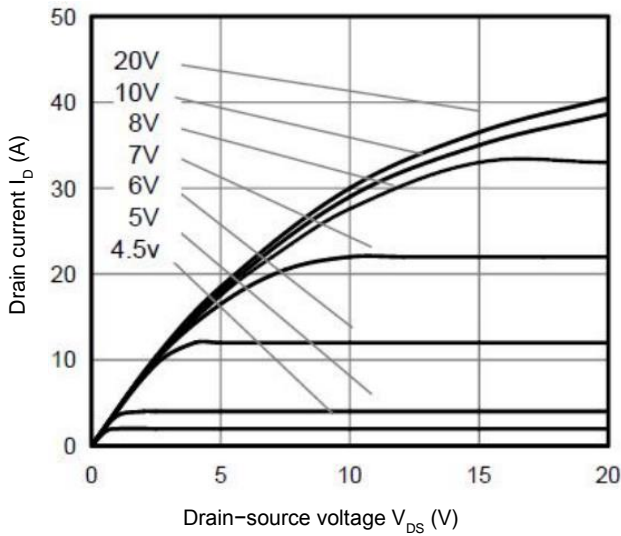


Figure 3. On-Resistance vs. Drain Current

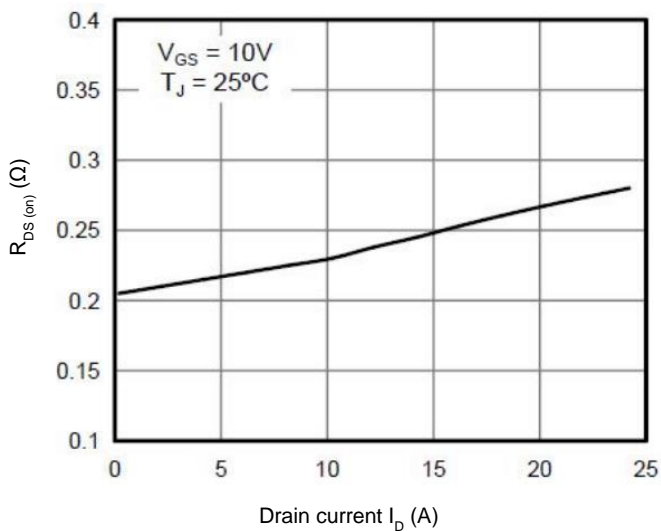


Figure 5. Gate Charge Characteristics

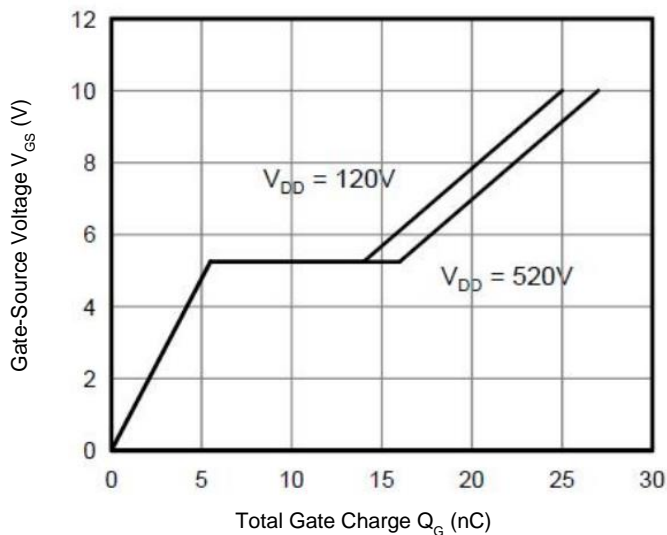


Figure 2. Transfer Characteristics

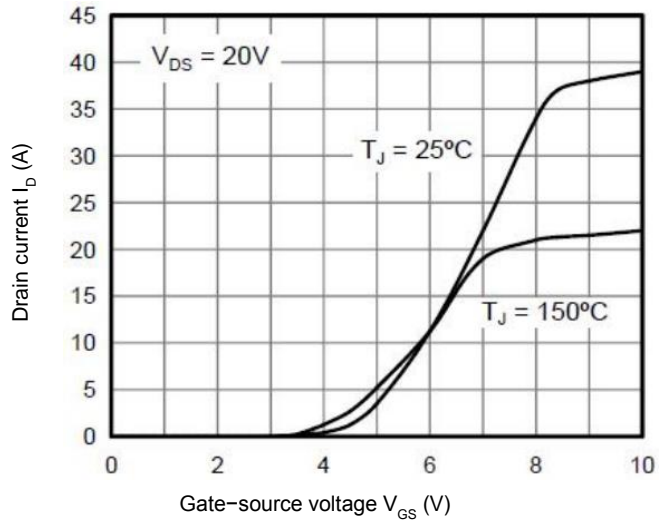


Figure 4. Capacitance Characteristics

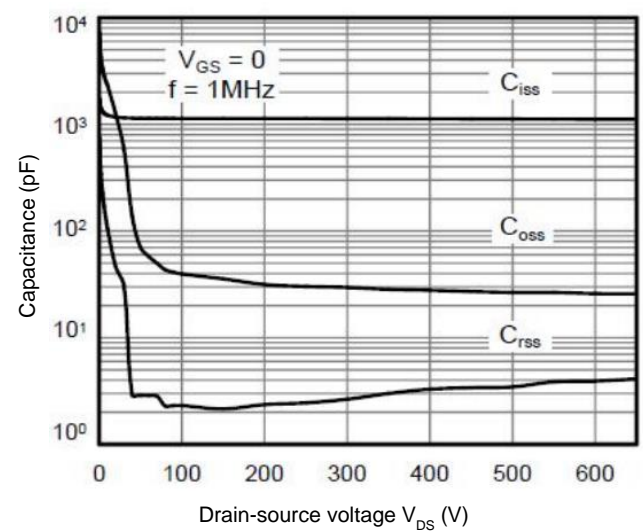


Figure 6. Body Diode Forward Voltage

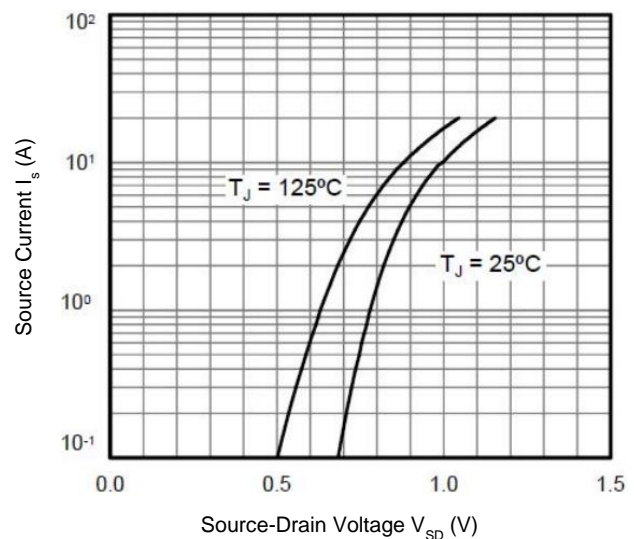


Figure 7. Breakdown Voltage vs. Temperature

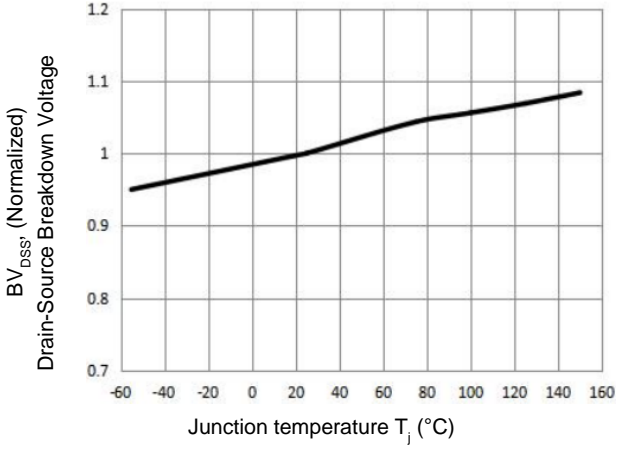


Figure 8. On-Resistance vs. Temperature

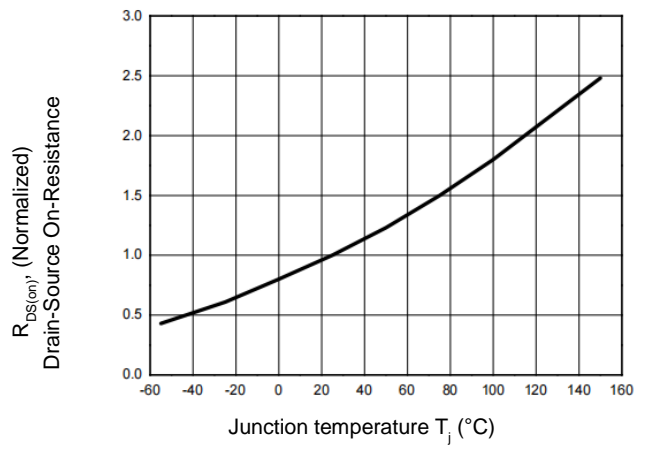
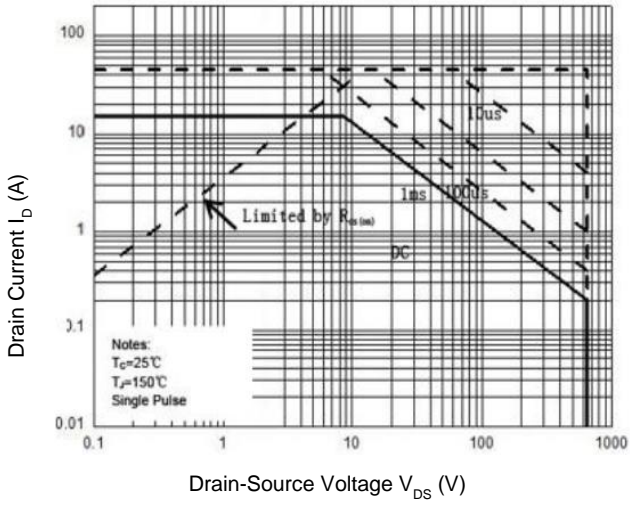
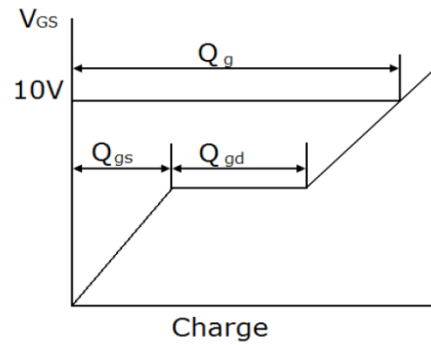
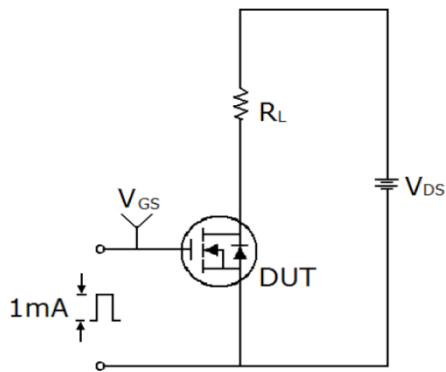


Figure 9. Maximum Safe Operating Area
DFN8*8

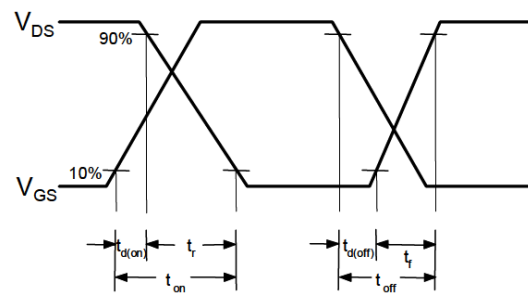
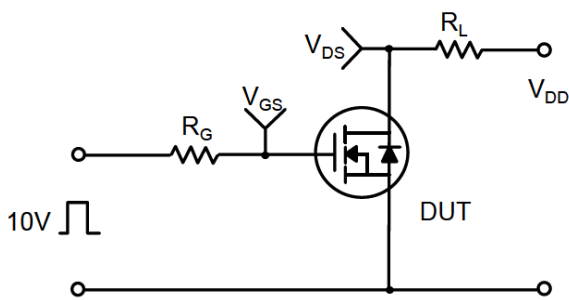


Test Circuits

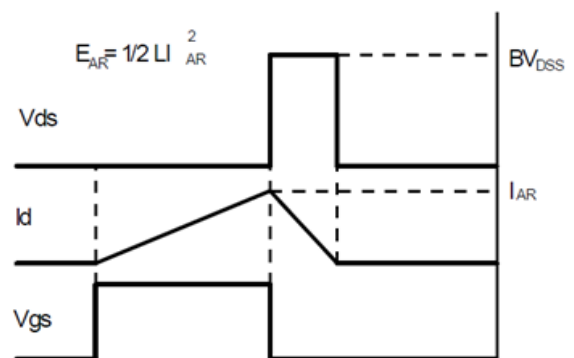
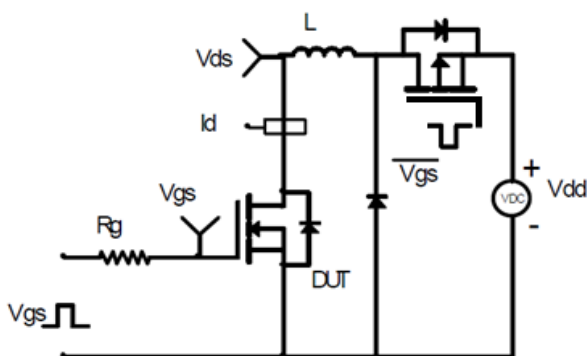
Gate Charge Test Circuit & Waveform



Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform



Mechanical Dimensions for DFN8*8

